

Rd-61 Specification

Version V1.1.0

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Document resume

Version	Date	Develop/revise content	Edition	Approved by
V1.1.0	2025.01.09	First Edition	Pengfei Dong	Ning Guan

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1. Product overviews

Rd-61 is an AiP radar module with low power consumption of 60 GHz FMCW carrier developed by Shenzhen Anxinke Technology Co., LTD. The module integrates 1T3R AiP radar chip and Flash, which can meet the rapid application development of developers after direct power supply to the module. The module contains four low-power management modes: power off, standby, idle, and working. Not only is it low power in standby mode, but also low power when sensing and connecting by a fast burst engine and high-performance RF transceiver. The flexible configuration of these modes allows for the power consumption of the entire module to meet a variety of stringent application requirements. Its configuration and data acquisition are implemented through the UART interface. The module is divided into three subsystems:

- mmWave RF and BB subsystem: This module includes all high-performance radar RF / analog and baseband digital signal circuits. It also includes a linear FM pulse generator and a Tx-to-Rx state machine.
- Processing and application subsystems: including a 32-bit RISC CPU and radar hardware accelerator HWA for different application processing and task management. Our proprietary HWA IP is used with specified radar calculations (e. g. FFT, CFAR, etc.), compression, and moving target perception and location.
- Power supply and clock subsystem: The module is a complex single-chip Soc with multiple functional systems. We use the internal integrated PMU system and clock subsystem for centralized management.

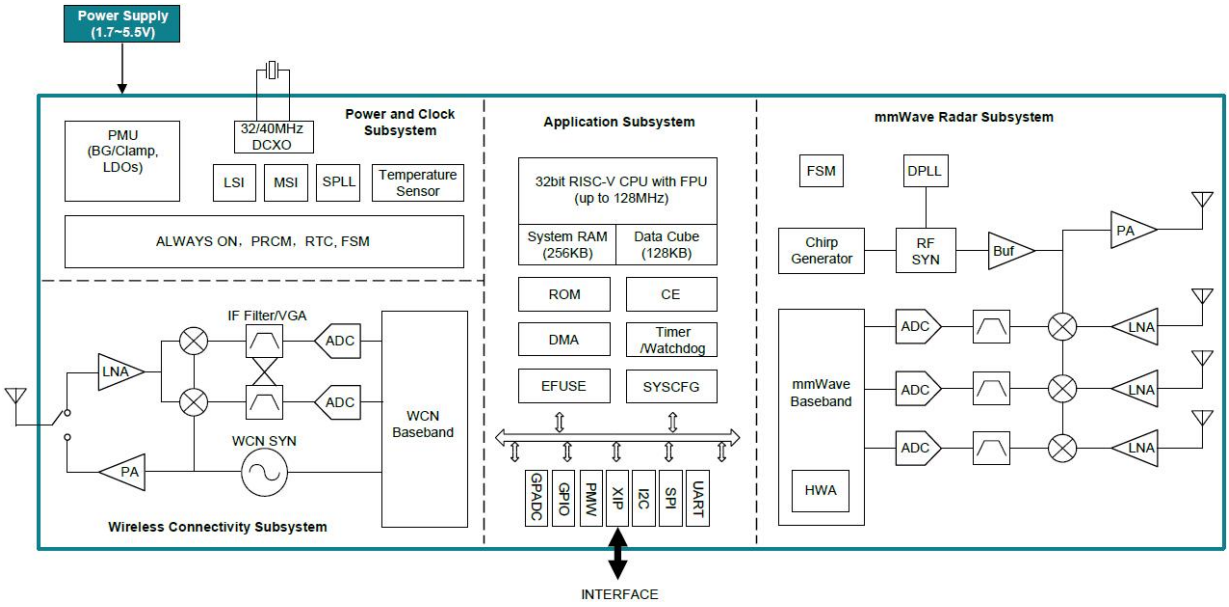


Figure 1 Main Chip Architecture Diagram

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2. Characteristics

- With SMD package, compatible with 1.27mm pin header
- The radar spectrum range is 58G ~64GHz, and the continuous sweep band width is 6GHz
- The radar antenna supports 1T3R, realizing different configuration modes of 1T1R, 1T2R and 1T3R
- Equivalent omnidirectional radiation power (EIRP)17.5dBm
- The radar detection Angle is large, the coverage is horizontal FOV 120° and vertical FOV 120°
- The maximum induction distance of the radar is 8 meters (depending on the size of the detection target RCS)
- The nearest inductive distance of radar is 0.01 m (with reference to human target)
- Ultra-small module size: 18.5*6.5mm
- Typical application scenarios

It can be widely used in the typical scenes of AIOT intelligent perception represented by intelligent home appliances, intelligent security, intelligent health care, intelligent cockpit, etc., to realize the high precision and high sensitivity detection of the movement and micro-motion of human body and objects:

- ✓ Human body movement / presence detection
- ✓ Human body tracking and counting
- ✓ Human breathing and heartbeat detection
- ✓ Gesture recognition
- ✓ Visible doorbell
- ✓ Web camera
- ✓ Air conditioner
- ✓ Refrigerator
- ✓ Television

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3. Main parameters

Table 1: Description of the main parameters

Model	Rd-61
Package	SMD8 compatible DIP-8
Size	18.5*6.5*5.8(±0.2)mm（with pin header） 18.5*6.5*1.6(±0.2)mm（without pin header）
Antenna	On-board antenna
Frequency	58G ~64GHz
Operating temperature	-40℃ ~ 85℃
Storage environment	-40℃ ~ 125℃, < 90%RH
Power supply range	Power supply voltage is 1.7V ~ 5.5V, the typical value is 3.3V, and power supply current ≥500mA
Support interface	UART/IIC
Serial port rate	The UART is compatible with the 16550 industrial standard and supports a port rate of up to 3Mbps. Default: 115200

3.1. Electrostatic requirements

Rd-61 is electrostatic sensitive equipment that requires special precautions during handling.



Figure 2 ESD ESstatic diagram

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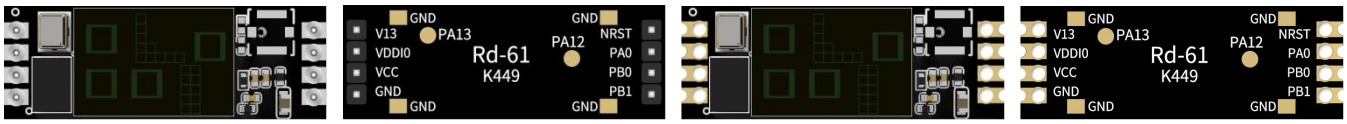
3.2. Electrical character

Table 2 Electrical characteristics table

Parameter		Condition	Minimum value	Typical value	Maximum value	Unit
Supply voltage		VCC	1.7	3.3	5.5	V
		VDDIO	1.7	3.3	3.6	V
		V13	1.2	1.3	2.5	V
I/O	VIL	-	-0.3	-	0.35*VDDIO	V
	VIH	-	0.65*VDDIO	-	1.1*VDDIO	V
	VOL	-	-0.3	-	0.1*VDDIO	V
	VOH	-	0.9*VDDIO	-	1.1*VDDIO	V

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4. Appearance size



Front (with pin header) Back (with pin header) Front (without pin header) Back (without pin header)

Figure 3 Appearance diagram (rendering diagram is for reference only, subject to physical objects)

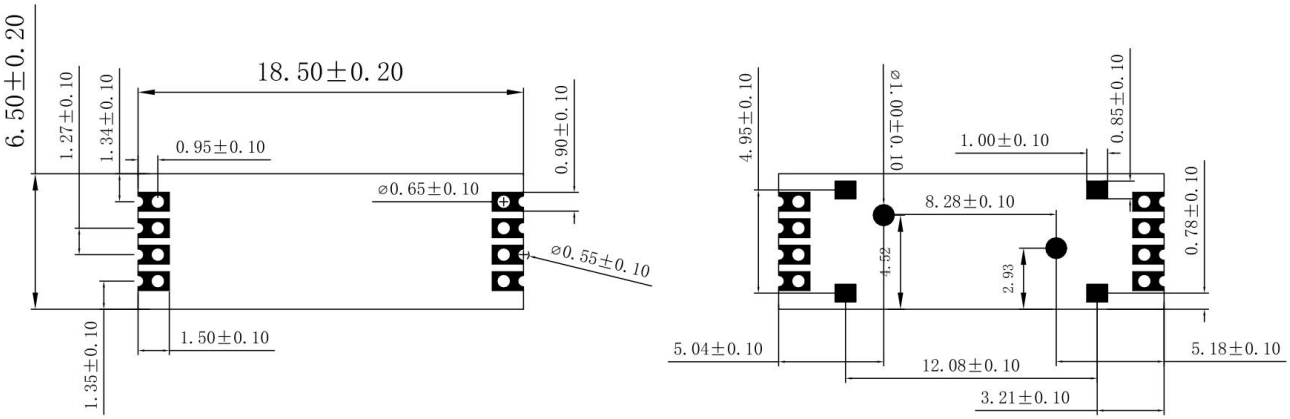


Figure 4. Size diagram

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5. Pin definition

The Rd-61 module is connected with 14 pins, refer to the pin schematic diagram, and the pin function definition table is the interface definition

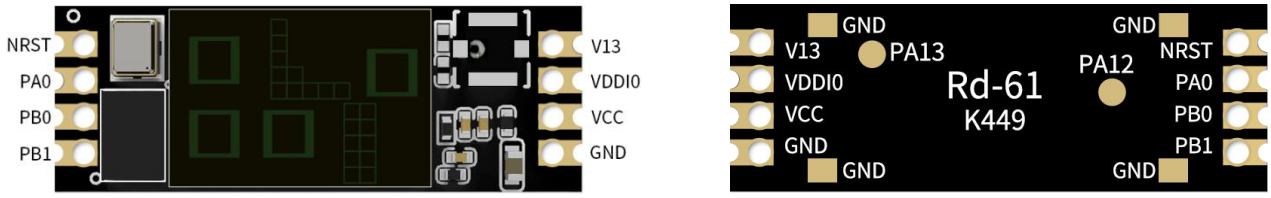


Figure 5 Pin schematic diagram

Table 5 Pin function definition table

No	Name	Function description
1	NRST	(IO), External reset, low level effective, default high level
2	PA0	(IO/WUP),PA0,WIO0,SPI0-CS,UART1-TX,PWM0,MCO-CLK
3	PB0	(IO/WUP),PB0,WIO16,UART0-TX,S-SWD-TMS,PWM0
4	PB1	(IO/WUP),PB1,WIO17,UART0-RX,S-SWD-TCK,PWM
5	GND	(G), connect to ground
6	VCC	(PI), Module power supply1.7V~5.5V，Default input3.3V
7	VDDIO	(PI), Module IO power supply1.7~3.6V，Default input 3.3V
8	V13	(PI), analog power supply input voltage 1.2V~2.5V, default 1.3V, see the power supply mode
9	GND	(G), connect to ground
10	GND	(G), connect to ground
11	GND	(G), connect to ground
12	GND	(G), connect to ground
13	PA12	(IO/WUP),PA12,WIO12,UART2-RX,UART1-RTS,PWM4, GPADC4, this pin connection internal FLASH is not available by default
14	PA13	(IO/WUP),PA13,WIO13,UART2-TX,MCO-CLK,PWM4,GPADC4

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Notice：

1. The PI represents the input power supply;

2. PO represents the output power supply;

3. IO represents the general digital function pin;

4.WUP represents the standby (standby) mode can maintain the state or wake up the digital pin of the system, where WIOx refers to the sequential number of WUP type IO;

5.The periphery of millimeter wave radar module includes power supply and 5 GPIO. VCC is the main power supply of the module, and VDDIO is the power supply of GPIO. When the power supply voltage does not exceed 3.6V, VCC and VDDIO can be directly short-connected when applied. The V13 is optionally powered by either an internal VINT or an external DCDC, and the module can achieve a lower power consumption if powered by an external DCDC.

6. Schematic diagram

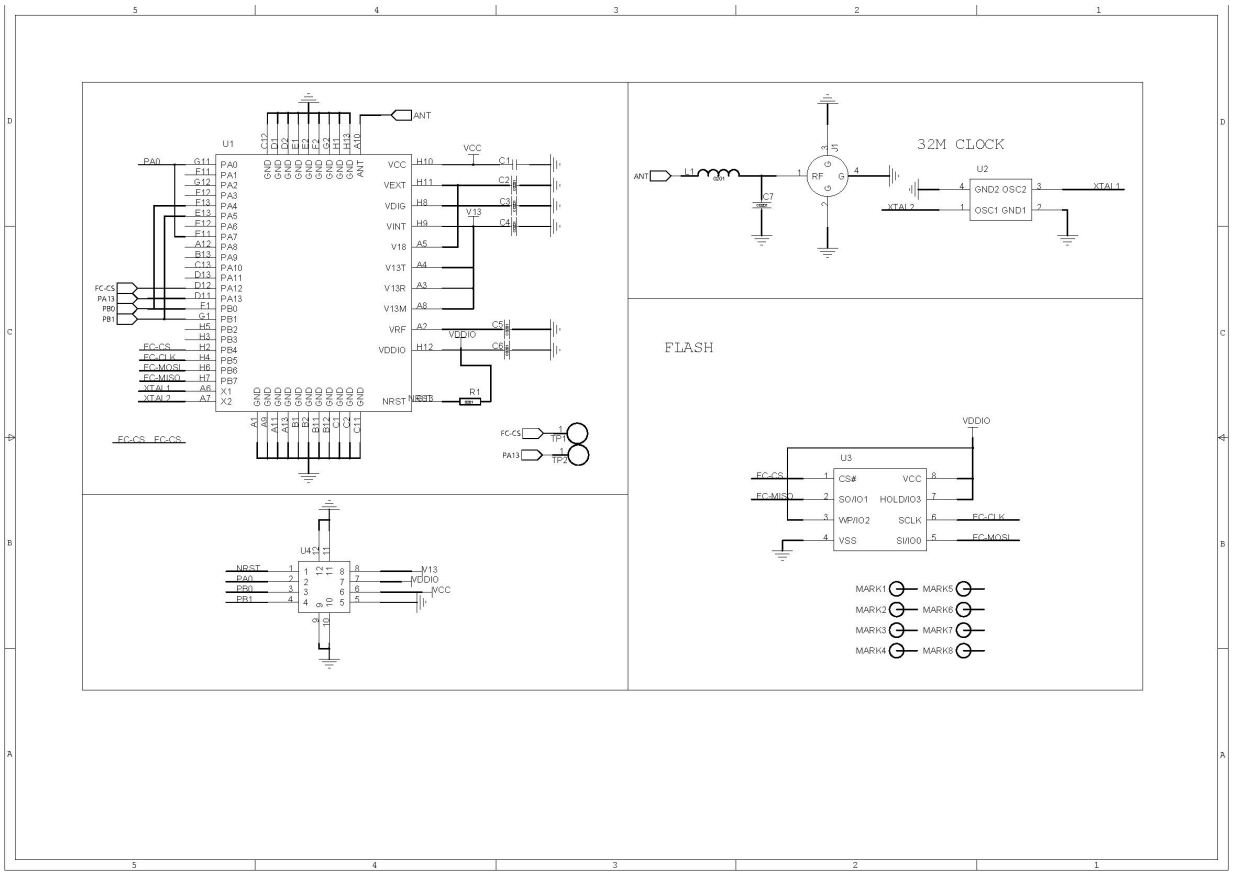


Figure 6 Schematics diagram

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7. Design guide

7.1. Application circuit guide

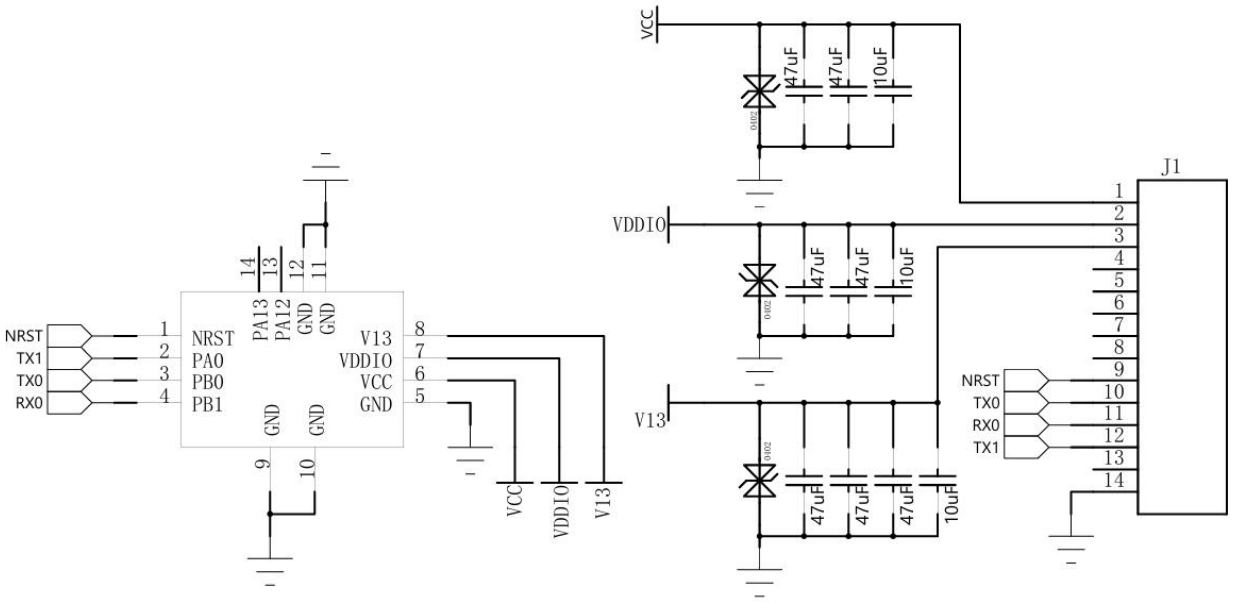


Figure 7Application circuit guide

- There is a difference between V13 external 1.3V and the low power current using the chip internal VINT. Table 6 shows the current difference comparison table using the maximum detection distance 5 m / radar report frequency 0.5Hz

Table 6 Comparison table of current differences

Parameter	Low power lining current (uA)	Average current in the unmanned state (uA)
V13 for 1.3V supply from an external DCDCVDDIO=VCC=3.3V	10.42	28.49
The V13 is powered by the internal chip VINTVDDIO=VCC=3.3V	16.9	45

- In the ultra-low power consumption scenario, providing the V13pin of the module with 1.3V power supply can further reduce the power consumption. For V13 external power supply of 1.3V DCDC, note the following:

1、The selection of DCDC requires minimal static current, recommended to be less than 1 uA;

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2、Power supply current of DCDC is> 500 mA;

- Serial port 0 (PB 0 / PB 1) receives the radar command, and serial port 1 (PA0) outputs the radar data.
- Use serial port 0 (PB 0 / PB 1). PB 0 is boot foot, and when the moment PB 0 is low level, the module enters the burning mode; when the moment PB 0 is high level.

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7.2. Recommended PCB package size

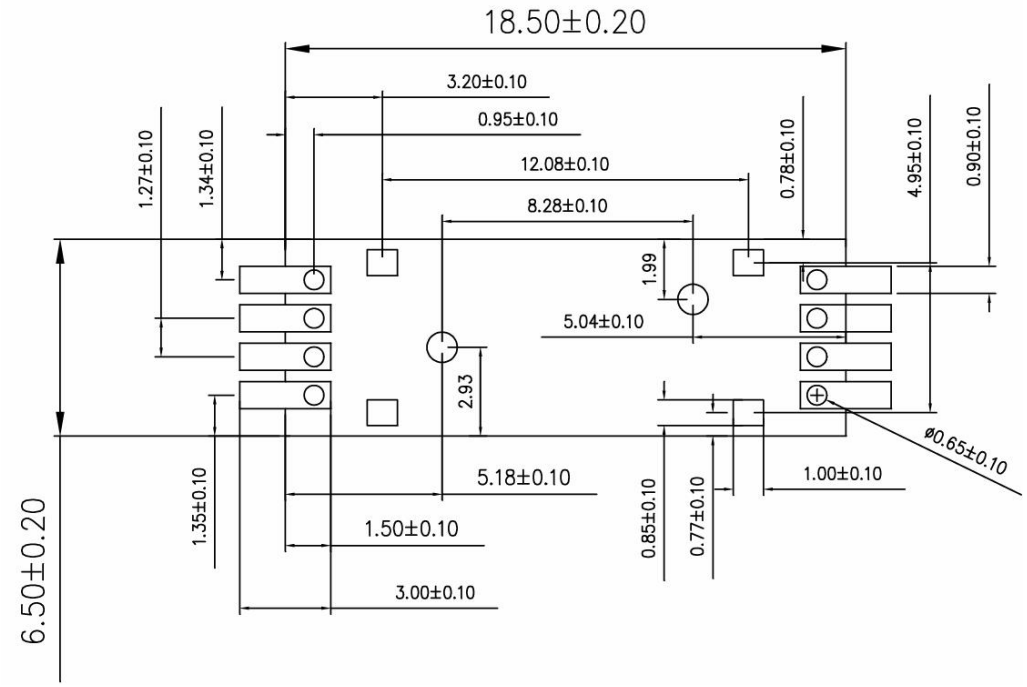


Figure 8 Recommended PCB package size

Notice：

- The Rd-61 module uses SMD 8 package standard pin header interface compatible with DIP8-1.27mm spacing

7.3. Precautions for radar installation

- In the installation position on the motherboard, the following ways are recommended:

✓ Try to ensure that the radar antenna is facing the area to be detected, and the antenna is open without shielding.

✓ To ensure that the installation position of the radar is firm and stable, the shaking of the radar itself will affect the detection effect.

✓ Make sure that there is no movement or vibration on the back of the radar. Due to the penetration of radar waves, the antenna signal back flap may detect moving objects on the back of the radar. Metal shield or metal backplane can be used to shield the radar back flap and weaken the impact of the radar back object.

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- ✓ Due to the target size, state, RCS, etc., the target distance accuracy can also fluctuate slightly.
- ✓ When there are multiple 60GHz band radars, do not beam directly and keep far away from installation to avoid possible mutual interference.
- ■ In order to meet the performance of on-board antenna, metal parts are prohibited around the antenna, away from high-frequency devices.
- ■ The power input voltage range is 1.7V-5.5V, requiring the power ripple within 100 kHz. Users should consider the corresponding electromagnetic compatibility design such as ESD and lightning surge.

7.4. Installation environment requirements

The product needs to be installed in the suitable environment, if used in the following environment, the test effect will be affected:

- ■ There are non-human objects with continuous movement in the induction area, such as animals, curtains that keep swinging, and large green plants facing the air outlet.
- ■ There is a large area of strong reflector in the induction area, which can cause interference to the radar antenna.
- ■ When installing the hanging wall, it is necessary to consider the external interference factors such as the air conditioning and electric fan on the top of the room.

7.5. Power supply

- ■ Recommended 3.3V voltage, peak current above 500 mA.
- ■ Recommended 3.3V voltage, peak current above 500 mA.
- ■ The DC-DC power supply circuit suggests to reserve the position of the dynamic response capacitor, which can optimize the output ripple when the load change is large.
- ■ In the 3.3V power interface, it is recommended to add ESD devices.

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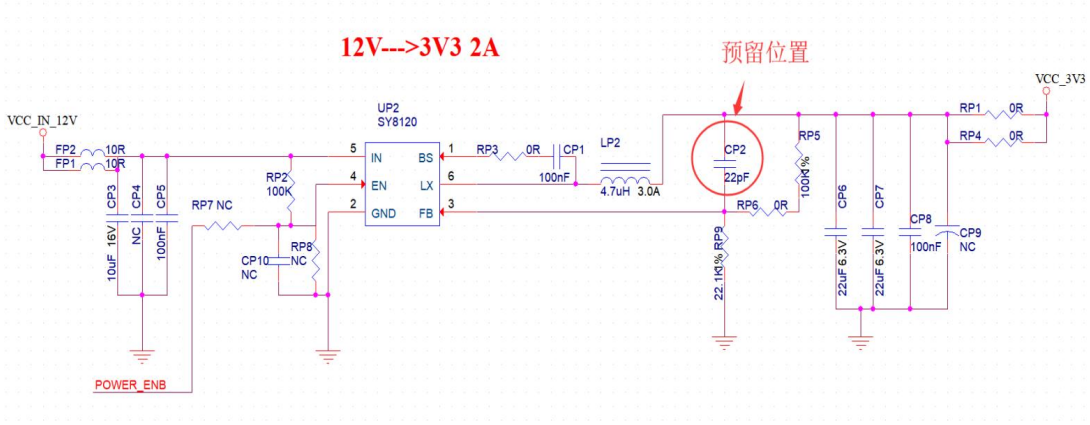


Figure 9 DC-DC buck circuit diagram

7.6. GPIO

- The periphery of the module introduces some IO ports and, if necessary, uses the recommended resistance of 10-100 ohms in series on the IO ports. This can suppress the overshoot and make the level on both sides more stable. For both EMI and ESD.
- Pull up and down of the special IO port, refer to the instructions of the specification, which will affect the startup configuration of the module.
- The IO port of the module is 3.3V. If the main control does not match the IO port level of the module, the level conversion circuit should be added.
- If the IO port is directly connected to the peripheral interface or terminals, it is recommended to reserve ESD devices at the IO port line near the terminal.

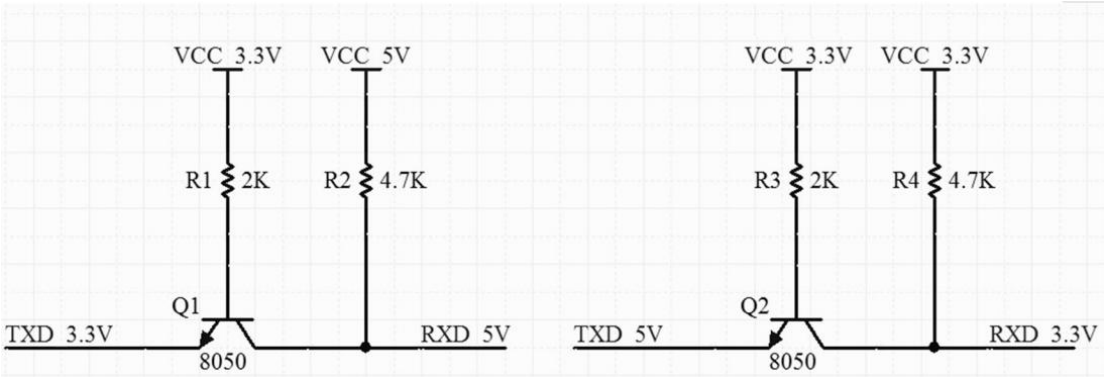


Figure 10 The level conversion circuit

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8. Storage condition

Products sealed in a moisture-proof bag shall be stored in a non-condensing atmosphere of <40℃ / 90% RH. The moisture sensitivity grade MSL of the module is level 3.

After the vacuum bag is unsealed, it must be used within 168 hours at 25 ± 5℃ / 60% RH, otherwise it needs to be baked before the secondary launch.

9. Reflow welding curve diagram

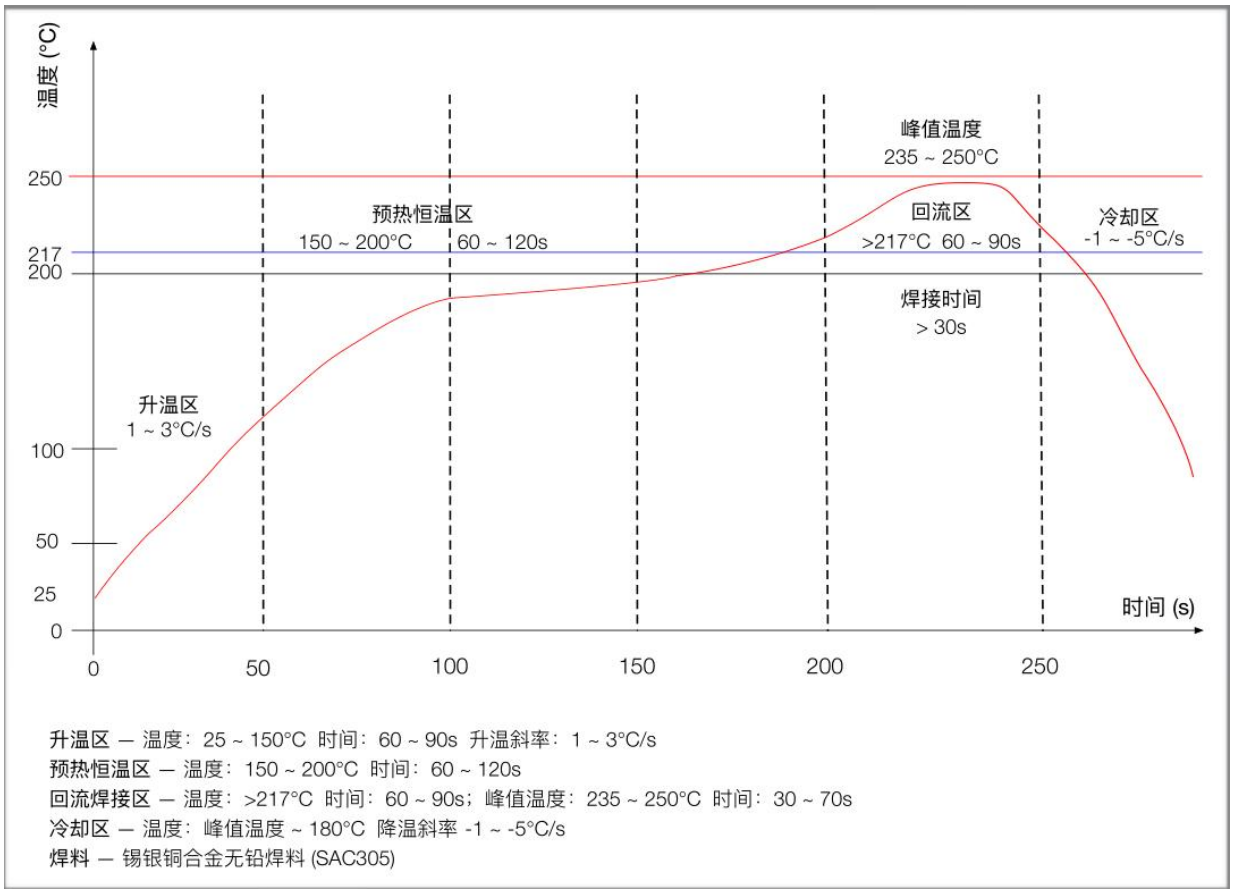


Figure 11 Reflow welding curve

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10. Product packaging information

The Rd-61 module is packaged in a tape at 800 pcs / reel. As shown in the figure below:



Figure 12 Packaging diagram

11.Contact us

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Linkedin Tmall shop Taobao shop Alibaba shop

Technial suppot Email: support@aithinker.com

Domestic business cooperation: sales@aithinker.com

Overseas business cooperation: overseas@aithinker.com

Company address: Room 403,408-410, Block C, Huafeng Smart Innovation Port, Gushu 2nd Road, Xixiang, Baoan District, Shenzhen.

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