



# PJD70P03E-AU

## 30V N-Channel Enhancement Mode MOSFET

**Voltage**

**-30 V**

**Current**

**-52 A**

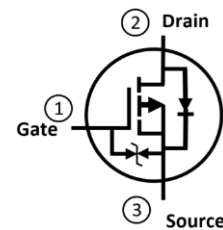
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@-10V$ ,  $I_D@-20A < 8.4m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V$ ,  $I_D@-10A < 13.5m\Omega$
- Excellent FOM
- Standard Level Drive
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.297 grams

TO-252AA



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		$V_{DS}$	-30	V
Gate-Source Voltage		$V_{GS}$	$\pm 25$	
Continuous Drain Current (Note 3)	$T_C=25^\circ\text{C}$	$I_D$	-52	A
	$T_C=100^\circ\text{C}$		-36	
Pulsed Drain Current (Note 1)	$T_C=25^\circ\text{C}$	$I_{DM}$	-208	
Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	79	W
	$T_C=100^\circ\text{C}$		39	
Continuous Drain Current (Note 4)	$T_A=25^\circ\text{C}$	$I_D$	-13.4	A
	$T_A=70^\circ\text{C}$		-11.2	
Power Dissipation	$T_A=25^\circ\text{C}$	$P_D$	2.4	W
	$T_A=70^\circ\text{C}$		1.7	
Single Pulse Avalanche Energy (Note 5)		$E_{AS}$	100	mJ
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~175	$^\circ\text{C}$
Typical Thermal Resistance (Note 4)	Junction to Case	$R_{\theta JC}$	1.9	$^\circ\text{C/W}$
	Junction to Ambient	$R_{\theta JA}$	62.5	



## PJD70P03E-AU

### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1	-1.7	-2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A	-	6.7	8.4	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	-	10.4	13.5	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±25V, V <sub>DS</sub> =0V	-	-	±10	uA
		V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V			±1	
<b>Dynamic</b> (Note 6)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-24V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V	-	54	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	6	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	17	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz	-	2310	-	pF
Output Capacitance	C <sub>oss</sub>		-	332	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	256	-	
Gate resistance	R <sub>g</sub>	f=1MHz	-	2.3	-	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =-24V, I <sub>D</sub> =-20A, V <sub>GS</sub> =-10V, R <sub>G</sub> =3Ω (Note 2)	-	9	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	91	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	47	-	
Turn-Off Fall Time	t <sub>f</sub>		-	99	-	
<b>Drain-Source Diode</b>						
Diode Forward Current	I <sub>S</sub>	T <sub>C</sub> =25°C	-	-	-52	A
Pulsed Diode Forward Current	I <sub>SM</sub>		-	-	-208	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-20A, V <sub>GS</sub> =0V	-	-0.85	-1.3	V
Reverse Recovery Time	T <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A dI <sub>S</sub> /dt=100A/us	-	22	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	10	-	nC

**NOTES :**

- Pulse width ≤ 300us, Duty cycle ≤ 2%.
- Essentially independent of operating temperature typical characteristics.
- The maximum current rating is package limited.
- R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz. square pad of copper.
- The test condition is L=0.5mH, I<sub>AS</sub>= -20A, V<sub>DD</sub>= -30V, V<sub>GS</sub>= -10V, Starting T<sub>J</sub>=25°C.
- Guaranteed by design, not subject to production testing.



# PJD70P03E-AU

## TYPICAL CHARACTERISTIC CURVES

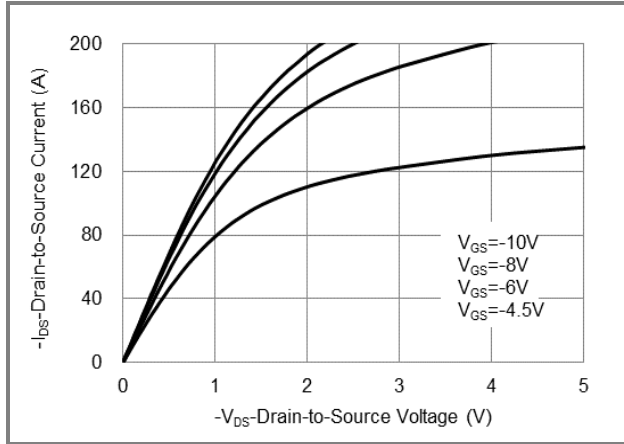


Fig.1 On-Region Characteristics

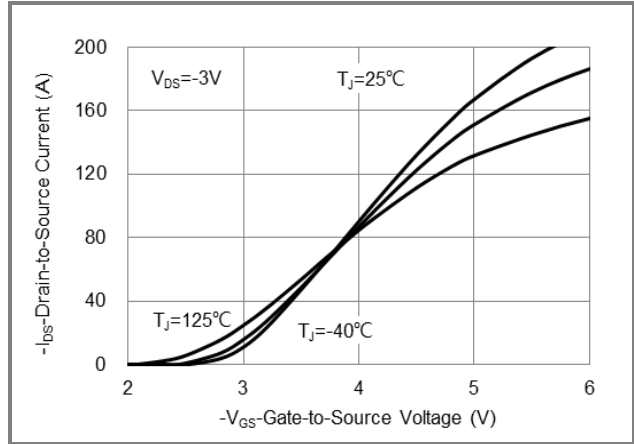


Fig.2 Transfer Characteristics

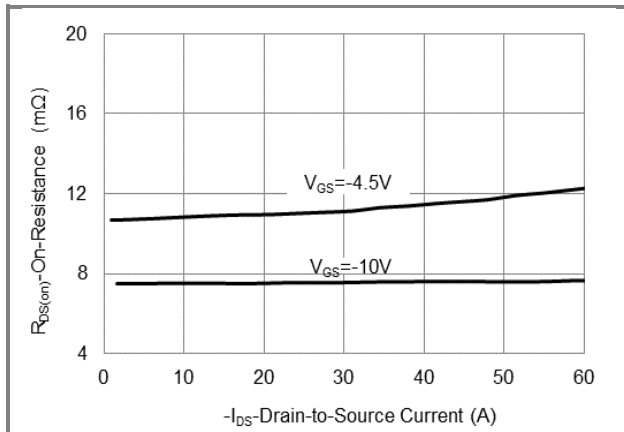


Fig.3 On-Resistance vs. Drain Current

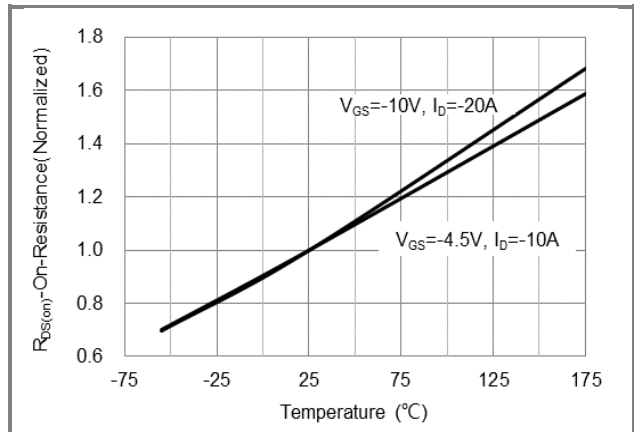


Fig.4 On-Resistance vs. Junction temperature

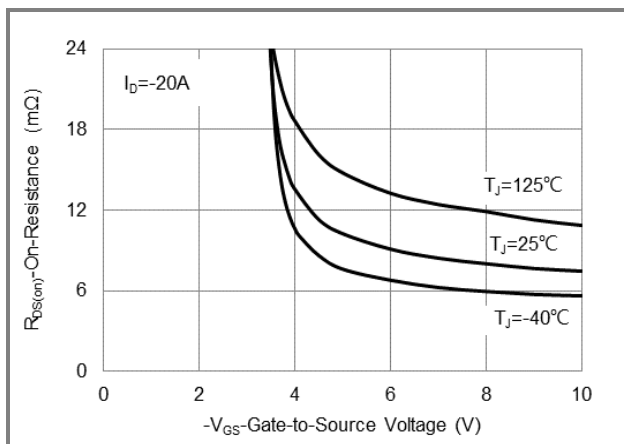


Fig.5 On-Resistance Variation with  $V_{GS}$

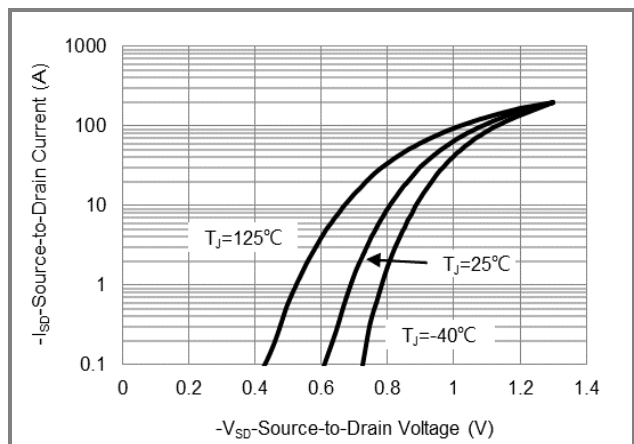
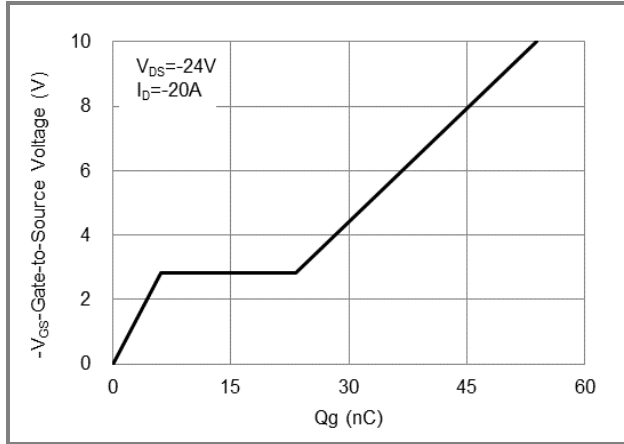


Fig.6 Source-Drain Diode Forward Voltage

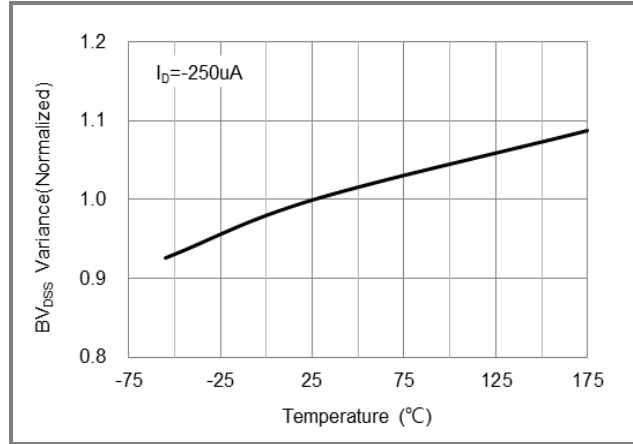


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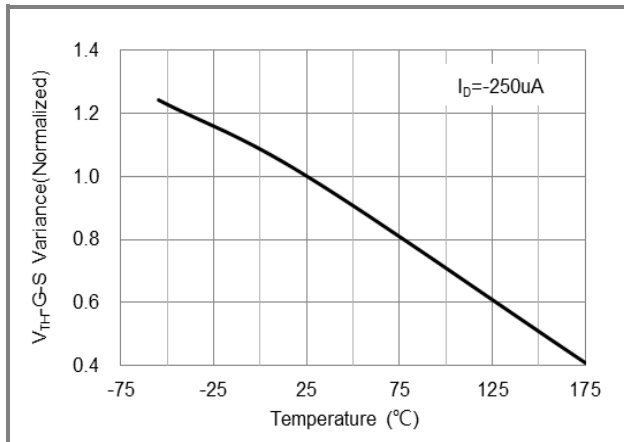
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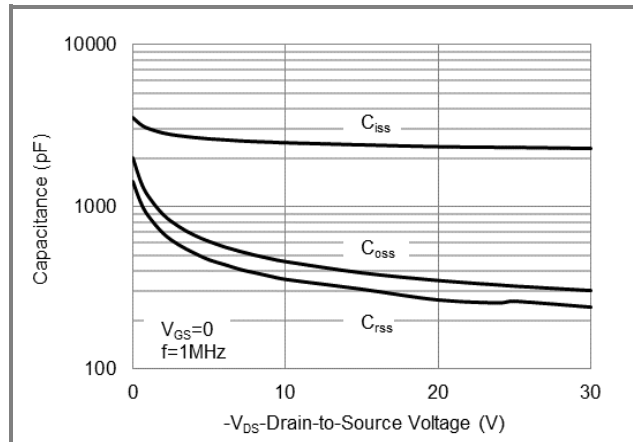
**Fig.7 Gate-Charge Characteristics**



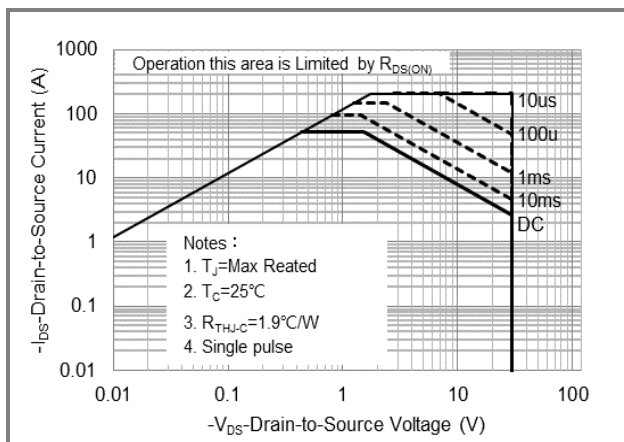
**Fig.8 Breakdown Voltage Variation vs. Temperature**



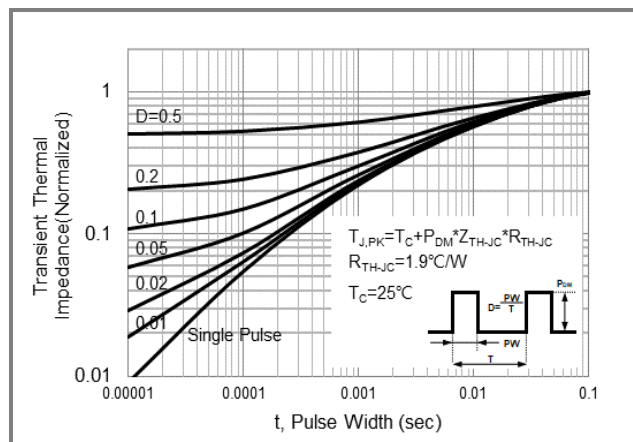
**Fig.9 Threshold Voltage Variation with Temperature**



**Fig.10 Capacitance vs. Drain-Source Voltage**



**Fig.11 Maximum Safe Operating Area**



**Fig.12 Normalized Transient Thermal Impedance**

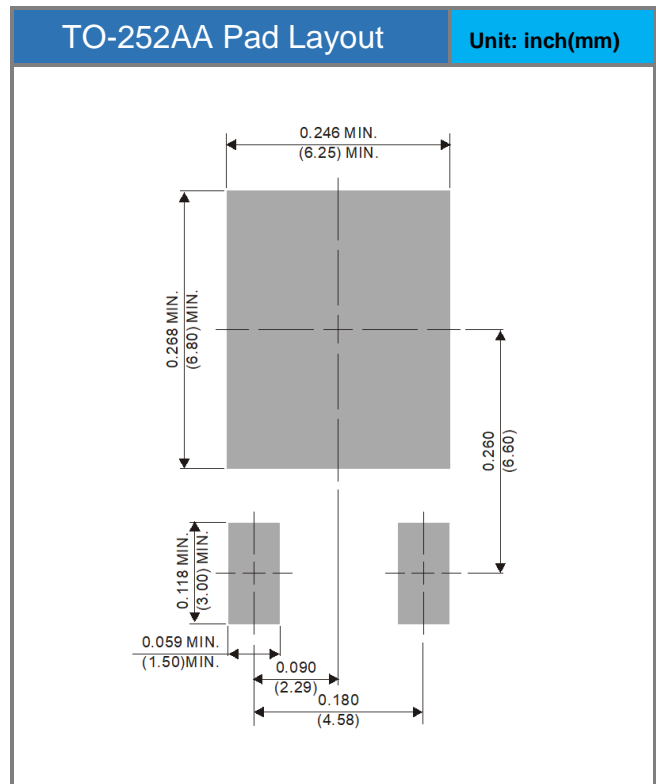
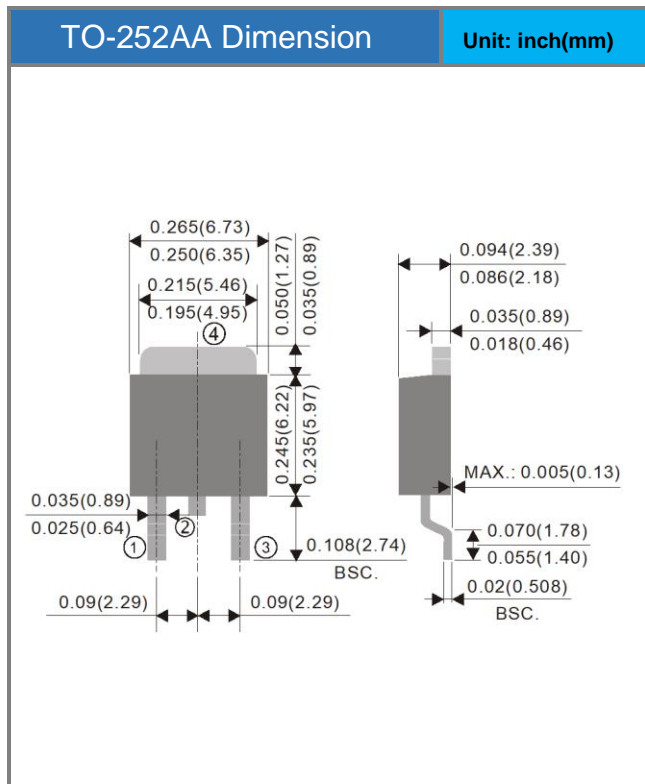


# PJD70P03E-AU

## Part No Packing Code Version

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD70P03E-AU_R2_002A1	TO-252AA	3,000 pcs / 13" reel	D70P03E	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout





## PJD70P03E-AU

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