



# PJQ1820U-20V

## 20V N-Channel Enhancement Mode MOSFET

Voltage	20 V	Current	1 A
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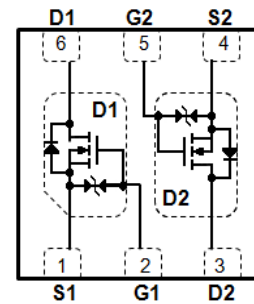
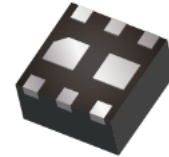
### Features

- Advanced Trench Process Technology
- ESD Protected
- Low Gate Charge
- Fast Switching
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN1010B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0011 grams

DFN1010B-6L



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	20	V
Gate-Source Voltage		V <sub>GS</sub>	±8	
Continuous Drain Current <sup>(Note 4)</sup>		I <sub>D</sub>	1.0	A
Pulsed Drain Current <sup>(Note 1)</sup>		I <sub>DM</sub>	2.0	
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	400	mW
	Derate above 25°C		3.2	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance		R <sub>θJA</sub>	312	°C/W
- Junction to Ambient <sup>(Note 5)</sup>				



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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	20	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	0.3	0.6	0.8	
Drain-Source On-State Resistance	R <sub>DSON</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =500mA	-	220	300	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =400mA	-	250	400	
		V <sub>GS</sub> =1.8V, I <sub>D</sub> =200mA	-	300	550	
		V <sub>GS</sub> =1.5V, I <sub>D</sub> =100mA	-	370	800	
		V <sub>GS</sub> =1.2V, I <sub>D</sub> =10mA	-	640	1500	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V	-	-	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	-	-	±10	
<b>Dynamic</b> (Note 6)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, I <sub>D</sub> =0.5A, V <sub>GS</sub> =4.5V(Note 2,3)	-	1.1	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	0.2	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =16V, V <sub>GS</sub> =0V, f=1MHZ	-	46	-	pF
Output Capacitance	C <sub>oss</sub>		-	12	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	6	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =16V, I <sub>D</sub> =0.5A, V <sub>GS</sub> =4.5V, R <sub>θ</sub> =3Ω (Note 2,3)	-	2.8	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	21	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	61	-	
Turn-Off Fall Time	t <sub>f</sub>		-	37	-	
<b>Drain-Source Diode</b>						
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =0.2A, V <sub>GS</sub> =0V	-	0.8	1	V
Reverse Recovery Time	T <sub>rr</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =0.5A	-	9	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt=100A/us (Note 2,3)	-	1	-	nC

Notes :

- 1.Pulse width<300us, Duty cycle<2%.
- 2.Essentially independent of operating temperature typical characteristics.
- 3.Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C.Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> =25°C.
- 4.The maximum current rating is package limited.
- 5.R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6.Guaranteed by design, not subject to production testing.



# PJQ1820U-20V

## TYPICAL CHARACTERISTIC CURVES

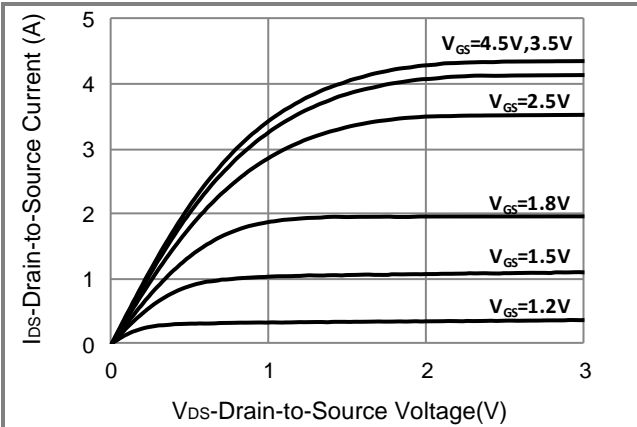


Fig.1 Output Characteristics

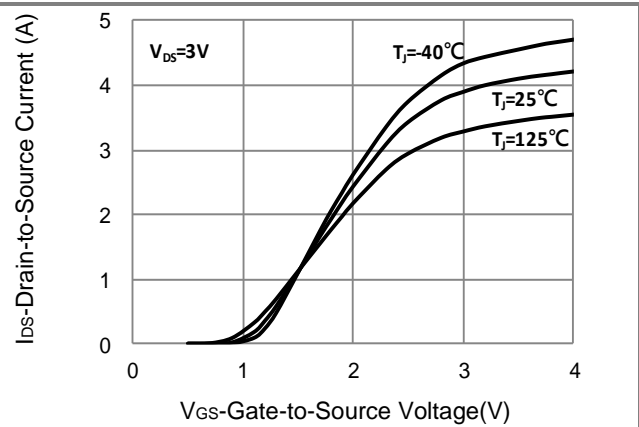


Fig.2 Transfer Characteristics

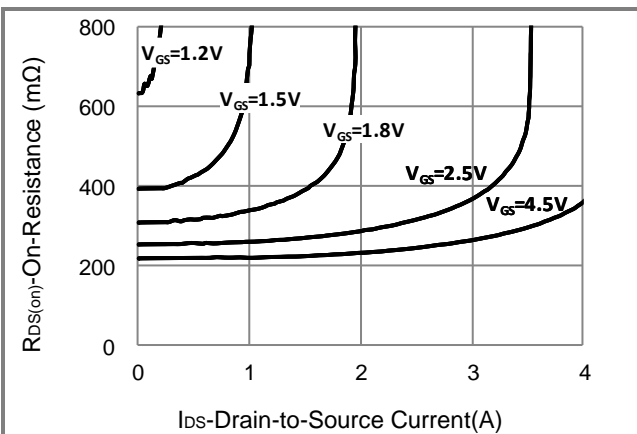


Fig.3 On-Resistance vs. Drain Current

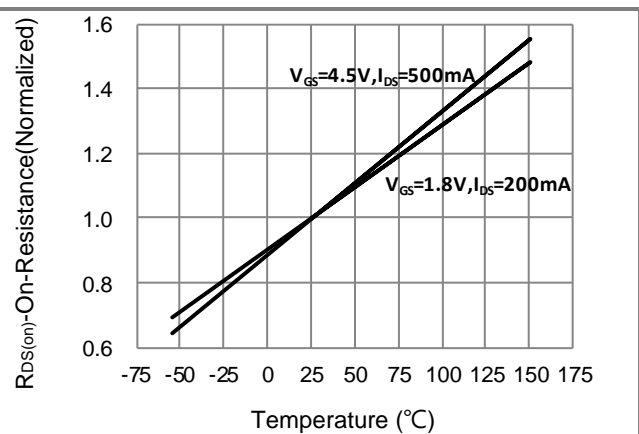


Fig.4 On-Resistance vs. Junction temperature

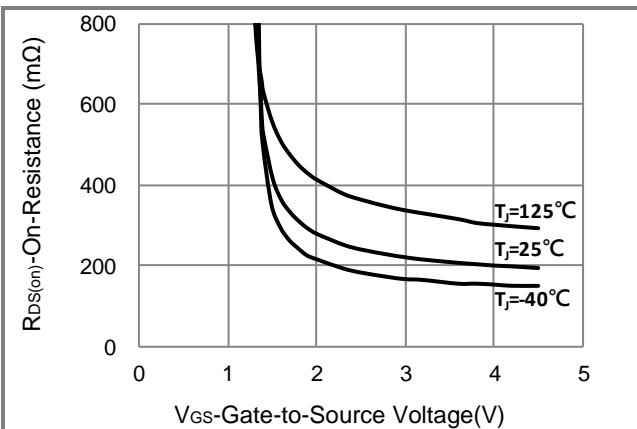


Fig.5 On-Resistance Variation with Vgs

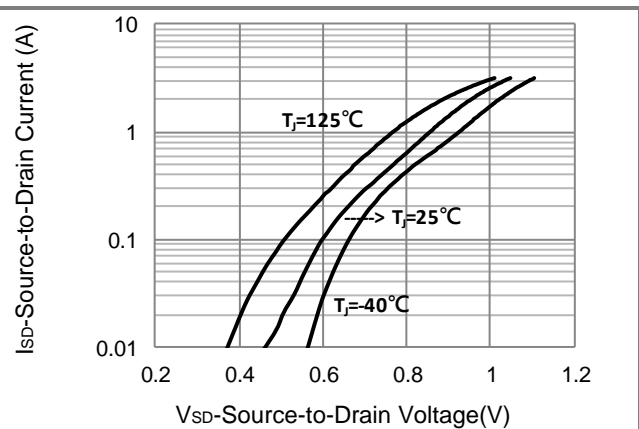
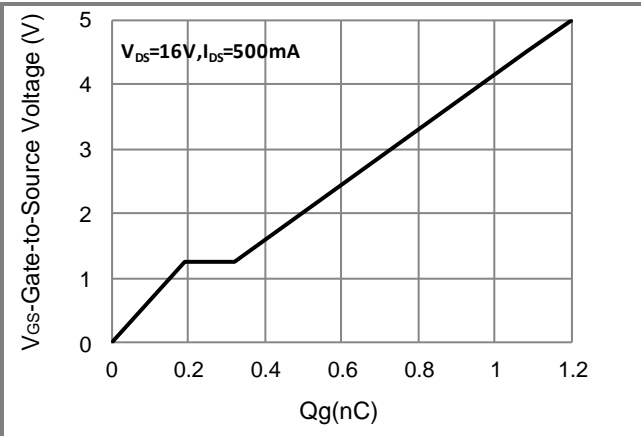


Fig.6 Source-Drain Diode Forward Voltage

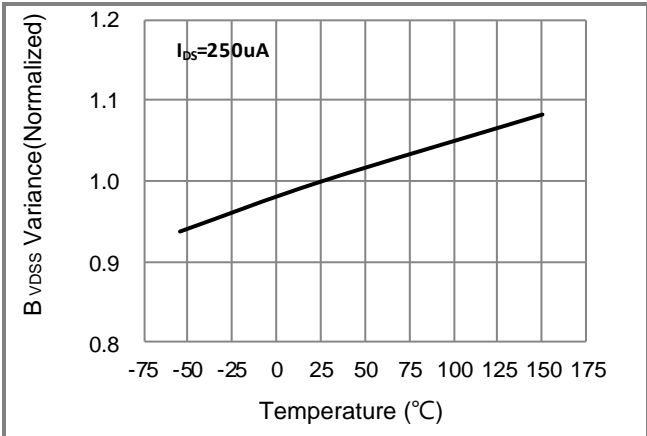


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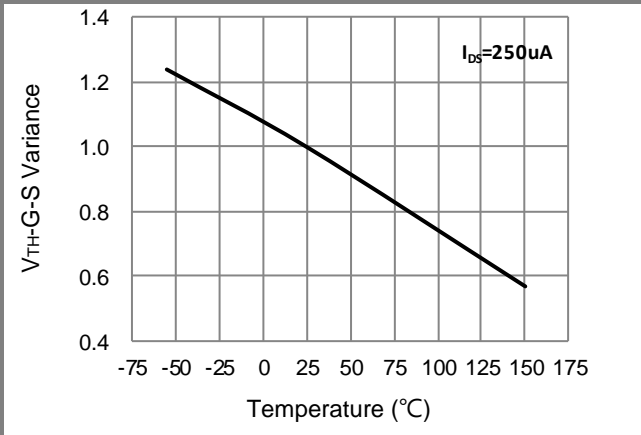
## TYPICAL CHARACTERISTIC CURVES



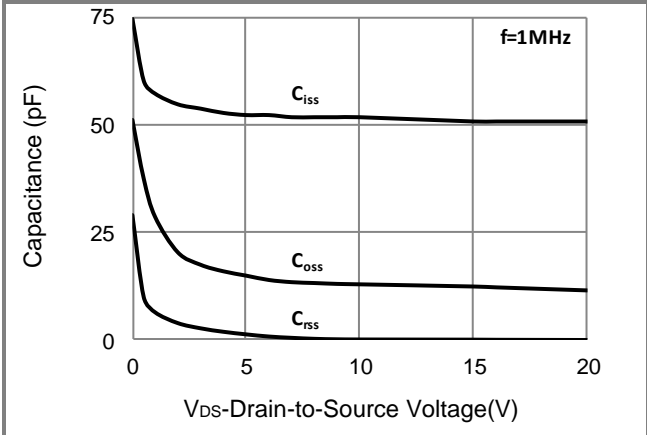
**Fig.7 Gate-Charge Characteristics**



**Fig.8 Breakdown Voltage Variation vs. Temperature**



**Fig.9 Threshold Voltage Variation with Temperature**



**Fig.10 Capacitance vs. Drain-Source Voltage**

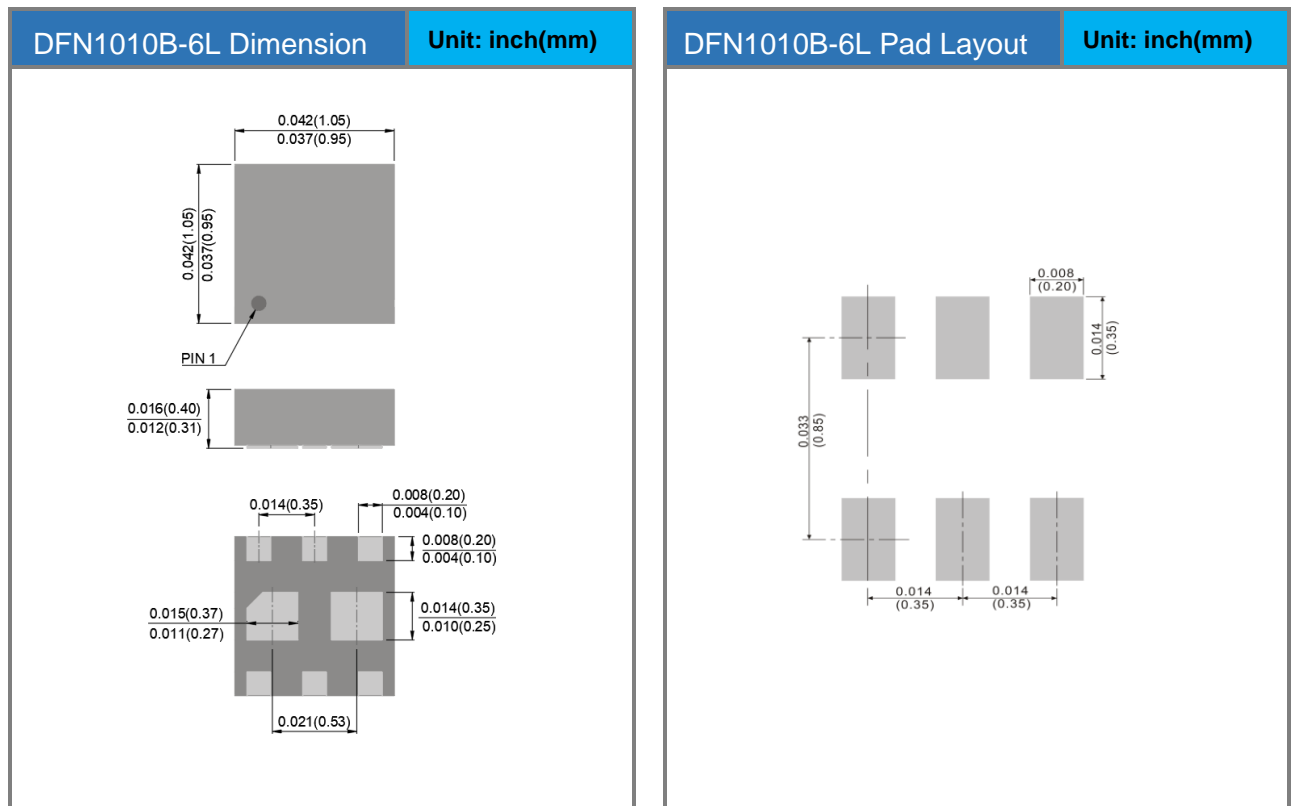


# PJQ1820U-20V

## Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ1820U-20V_R1_00201	DFN1010B-6L	5K pcs / 7" reel	20	Halogen free RoHS compliant

## Packaging Information & Mounting Pad Layout





## PJQ1820U-20V

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