

2MBI800XRNE170-50

IGBT Modules

Power Module (X series)
1700V / 800A / 2-in-1 package

■ **Features**

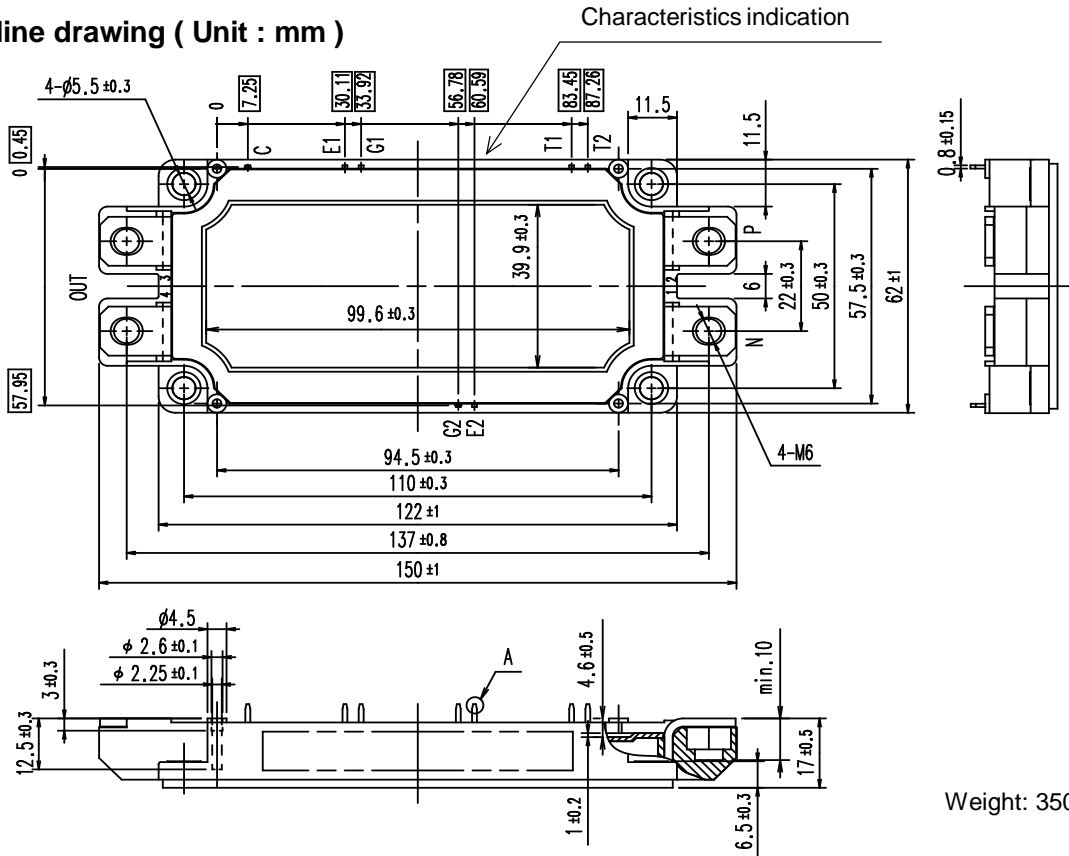
- Low $V_{CE(sat)}$
- Low Inductance Module structure
- Solder pin terminals

■ **Applications**

- Inverter for Motor Drives, AC and DC Servo Drives
- Uninterruptible Power Supply Systems, Wind Turbines, PV Power Conditioning Systems

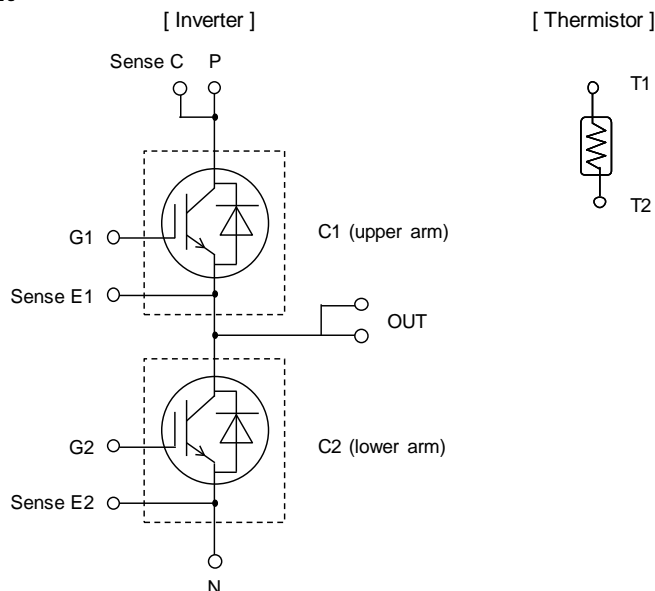


■ **Outline drawing (Unit : mm)**



NOTE) shows theoretical dimension and tolerance is $\phi \pm 0.5$

■ **Equivalent circuit**



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■ Absolute maximum ratings (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Items		Symbols	Conditions	Maximum ratings	Units
Inverter	Collector-Emitter voltage, Gate-Emitter short-circuited	V_{CES}		1700	V
	Gate-Emitter voltage, Collector-Emitter short-circuited	V_{GES}		± 20	V
	Collector current	I_C	Continuous $T_c=100^\circ\text{C}$	800	A
	Repetitive peak collector current	I_{CRM}	1ms	1600	
	Reverse-conducting current	I_{RC}	Continuous	800	
	Repetitive peak reverse-conducting current	I_{RCRM}	1ms	1600	
	Total power dissipation	P_{tot}	1 device	7140	W
	Virtual junction temperature	T_{vj}		175	°C
	Operating virtual junction temperature (under switching conditions)	T_{vjop}		175	
	Case temperature	T_c		125	
Storage temperature	T_{stg}		-40 ~ 125		
Isolation voltage	between terminals and copper base (*1) between thermistor and others (*2)	V_{isol}	AC: 1min.	4000	Vrms
Mounting torque for screws to heatsink (*3)		M_s	M5	6.0	N·m
Mounting torque for terminal screws (*3)		M_t	M6	6.0	

(*1) All terminals should be connected together during the test.

(*2) Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

(*3) Recommendable Value: Mounting 2.5 ~ 6.0 N·m (M5)
 Recommendable Value: Terminals 3.5 ~ 6.0 N·m (M6)

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■ Electrical characteristics (at $T_{vj}= 25^{\circ}\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Collector-Emitter cut-off current, Gate-Emitter short-circuited	I_{CES}	$V_{GE} = 0V$ $V_{CE} = 1700V$	-	-	200	μA	
Gate leakage current, Collector-Emitter short-circuited	I_{GES}	$V_{CE}=0V, V_{GE}=\pm 20V$	-	-	400	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V$ $I_C = 800\text{mA}$	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (terminal)	$V_{GE} = 15V$ $I_C = 800A$	$T_{vj}=25^{\circ}\text{C}$	-	3.25	3.80	V
	$V_{CE(sat)}$ (chip)		$T_{vj}=25^{\circ}\text{C}$	-	2.30	2.75	
			$T_{vj}=125^{\circ}\text{C}$	-	2.75	-	
			$T_{vj}=150^{\circ}\text{C}$	-	2.80	-	
			$T_{vj}=175^{\circ}\text{C}$	-	2.85	-	
Internal Gate resistance	r_g	-	-	0.95	-	Ω	
Input capacitance	C_{ies}	$V_{CE}=10V, V_{GE}=0V, f=1\text{MHz}$	-	119	-	nF	
Output capacitance	C_{oes}		-	3.8	-		
Reverse transfer capacitance	C_{ies}		-	0.90	-		
Gate charge	Q_G		$V_{CC} = 900V, I_C = 800A$ $V_{GE} = -15 \rightarrow +15V$	-	6.9		-
Reverse-conducting voltage	V_{RC} (terminal)	$V_{GE} = 0V$ $I_{RC} = 800A$	$T_{vj}=25^{\circ}\text{C}$	-	3.25	3.80	V
	V_{RC} (chip)		$T_{vj}=25^{\circ}\text{C}$	-	2.30	2.75	
			$T_{vj}=125^{\circ}\text{C}$	-	2.70	-	
			$T_{vj}=150^{\circ}\text{C}$	-	2.75	-	
			$T_{vj}=175^{\circ}\text{C}$	-	2.75	-	
Turn-on delay time(*1)	$t_{d(on)}$	$V_{CC} = 900V$ $I_C, I_{RC} = 800A$ $V_{GE} = +15V / -15V$ $R_G = 1.2\Omega$ $L_S = 35 \text{ nH}$	$T_{vj}=25^{\circ}\text{C}$	-	0.47	-	μs
			$T_{vj}=125^{\circ}\text{C}$	-	0.48	-	
			$T_{vj}=150^{\circ}\text{C}$	-	0.47	-	
			$T_{vj}=175^{\circ}\text{C}$	-	0.49	-	
Rise time	t_r		$T_{vj}=25^{\circ}\text{C}$	-	0.10	-	
			$T_{vj}=125^{\circ}\text{C}$	-	0.12	-	
			$T_{vj}=150^{\circ}\text{C}$	-	0.12	-	
			$T_{vj}=175^{\circ}\text{C}$	-	0.13	-	
Turn-off delay time(*2)	$t_{d(off)}$		$T_{vj}=25^{\circ}\text{C}$	-	0.57	-	
			$T_{vj}=125^{\circ}\text{C}$	-	0.60	-	
			$T_{vj}=150^{\circ}\text{C}$	-	0.62	-	
			$T_{vj}=175^{\circ}\text{C}$	-	0.64	-	
Fall time	t_f	$T_{vj}=25^{\circ}\text{C}$	-	0.23	-		
		$T_{vj}=125^{\circ}\text{C}$	-	0.40	-		
		$T_{vj}=150^{\circ}\text{C}$	-	0.47	-		
		$T_{vj}=175^{\circ}\text{C}$	-	0.55	-		
Forward recovery time	t_{fr}	$T_{vj}=25^{\circ}\text{C}$	-	0.30	-		
		$T_{vj}=125^{\circ}\text{C}$	-	0.45	-		
		$T_{vj}=150^{\circ}\text{C}$	-	0.52	-		
		$T_{vj}=175^{\circ}\text{C}$	-	0.56	-		

(*1) Turn on time (t_{on}) = $t_{d(on)} + t_r$

(*2) Turn off time (t_{off}) = $t_{d(off)} + t_f$

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■ Electrical characteristics (at $T_{vj}= 25^{\circ}\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units		
			min.	typ.	max.			
Inverter	Turn-on energy (per puls)	$V_{CC} = 900\text{V}$ $I_C, I_{RC} 800\text{A}$ $V_{GE} = +15\text{V} / -15\text{V}$ $R_G = 1.2\Omega$ $L_S = 35\text{ nH}$	$T_{vj}=25^{\circ}\text{C}$	-	149.2	-	mJ	
			$T_{vj}=125^{\circ}\text{C}$	-	235.8	-		
			$T_{vj}=150^{\circ}\text{C}$	-	277.7	-		
	Turn-off energy (per puls)		E_{off}	$T_{vj}=175^{\circ}\text{C}$	-	302.9		-
				$T_{vj}=25^{\circ}\text{C}$	-	144.1		-
				$T_{vj}=125^{\circ}\text{C}$	-	191.7		-
	Forward recovery energy (per puls)		E_{fr}	$T_{vj}=150^{\circ}\text{C}$	-	211.1		-
				$T_{vj}=175^{\circ}\text{C}$	-	234.0		-
				$T_{vj}=25^{\circ}\text{C}$	-	76.5		-
$T_{vj}=125^{\circ}\text{C}$		-		124.8	-			
Thermistor	Resistance	R	$T = 25^{\circ}\text{C}$	-	5000	-	Ω	
			$T = 100^{\circ}\text{C}$	465	495	520		
	B value	B	$T = 25/ 50^{\circ}\text{C}$		3305	3375	3450	K

NOTICE:

The external gate resistance (R_G) shown above is one of our recommended value for the purpose of minimum switching loss. However the optimum R_G depends on circuit configuration and/or environment. We recommend that the R_G has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on.

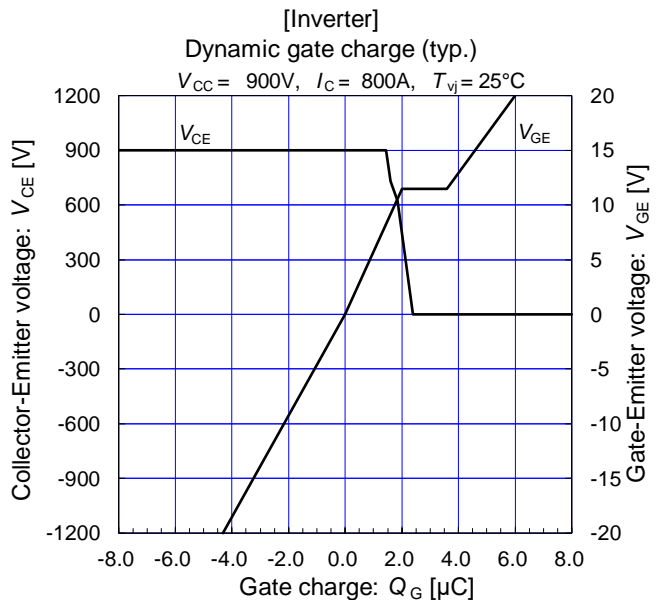
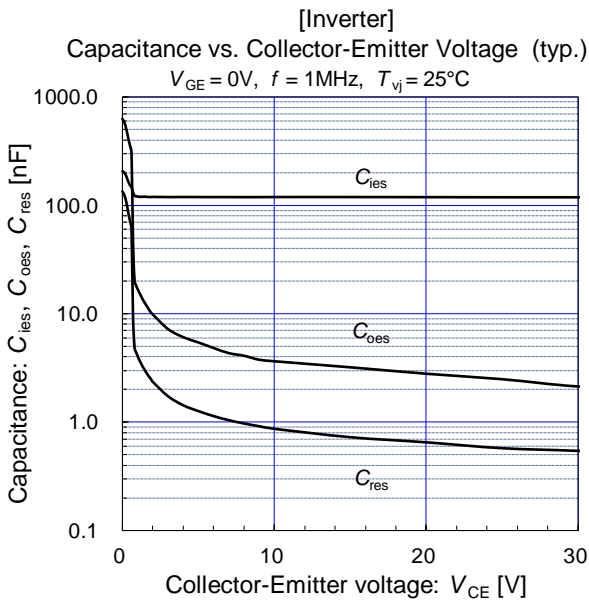
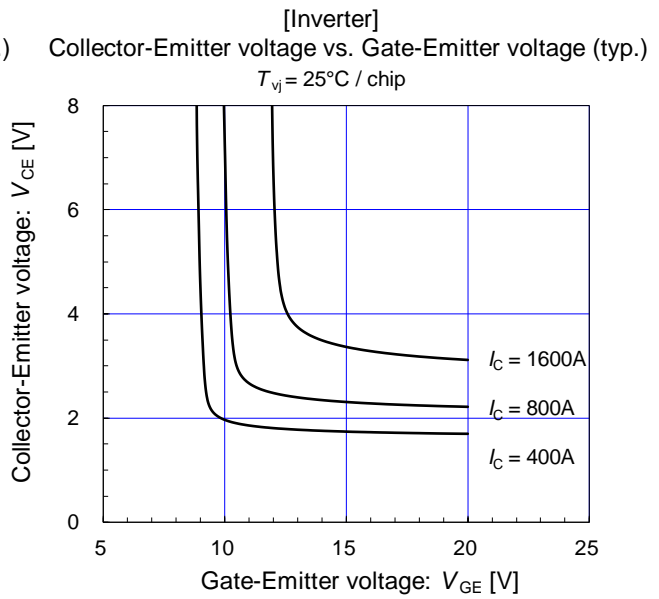
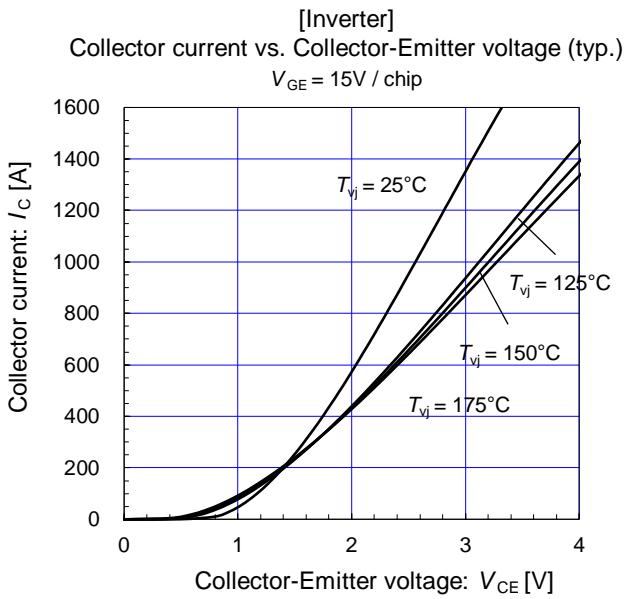
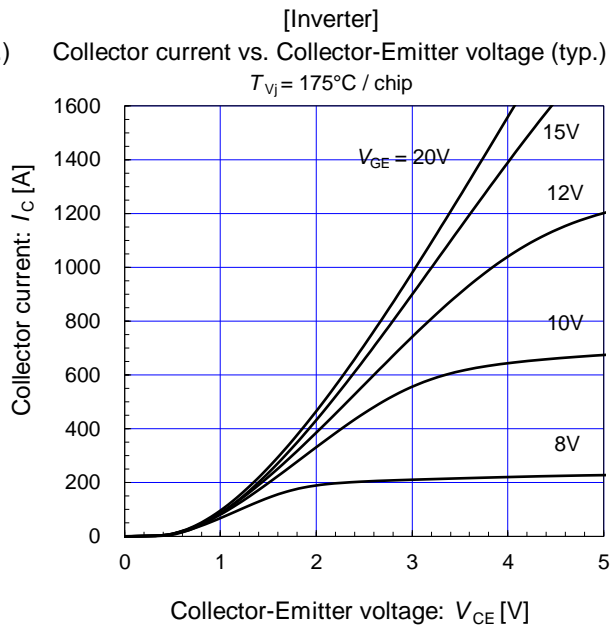
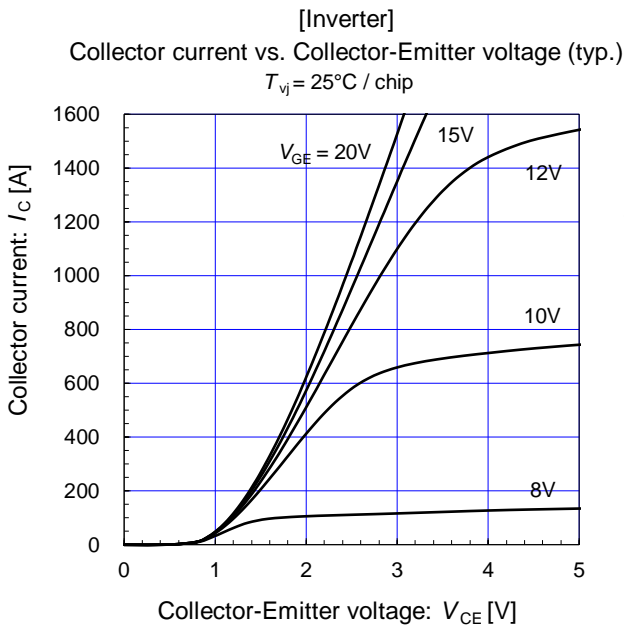
■ Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance junction to case(1 device)	$R_{th(j-c)}$	RC-IGBT	-	-	0.021	K/W
Thermal resistance case to heatsink(1 device)(*1)	$R_{th(c-s)}$	with 1 W/(m·K) thermal grease	-	0.02	-	

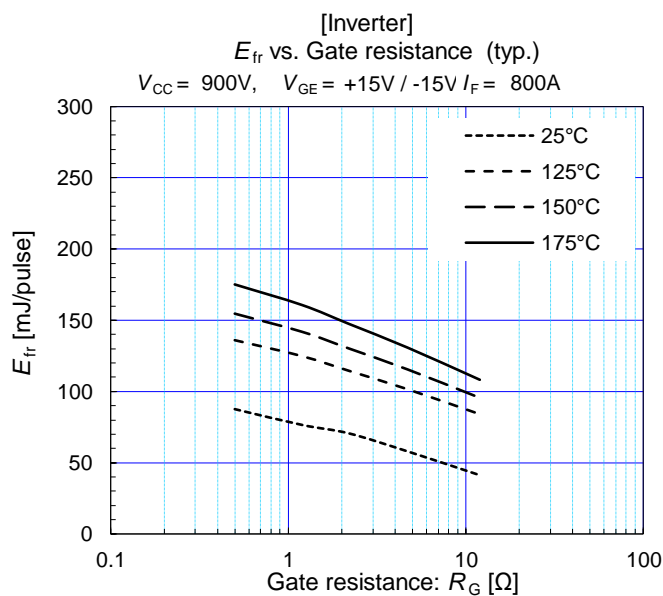
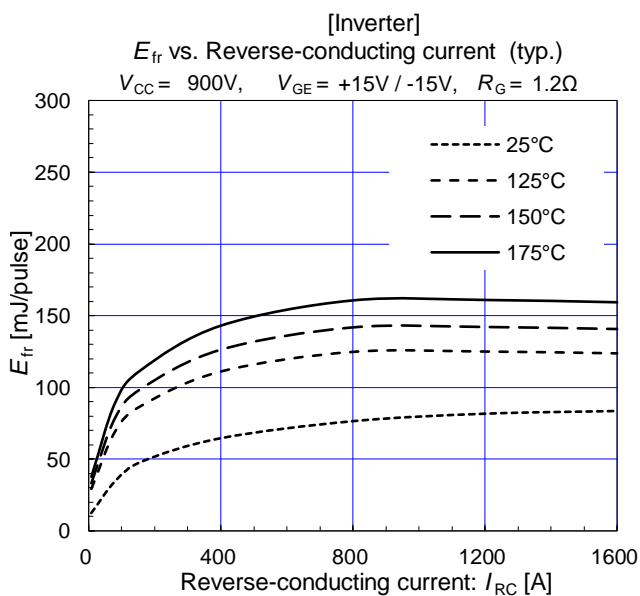
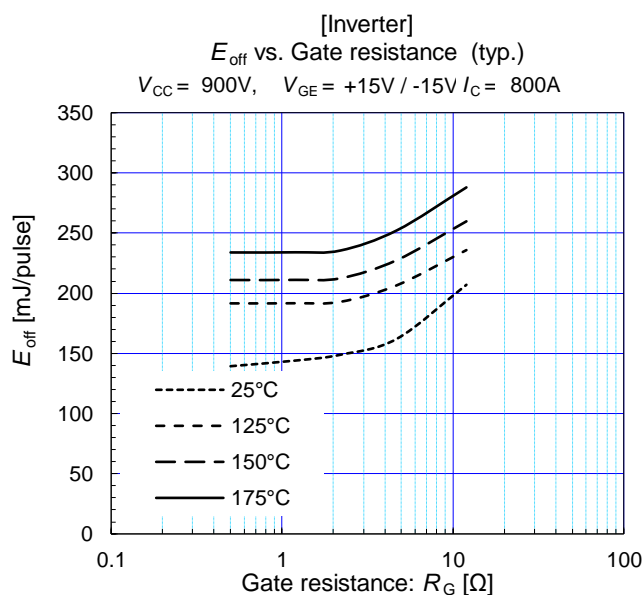
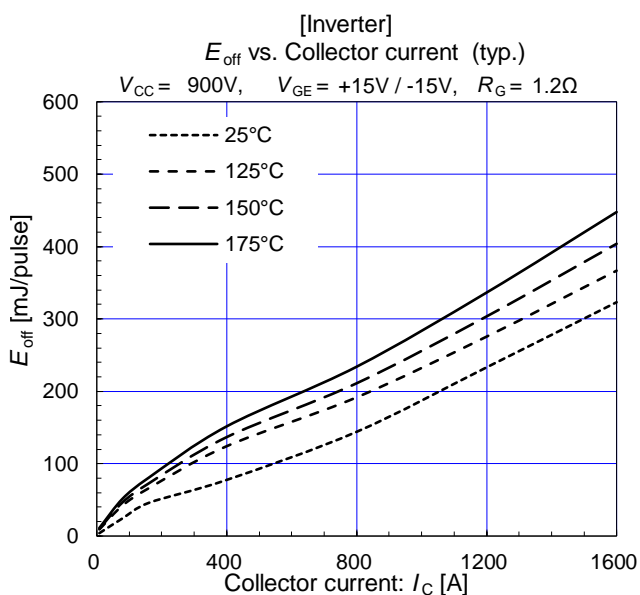
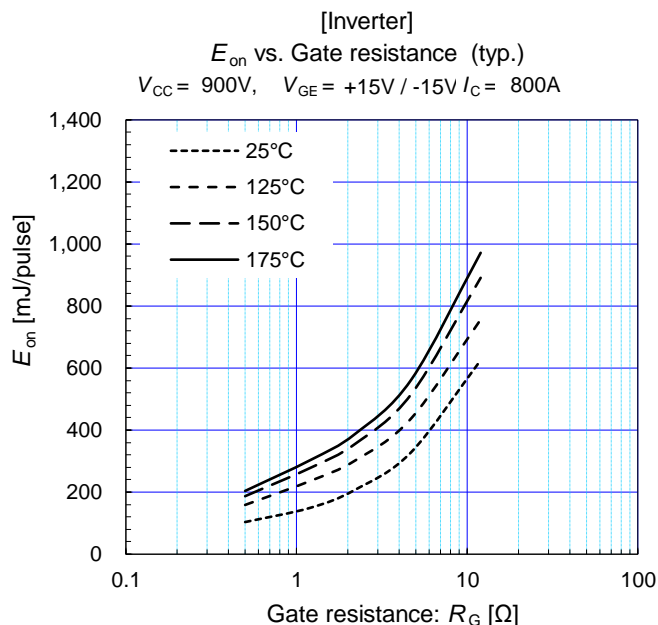
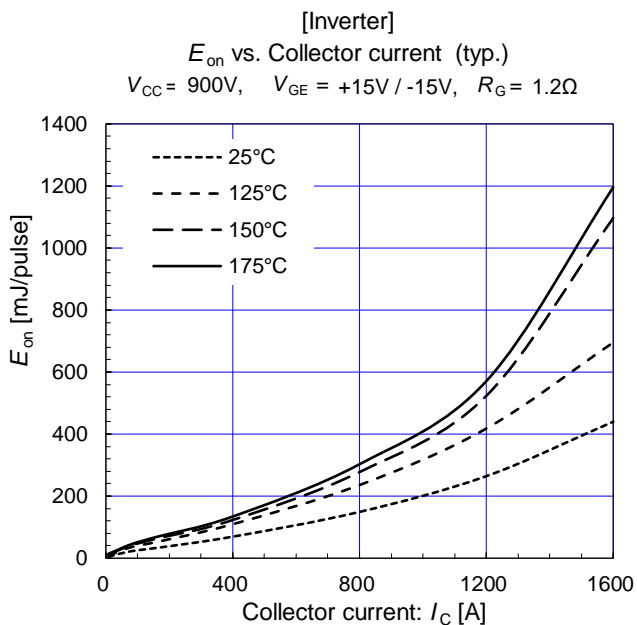
(*1) This is the value which is defined mounting on the additional heatsink with thermal grease.

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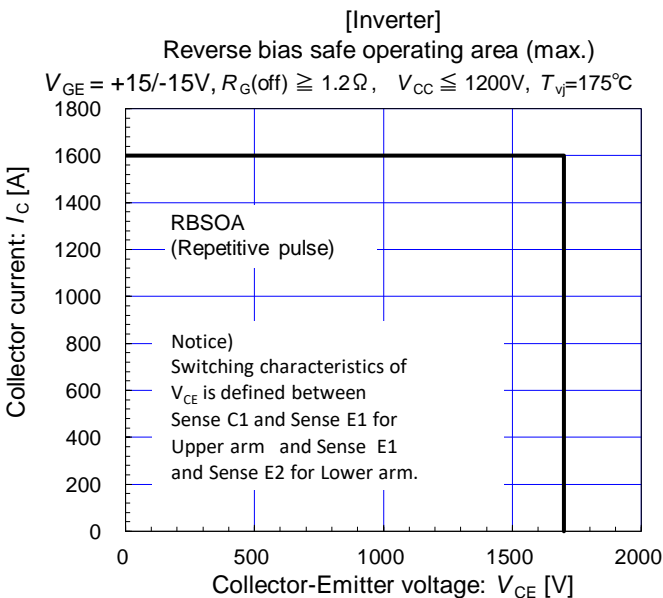
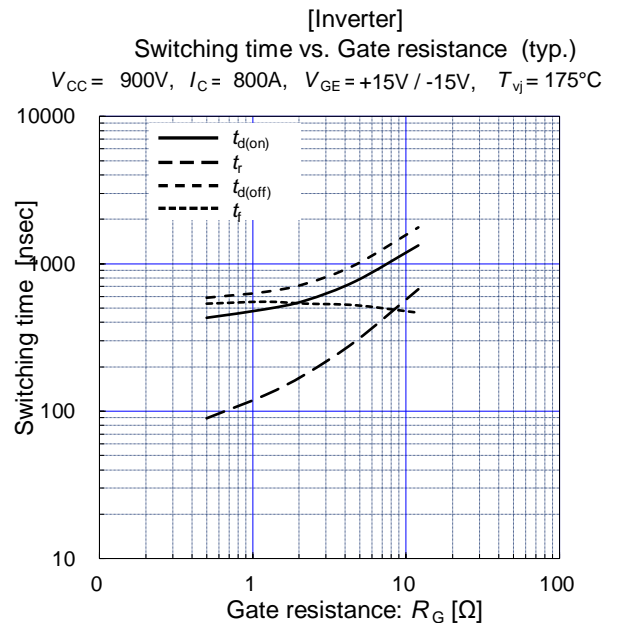
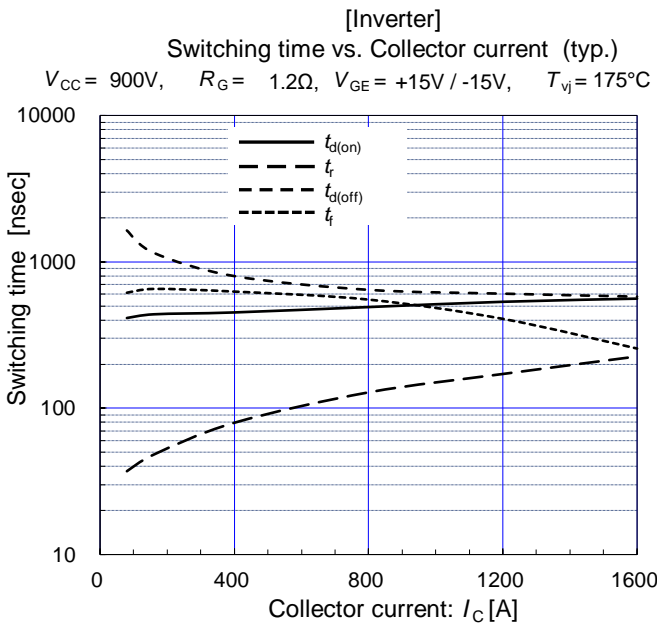
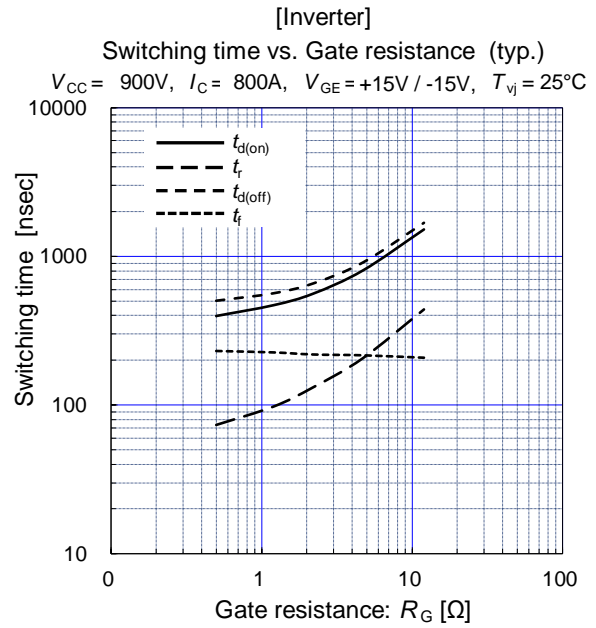
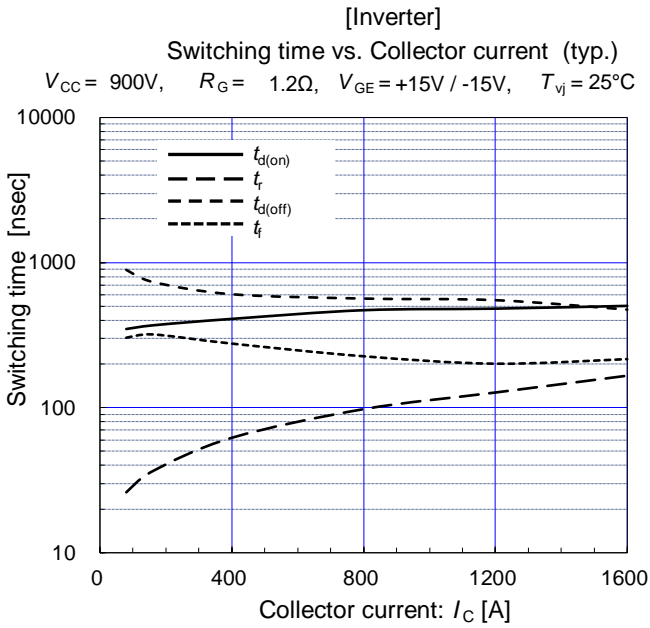


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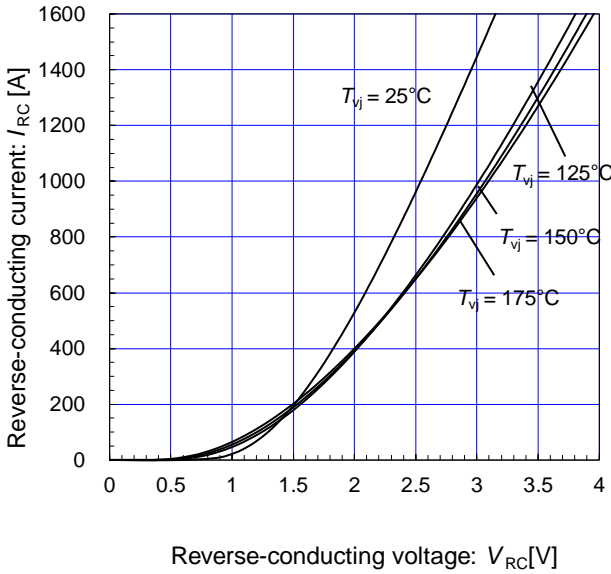
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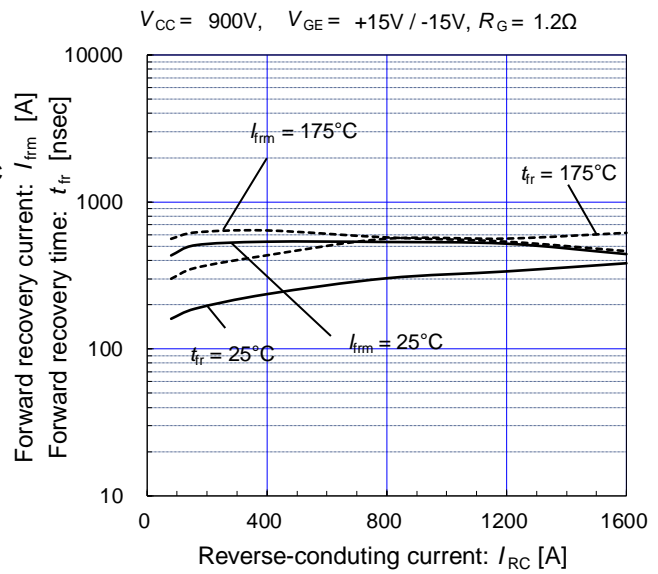
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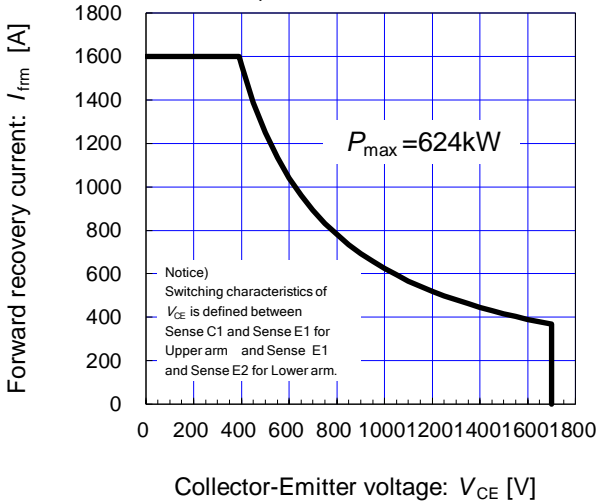
[Inverter]
Reverse-conducting current vs. Reverse-conducting voltage (typ.) chip



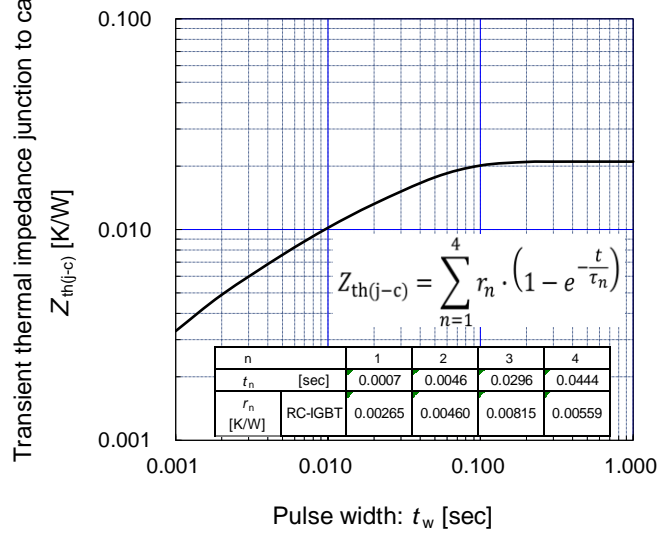
[Inverter]
Forward recovery characteristics (typ.)



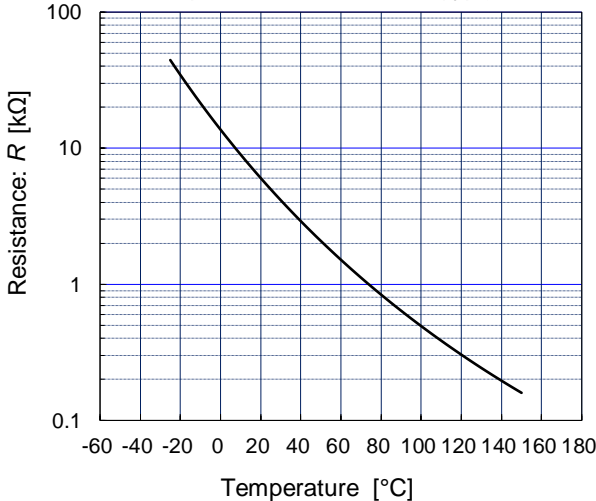
[Inverter]
FWD safe operation area (typ.)
 $T_{vj} = 175^\circ\text{C}$, $V_{CC} \leq 1200\text{V}$



[Inverter]
Transient thermal impedance junction to case



[Thermistor]
Temperature characteristic (typ.)



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IGBT Modules

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