



#### **ULTRA LOW CAPACITANCE ESD PROTECTION**

Voltage

5 V

#### **Features**

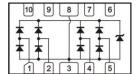
- IEC61000-4-2(ESD): ± 20 kV Air, ± 15 kV Contact
- IEC61000-4-4(EFT): 40 A(5/50 ns)
- IEC61000-4-5(Lightning): 5 A(8/20 uS)
- Low leakage current, maximum 1uA at rated voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard
- AEC-Q101 qualified

#### **Mechanical Data**

- Case: Molded plastic, DFN2510-10L
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0002 ounces, 0.005 grams

#### DFN2510-10L





## **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub> = 25 °C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
ESD IEC61000-4-2(Air)	V	±20	kV
ESD IEC61000-4-2(Contact)	V <sub>ESD</sub>	±15	
Operating Junction Temperature Range	TJ	-55~150	°C
Storage Temperature Range	T <sub>STG</sub>	-55~150	°C





## **Electrical Characteristics** (T<sub>A</sub> = 25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Reverse Stand-Off Voltage	V <sub>RWM</sub> <sup>(1)</sup>	-	-	-	5.5	V	
Reverse Breakdown Voltage	$V_{BR}$	I <sub>BR</sub> = 1 mA, any I/O to GND	6.5	-	11	V	
Reverse Leakage Current	I <sub>R</sub>	V <sub>R</sub> = 5.0 V	-	-	0.1	uA	
Clamping Voltage	V <sub>CL</sub>	$I_{PP} = 1 \text{ A}, t_P = 8/20 \text{ us},$ any I/O to GND	-	-	12	V	
		$I_{PP}$ = 5 A, $t_P$ = 8/20 us, any I/O to GND	-	-	20		
Clamping Voltage TLP	V <sub>CL</sub> <sup>(2)</sup>	$I_{PP} = 8 \text{ A}, t_P = 100 \text{ ns}$	-	17.1	-	V	
		$I_{PP} = 16 \text{ A}, t_P = 100 \text{ ns}$	-	21.4	-		
Dynamic Resistance	$R_{DYN}$	t <sub>P</sub> = 100 ns	-	0.53	-	Ω	
Off State Junction Capacitance	CJ	0Vdc Bias f = 1 MHz, Between any I/O to GND	-	-	0.8	pF	
		0Vdc Bias f = 1 MHz, Between any I/O pins	-	-	0.4		

#### NOTES:

- 1. A transient suppressor is selected according to the working peak reverse voltage(V<sub>RWM</sub>), which should be equal to or greater than the DC or continuous peak operation voltage level.
- 2. Testing using Transmission Line Pulse (TLP) conditions: Z0 =  $50\Omega$  ,  $t_P$  = 100 ns.





#### **TYPICAL CHARACTERISTIC CURVES**

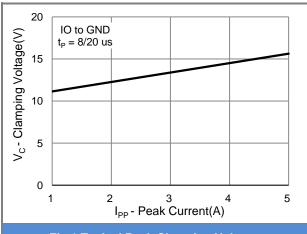


Fig.1 Typical Peak Clamping Voltage

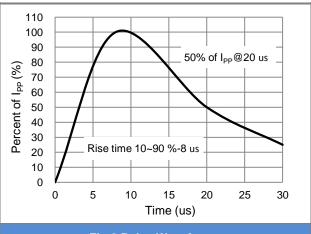


Fig.2 Pulse Waveform

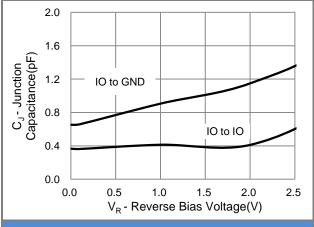


Fig.3 Typical Junction Capacitance

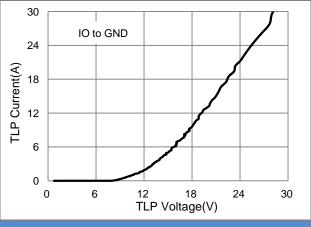


Fig.4 TLP Measurement

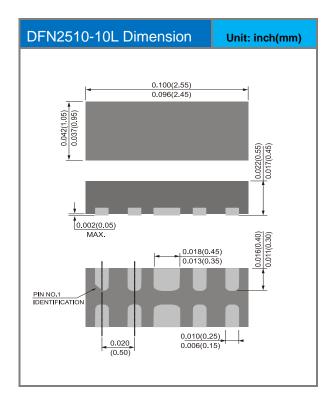


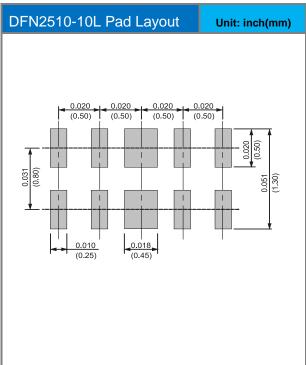


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
PE1805M4Q-AU_R1_000A1	DFN2510-10L	5K / 7" Reel	5M4Q	Halogen Free

### **Packaging Information & Mounting Pad Layout**









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