HTDISPLAY

SPECIFICATIONS

Version: V0
This module uses ROHS material

PRODUCT: **TFT LCD MODULE**

MODEL NO: **HT0700EI02AC1**

SUPPLIER: **HTDisplay**

ISSUED DATE: **2019-10-17**

■ **Preliminary Specification**
☐ **Final Product Specification**

HT display		Customer
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Revision Record

REV No.	REV Date	Contents	Editor	Remarks
V0	2019-10-17	First release	Yuan he	Preliminary

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HTDISPLAY

1. General information

Feature	Spec	Unit
LCD size	7.0	inch
Resalution	1024RGB x 600 Dots	---
Pixel pitch (WxH)	0.0502 x0.1432	mm
Active area	154.2144(W) x 85.92(H)	mm
Viewing area	155.08x86.92	mm
Display Mode	IPS	---
LCM Outline(with TP) (WxHxT)	164.90(W) × 100.00 (H) × 4.57(T)	mm
With/Without TP	With CTP	---
Weight (g)	--	g
TFT Driver IC	Source:EK79001HN Gate:EK73215BCGA	---
TFT Interface	RGB 24bit	---
TFT Input voltage	3.3	V
TFT Power consumption	TBD	mW
Backlight Power consumption	TBD	mW
Number of simultaneous touches	5	---
Minimum touch area	--	mm
Finger touch pitch	--	mm
Sensor structure	Glass/DITO	---
CTP touch method	Finger	---
TP driver	GT911	---
TFT&TP Connector Type	FPC/ZIF	---

2. Mechanical drawing

NOTES:

1. Display Type: 7.0″ TFT
2. Display Mode: Transmissive
3. Viewing Direction: FREE
4. Operation Temperature: -20°C to +70°C
5. Storage Temperature: -30°C to +80°C
6. Contrast Ratio: 800:1
7. Luminance: 850cd/m²
8. Driver IC: Source:EK79001HN+Gate:EK73215BCGA
9. ROHS Compliant

Unmarked Tolerance: ≤20mm,±0.05mm
≥20mm,±0.3mm

CTP Note:
1. COVER PET+ITO GLASS+FPC
   IC:GT911
2. Operating Voltage: 1.8V-3.3 V
3. Transmittance: ≥86%
4. Hardness: ≥3H

CIRCUIT DIAGRAM

Backlight LED Circuit
LED IF=200mA VF=18V
BACKLIGHT: 30 CHIP-WHITE

43.59±0.5
100.00±0.3 BL
99.50±0.1 LENS OD
0.26±0.2
86.92±0.1 LENS V.A
4.25
85.92 CTP A.A
4.78
85.92 LCD A.A

Silk screen black

7.0″TFT+CTP
1024RGB*600dots
View direction：
FREE

81.31±0.5
164.40±0.3 BL
164.90±0.3 BL
155.08±0.15 LENS OD
154.08 CTP A.A
154.2144 LCD A.A

0.11±0.2
6.23
6.75

Pull tape

FPC+PI
T=0.3±0.03

P/N	NAME
1	LED-A
2	LED-A
3	LED-K
4	LED-K
5	GND
6	VCOM
7	VDD
8	MODE
9	DE
10	VS
11	HS
12-19	B7-B0
20-27	G7-G0
28-35	R7-R0
36	GND
37	DCLK
38	GND
39	L/R
40	U/D
41	VGH
42	VGL
43	AVDD
44	RESET
45	VCOM
46	DITHB
47	GND
48	GND
49	NC
50	NC

1	INT
2	SDA
3	SCL
4	RESET
5	GND
6	VDD

REV.	CHANGE CONTENT	BY	DATE
A	FIRST ISSUE	WYJ	19.7.11
B			

3. Absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Supply voltage for logic	VDD	-0.3	3.6	V
Input voltage	VIN	-0.3	VDD+ 0.3	V
Operating temperature	TOP	-20	70	°C
Storage temperature	TST	-30	80	°C
Humidity	RH	--	90%(Max60 °C)	RH

4. Electrical characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply voltage for logic	VDD	2.3	3.3	3.6	V
Input Current	Idd	--	TBD	TBD	mA
Input voltage 'H' level	VIH	0.7VDD	--	VDD	V
Input voltage 'L' level	VIL	VSS	--	0.3VDD	V
Output voltage 'H' level	VOH	0.8VDD	--	VDD	V
Output voltage 'L' level	VOL	VSS		0.2VDD	V

5. Backlight characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward Current	If	--	200	---	mA	
Forward Voltage	Vf	17.5	18	18.8	V	
Luminous Intensity	--	--	850	--	Cd/m2	
LED Life Time	L_L	30000	--	--	Hrs	Ta=25°C
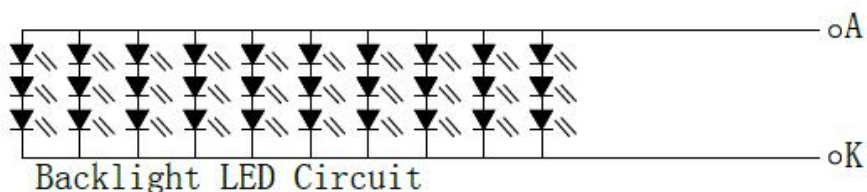
Figure   2

Note1: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note2: Optical performance should be evaluated at Ta=25℃. if LED is driven by high current, high ambient temperature & Humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

6. Electro-optical characteristics

Optical Specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Transmittance (with Polarizer)		T(%)	Θ=0 Normal Viewing angle	4.8	5.0	—	%	Measuring with normal polarizer,
Transmittance (without Polarizer)		T(%)		—	—	—	%	
Contrast Ratio		CR		—	800	—	—	(1)(2)
Response Time		$T_{ON}+T_{OFF}$		—	30	40	msec	(1)(3)
Color Gamut	(%)			—	50	—	%	C-light
Color Chromaticity (CIE1931)	White	W_x		—	(0.308)	—	—	(1)(4) CF glass C-light
		W_Y		—	(0.336)	—		
	Red	R_x		—	(0.599)	—	—	
		R_Y		—	(0.338)	—		
	Green	G_x		—	(0.299)	—	—	
		G_Y		—	(0.550)	—		
	Blue	B_x		—	(0.139)	—	—	
		B_Y		—	(0.131)	—		
Viewing Angle	Hor.	Θ_L	CR>10	—	85	—	—	(1)(4) Measuring with normal polarizer, Reference Only
		Θ_R		—	85	—		
	Ver.	Θ_U		—	85	—		
		Θ_D		—	85	—		
Optimal View Direction			Free					

Note(1) Definition of Viewing Angle:

6' o' clock
Φ=270°
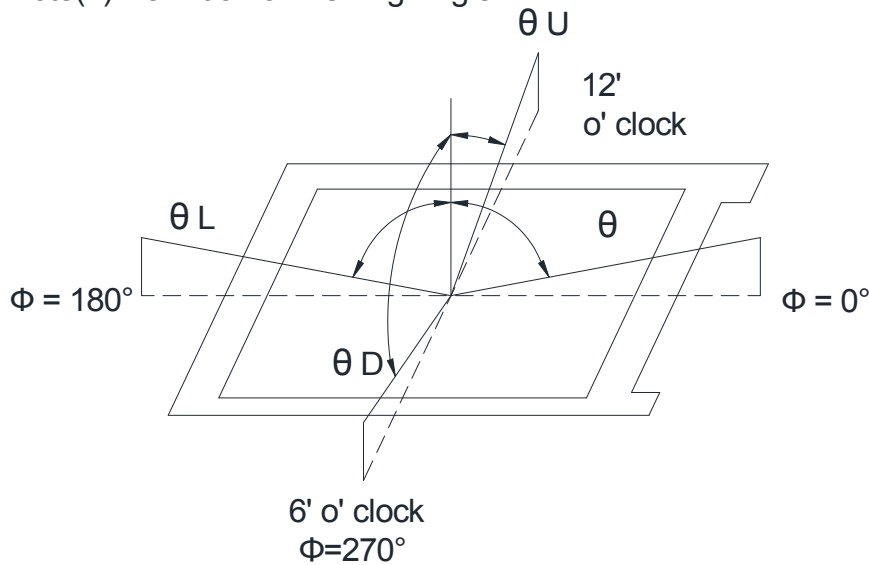
Note(2) Definition of Contrast Ratio(CR):
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note(3): Definition of Response time: Sum of T_{ON} and T_{OFF}

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.
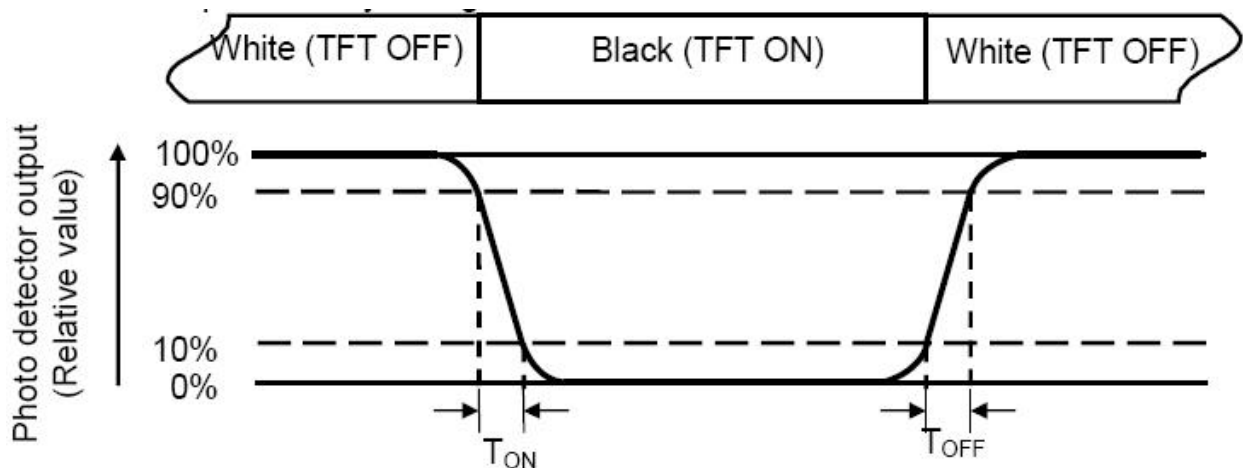
Note (4): Definition of color chromaticity (CIE1931)Color coordinates measured at center point of LCD.

7. Read/Write timing
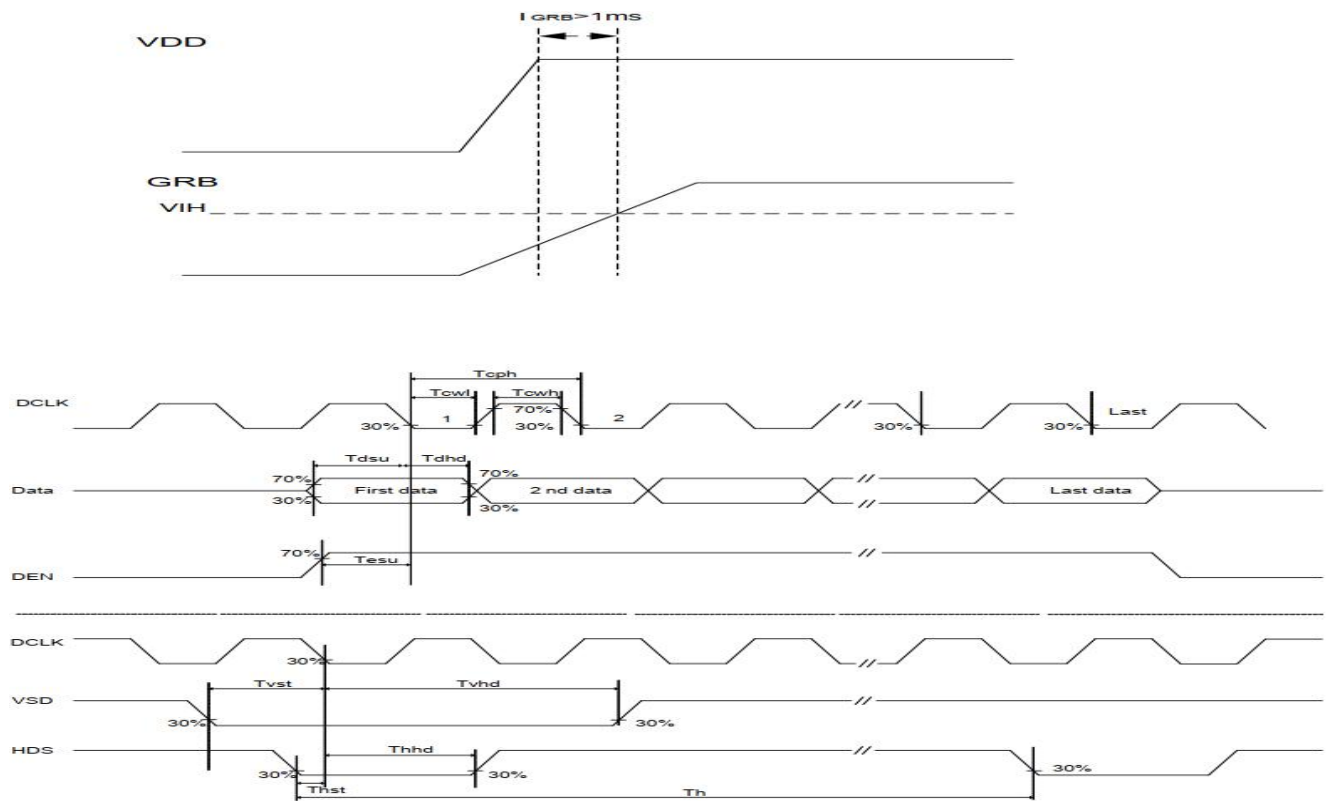
Parallel Input Clock and Data timing

(TA = -20 to 85°C, VDD = 2.3 to 3.6V, AVDD = 8   to 13.5V, GND = AGND = 0V)

TTL mode

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
VDD Power On Slew rate	TPOR	From 0V to 90% VDD	1	-	20	ms
RST pulse width	TRST	DCLK = 65MHz	50	-	-	us
DCLK cycle time	Tcph	-	14	-	-	ns
DCLK pulse duty	Tcwh	-	40	50	60	%
VSD setup time	Tvst	-	5	-	-	ns
VSD hold time	Tvhd	-	5	-	-	ns
HSD setup time	Thst	-	5	-	-	ns
HSD hold time	Thhd	-	5	-	-	ns
Data set-up time	Tdsu	D0[7:0], D1[7:0], D2[7:0] to DCLK	5	-	-	ns
Data hold time	Tdhd	D0[7:0], D1[7:0], D2[7:0] to DCLK	5	-	-	ns
DE setup time	Tesu	-	5	-	-	ns
DE hold time	Tehd	-	5	-	-	ns
Output stable time	Tsst	Dual gate	-	-	3	us

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Parallel 24-bit RGB input Timing

DE mode

DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd		1024		DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd		600		H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

HV mode(1)

HV mode
Horizontal input timing

Parameter		Symbol	Value			Unit
Horizontal display area		thd		1024		DCLK
DCLK frequency@ Frame rate=60hz		fclk	Min.	Typ.	Max.	
			44.9	51.2	63	Mhz
1 Horizontal Line		th	1200	1344	1400	
HSYNC pulse width	Min.	thpw		1		DCLK
	Typ.			—		
	Max.			140		
HSYNC back porch		thbp	160	160	160	
HSYNC front porch		thfp	16	160	216	

HV mode(2)

Vertical input timing

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd		600		H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tvpw	1	—	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

8. Interface description
8.1 TFT interface

No.	SYMBOL	I/O	Description
1-2	VLED+(A)		LED power anode.
3-4	VLED-(K)		LED power cathode.
5	GND		Ground for digital circuits.
6	VCOM		A power supply for the TFT-LCD common electrode.
7	VDD		Analog supply voltage range VCI to AVSS: 3.3V.
8	MODE		DE/SYNC mode select. Normally pull high.
9	DE		Display enable pin from controller.
10	VSYNC		Frame synchronization signal.
11	HSYNC		Line synchronization signal.
12-19	B7~B0		Graphic Data Input Pins.
20-27	G7~G0		Graphic Data Input Pins.
28-35	R7~R0		Graphic Data Input Pins.
36	GND		Ground for digital circuits.
37	DCLK		Dot-clock signal and oscillator source.
38	GND		Ground for digital circuits.
39	L/R		Left / right selection
40	U/D		Up/down selection
41	VGH		A positive power output pin for gate driver. **VGH = 18V.**
42	VGL		A negative power output pin for gate driver. **VGL = -6V.**
43	AVDD		Power pad for analog circuit. **AVDD = 9.6V.**
44	RESET		This signal will reset the device and it must be applied to properly.
45	NC		-
46	VCOM		A power supply for the TFT-LCD common electrode. **VCOM = 3.2V.**
47	DITHB		Dithering function enable control, normally pull high.
48	GND		Ground for digital circuits.
49-50	NC		-

8.2 CTP interface

No.	SYMBOL	I/O	Description
1	INT	O	Interrupt pin
2	SDA	IO	I2C data pin
3	SCL	I	I2C clock input pin
4	RESET	I	Reset pin for touch panel
5	GND	P	Ground
6	VDD	P	Supply voltage for touch panel

9. Reliability test conditions

No.	Test Item	Test condition	Remark
1	High Temperature Storage	80°C±2°C 240H	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Storage	-30°C±2°C 240H	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Operation	70°C±3°C 240H	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Operation	-20°C±3°C 240H	IEC60068-2-1:2007 GB2423.1-2008
5	High Temperature /Humidity Storage	60°C 90%RH 240H	IEC60068-2-78:2007 GB2423.3-2006
6	Temperature Cycle	-30℃/30min←→80℃/30min For a total 100 cycles. Start with cold temperature and end with high temperature.	IEC60068-2-14:1984 GB2423.22-2002
7	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2hours for each direction of X,Y,Z. Component handle as note1	IEC60068-2-6:1982 GB/T2423.10 -1995
8	Mechnical shock	100G ±X,±Y,±Z 3times for each direction	IEC60068-2-27:1987 GB/T2423.5 -1995
9	Packing vibration test	$0.015G^2$/Hz from 50~200Hz -6dB/Octave from 200~500Hz 2hours for each direction of X ,Y,Z	IEC60068-2-34 GB/T2423.11
10	Dropping test	Height::60cm 1conner ,3edges,6surfaces	IEC60068-2-32:1990 GB/T2423.8 -1995
11	ESD test	±2kv,human body mode 100pF/1500Ω	IEC61000-4-2:2001 GB/T17626.2 - 2006

Note1:
The component placed on a vibrating platform as it is assembled in the machine, wires included, is subjected to sinusoidal vibration in all directions XYZ
Note2:
After completing the reliability test, leave the samples under the room temperature and f

or the following inspection items:

1. No clearly visible defects or deterioration of display quality allowed.
2. No function-related abnormalities.
3. Connected parts still connecting tightly.
4. Display characteristics fulfill initial value contrast ratio should be an least 30% of initial value.

10. Storage and use precautions

**When storing and using the LCD modules, the following precaution are necessary：**

10.1 Store them in a sealed polyethylene bag. If properly sealed, there is no need for the desiccant.

10.2 Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.

10.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.4 The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).

10.5 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

10.6 Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

10.7 If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be gained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

10.8 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.9 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.10 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.11 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.12 If the display surface is contaminated, gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
— Isopropyl alcohol
— Ethyl alcohol
Solvents other than those mentioned above may damage the polarizer.
Especially, do not use the following:
— Water
— Ketone
— Aromatic solvents

10.13 Do not attempt to disassemble the LCD Module.

10.14 If the logic circuit power is off, do not apply the input signals.

10.15 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
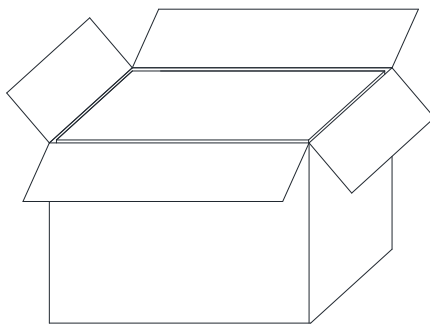
--Be sure to ground the body when handling the LCD Modules.

–Tools required for assembly, such as soldering irons, must be properly ground.

–To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions

--The LCD Module is coated with a film to protect the display surface. -

--Be care when peeling off this protective film since static electricity may be generated.

--Exposed area of the printed circuit board.
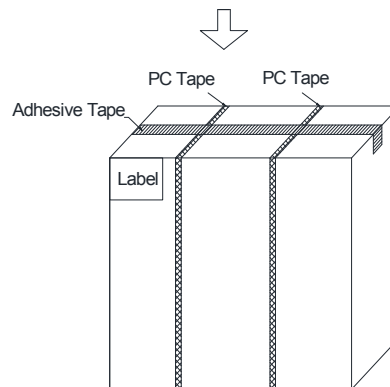
--Terminal electrode sections

11. Packing
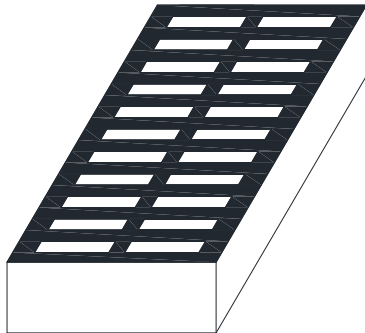
(1) Place the module into the pearl cotton tray.

(2) Place the pear cotton tray into the carton.

(3) Wrap the carton well.