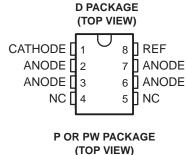
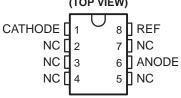
- **Equivalent Full-Range Temperature** Coefficient . . . 30 ppm/°C
- 0.2-Ω Typical Output Impedance
- Sink-Current Capability . . . 1 mA to 100 mA
- **Low Output Noise**
- Adjustable Output Voltage . . . V_{ref} to 36 V
- Available in a Wide Range of High-Density **Packages**

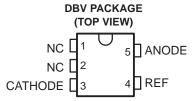
description

The TL431 and TL431A are three-terminal adjustable shunt regulators with specified thermal stability over applicable automotive, commercial, and military temperature ranges. The output voltage can be set to any value between V_{ref} (approximately 2.5 V) and 36 V, with two external resistors (see Figure 17). These devices have a typical output impedance of 0.2 Ω . Active output circuitry provides a very sharp turn-on characteristic, making these devices excellent replacements for Zener diodes in many applications, such as onboard regulation, adjustable power supplies, and switching power supplies.

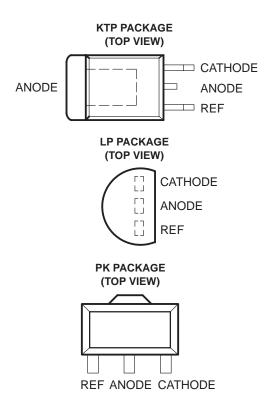
The TL431C and TL431AC are characterized for operation from 0°C to 70°C, and the TL431I and TL431Al are characterized for operation from -40°C to 85°C.







NC - No internal connection





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

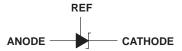


AVAILABLE OPTIONS

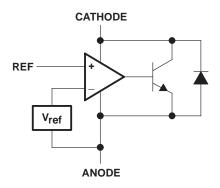
		PACKAGED DEVICES								
TA	T _A SMALL- OUTLINE (D) SOT-23 (DBV)		PLASTIC FLANGE MOUNT (KTP)	TO-226AA (LP)	PLASTIC DIP (P)	PLASTIC SHRINK SMALL-OUTLINE (PW)	SOT-89 (PK)			
0°C to 70°C	TL431CD TL431ACD	TL431CDBVR	TL431CKTPR	TL431CLP TL431ACLP	TL431CP TL431ACP	TL431CPWR TL431ACPWR	TL431CPKR			
-40°C to 85°C	TL431ID TL431AID			TL431ILP TL431AILP	TL431IP TL431AIP		TL431IPKR			

The D, LP, and PW packages are available taped and reeled. The DBV, KTP, and PK packages are only available taped and reeled. Add the suffix R to device type (e.g., TL431CDR).

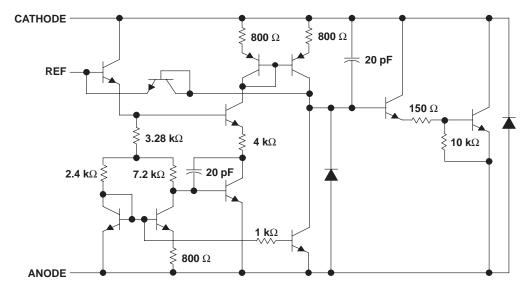
symbol



functional block diagram



equivalent schematic†



† All component values are nominal.



TL431, TL431A ADJUSTABLE PRECISION SHUNT REGULATORS

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Cathode voltage, V _{KA} (see Note 1)		37 V
Continuous cathode current range, I _{KA}		00 mA to 150 mA
Reference input current range		$-50 \mu\text{A}$ to 10 mA
Package thermal impedance, θ_{JA} (see Notes 2 and 3):	D package	97°C/W
,	DBV package	
	KTP package	28°C/W
	LP package	156°C/W
	P package	
	PK package	52°C/W
	PW package	149°C/W
Lead temperature 1,6 mm (1/16 inch) from case for 10	seconds	260°C
Storage temperature range, T _{stg}		65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. Voltage values are with respect to the anode terminal unless otherwise noted.

- 2. Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability.
- 3. The package thermal impedance is calculated in accordance with JESD 51-7.

recommended operating conditions

		MIN	MAX	UNIT
Cathode voltage, V _{KA}		V _{ref}	36	V
Cathode current, I _{KA}		1	100	mA
Operating free air temperature range. To	TL431C, TL431AC	0	70	°C
Operating free-air temperature range, T _A	TL431I, TL431AI	-40	85	C



electrical characteristics over recommended operating conditions, T_A = 25°C (unless otherwise noted)

PARAMETER		TEST	TEST C	CONDITIONS		TL431C		UNIT
	PARAMETER	CIRCUIT	TEST CONDITIONS		MIN	TYP	MAX	UNIT
V _{ref}	Reference voltage	2	$V_{KA} = V_{ref}$	$I_{KA} = 10 \text{ mA}$	2440	2495	2550	mV
V _{I(dev)}	Deviation of reference voltage over full temperature range (see Figure 1)	2	$V_{KA} = V_{ref}$, $I_{KA} = T_A = 0$ °C to 70°C	10 mA,		4	25	mV
ΔV_{ref}	Ratio of change in reference voltage	3	I _{KA} = 10 mA	$\Delta V_{KA} = 10 V - V_{ref}$		-1.4	-2.7	mV
$\overline{\Delta V_{KA}}$	to the change in cathode voltage	3	IKA – IOIIIA	$\Delta V_{KA} = 36 \text{ V} - 10 \text{ V}$		-1	-2	$\frac{mV}{V}$
I _{ref}	Reference current	3	I _{KA} = 10 mA, R1 =	= 10 kΩ, R2 = ∞		2	4	μΑ
I _{I(dev)}	Deviation of reference current over full temperature range (see Figure 1)	3	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞, T_A = 0°C to 70°C			0.4	1.2	μА
I _{min}	Minimum cathode current for regulation	2	V _{KA} = V _{ref}			0.4	1	mA
l _{off}	Off-state cathode current	4	$V_{KA} = 36 V$,	V _{ref} = 0		0.1	1	μΑ
z _{KA}	Dynamic impedance (see Figure 1)	1	$I_{KA} = 1 \text{ mA to } 100$ $f \le 1 \text{ kHz}$	mA, $V_{KA} = V_{ref}$,		0.2	0.5	Ω

The deviation parameters V_{ref(dev)} and I_{ref(dev)} are defined as the differences between the maximum and minimum values obtained over the recommended temperature range. The average full-range temperature coefficient of the reference voltage, α_{Vref} , is defined as:

$$\left|\alpha_{Vref}\right| \left(\frac{ppm}{^{\circ}C}\right) = \frac{\left(\frac{V_{\text{I(dev)}}}{V_{ref} \text{ at } 25^{\circ}C}\right) \times 10^{6}}{\Delta T_{A}} \qquad \text{Minimum } V_{ref} \qquad \qquad V_{\text{I(dev)}} \qquad V_{\text{I(dev)}}$$



where:

 ΔT_A is the recommended operating free-air temperature range of the device.

 α_{Vref} can be positive or negative, depending on whether minimum V_{ref} or maximum V_{ref} , respectively, occurs at the lower temperature.

Example: maximum V_{ref} = 2496 mV at 30°C, minimum V_{ref} = 2492 mV at 0°C, V_{ref} = 2495 mV at 25°C, $\Delta T_{\Delta} = 70^{\circ}C$ for TL431C

$$\left|\alpha_{\text{Vref}}\right| = \frac{\left(\frac{4 \text{ mV}}{2495 \text{ mV}}\right) \times 10^6}{70^{\circ}\text{C}} \approx 23 \text{ ppm/}^{\circ}\text{C}$$

Because minimum V_{ref} occurs at the lower temperature, the coefficient is positive.

Calculating Dynamic Impedance

Calculating Dynamic impedance The dynamic impedance is defined as: $\left|z_{KA}\right| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$

When the device is operating with two external resistors (see Figure 3), the total dynamic impedance of the circuit is given by:

$$|z'| = \frac{\Delta V}{\Delta I} \approx |z_{KA}| \left(1 + \frac{R1}{R2}\right)$$

Figure 1. Calculating Deviation Parameters and Dynamic Impedance



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electrical characteristics over recommended operating conditions, $T_A = 25^{\circ}C$ (unless otherwise noted)

	PARAMETER		TEST C	CONDITIONS	TL431I			UNIT
	PARAMETER	CIRCUIT	TEST CONDITIONS		MIN	TYP	MAX	UNIT
V _{ref}	Reference voltage	2	$V_{KA} = V_{ref}$	$I_{KA} = 10 \text{ mA}$	2440	2495	2550	mV
V _I (dev)	Deviation of reference voltage over full temperature range (see Figure 1)	2	V _{KA} = V _{ref} , I _{KA} = 10 mA, T _A = -40°C to 85°C			5	50	mV
ΔV_{ref}	Ratio of change in reference voltage	3	lica = 10 mA	$\Delta V_{KA} = 10 \text{ V} - V_{ref}$		-1.4	-2.7	<u>mV</u>
ΔV_{KA}	to the change in cathode voltage	3	$I_{KA} = 10 \text{ mA}$	$\Delta V_{KA} = 36 \text{ V} - 10 \text{ V}$		-1	-2	V
I _{ref}	Reference current	3	I _{KA} = 10 mA, R1 =	= 10 kΩ, R2 = ∞		2	4	μΑ
I(dev)	Deviation of reference current over full temperature range (see Figure 1)	3	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞, T_{A} = -40°C to 85°C			0.8	2.5	μΑ
I _{min}	Minimum cathode current for regulation	2	V _{KA} = V _{ref}			0.4	1	mA
l _{off}	Off-state cathode current	4	V _{KA} = 36 V,	V _{ref} = 0		0.1	1	μΑ
z _{KA}	Dynamic impedance (see Figure 1)	2	$I_{KA} = 1 \text{ mA to } 100$ $f \le 1 \text{ kHz}$	mA, $V_{KA} = V_{ref}$,		0.2	0.5	Ω

electrical characteristics over recommended operating conditions, T_A = 25°C (unless otherwise noted)

	PARAMETER		TEST CONDITIONS		TL431AC			UNIT
	PARAMETER	CIRCUIT	TEST CONDITIONS		MIN	TYP	MAX	UNIT
V _{ref}	Reference voltage	2	$V_{KA} = V_{ref}$	$I_{KA} = 10 \text{ mA}$	2470	2495	2520	mV
V _{I(dev)}	Deviation of reference voltage over full temperature range (see Figure 1)	2	$V_{KA} = V_{ref}$, $I_{KA} = 10$ mA, $T_A = 0$ °C to 70°C			4	25	mV
ΔV_{ref}	Ratio of change in reference voltage	3	lica = 10 mA	$\Delta V_{KA} = 10 V - V_{ref}$		-1.4	-2.7	mV
ΔV_{KA}	to the change in cathode voltage	3	$I_{KA} = 10 \text{ mA}$ $\Delta V_{KA} = 36 \text{ V} - 10$			-1	-2	V
I _{ref}	Reference current	3	I _{KA} = 10 mA, R1 =	= 10 kΩ, R2 = ∞		2	4	μΑ
l(dev)	Deviation of reference current over full temperature range (see Figure 1)	3	I _{KA} = 10 mA, R1 = T _A = 0°C to 70°C	= 10 kΩ, R2 = ∞,		0.8	1.2	μΑ
I _{min}	Minimum cathode current for regulation	2	V _{KA} = V _{ref}			0.4	0.6	mA
l _{off}	Off-state cathode current	4	$V_{KA} = 36 V$,	V _{ref} = 0		0.1	0.5	μΑ
z _{KA}	Dynamic impedance (see Figure 1)	1	$I_{KA} = 1 \text{ mA to } 100$ $f \le 1 \text{ kHz}$	mA , $V_{KA} = V_{ref}$,		0.2	0.5	Ω

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electrical characteristics over recommended operating conditions, T_A = 25°C (unless otherwise noted)

DADAMETED		TEST	TEST COMPITIONS		TL431AI				
	PARAMETER	CIRCUIT	TEST CONDITIONS		MIN	TYP	MAX	UNIT	
V _{ref}	Reference voltage	2	$V_{KA} = V_{ref}$	I _{KA} = 10 mA	2470	2495	2520	mV	
V _{I(dev)}	Deviation of reference voltage over full temperature range (see Figure 1)	2	$V_{KA} = V_{ref}$, $I_{KA} = T_A = -40$ °C to 85°C	10 mA, C		5	50	mV	
ΔV_{ref}	Ratio of change in reference voltage	3	l = 10 mA	$\Delta V_{KA} = 10 V - V_{ref}$		-1.4	-2.7	mV	
ΔV_{KA}	to the change in cathode voltage	3	I _{KA} = 10 mA	$\Delta V_{KA} = 36 \text{ V} - 10 \text{ V}$		-1	-2	<u>mV</u> V	
I _{ref}	Reference current	3	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞			2	4	μΑ	
I _{I(dev)}	Deviation of reference current over full temperature range (see Figure 1)	3	I_{KA} = 10 mA, R1 = 10 kΩ, R2 = ∞, T_{A} = -40°C to 85°C			0.8	2.5	μΑ	
I _{min}	Minimum cathode current for regulation	2	V _{KA} = V _{ref}			0.4	0.7	mA	
l _{off}	Off-state cathode current	4	V _{KA} = 36 V,	V _{ref} = 0		0.1	0.5	μΑ	
z _K A	Dynamic impedance (see Figure 1)	2	$I_{KA} = 1 \text{ mA to } 100$ $f \le 1 \text{ kHz}$	mA , $V_{KA} = V_{ref}$,		0.2	0.5	Ω	

PARAMETER MEASUREMENT INFORMATION

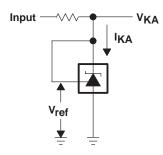


Figure 2. Test Circuit for $V_{KA} = V_{ref}$

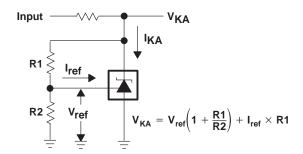


Figure 3. Test Circuit for $V_{KA} > V_{ref}$

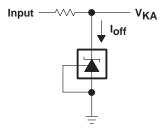


Figure 4. Test Circuit for Ioff

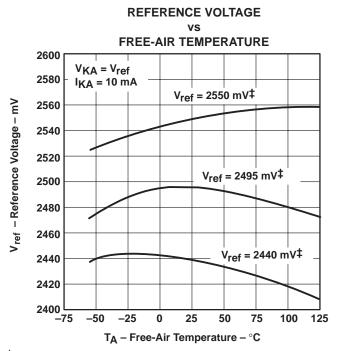
Table 1. Graphs

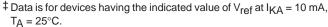
	FIGURE
Reference input voltage vs Free-air temperature	5
Reference input current vs Free-air temperature	6
Cathode current vs Cathode voltage	7, 8
Off-state cathode current vs Free-air temperature	9
Ratio of delta reference voltage to change in cathode voltage vs Free-air temperature	10
Equivalent input noise voltage vs Frequency	11
Equivalent input noise voltage over a 10-second period	12
Small-signal voltage amplification vs Frequency	13
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Stability boundary conditions	16

Table 2. Application Circuits

	FIGURE
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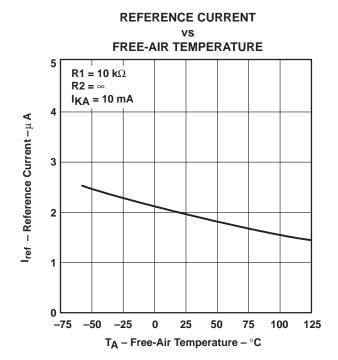
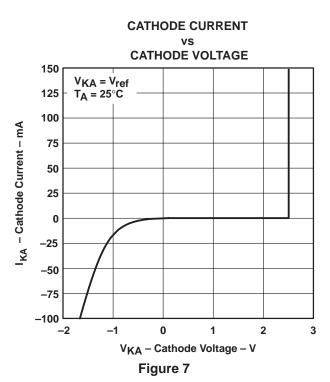
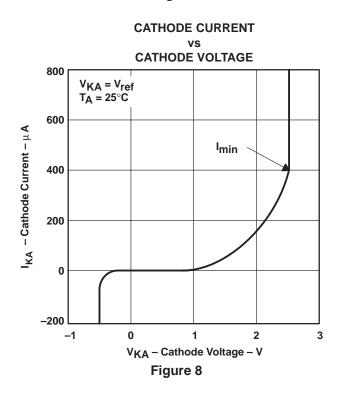


Figure 6



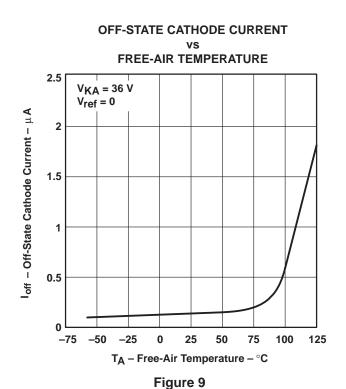




[†] Data at high and low temperatures are applicable only within the recommended operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS[†]



RATIO OF DELTA REFERENCE VOLTAGE TO DELTA CATHODE VOLTAGE

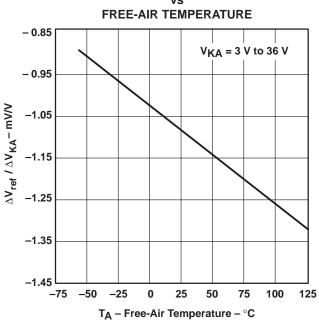
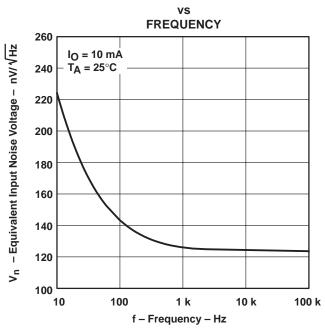


Figure 10

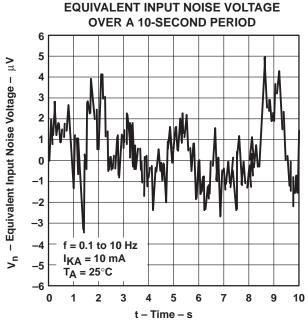
EQUIVALENT INPUT NOISE VOLTAGE



† Data at high and low temperatures are applicable only within the recommended operating free-air temperature ranges of the various devices.

Figure 11





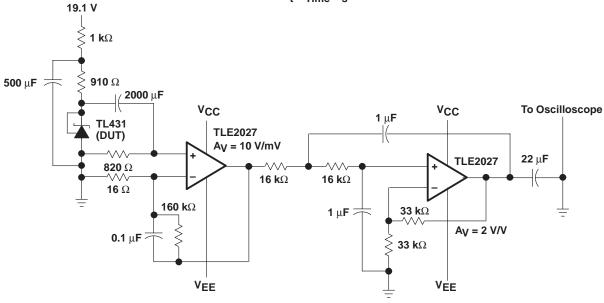
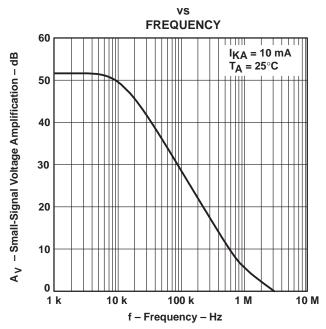
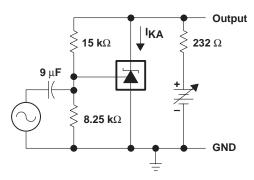


Figure 12. Test Circuit for Equivalent Input Noise Voltage

SMALL-SIGNAL VOLTAGE AMPLIFICATION

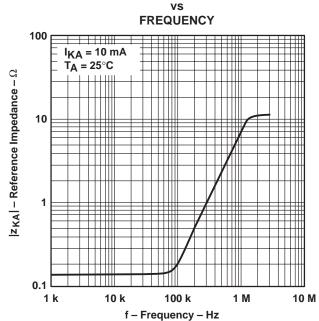


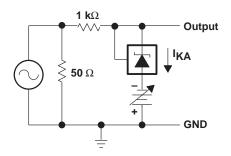


TEST CIRCUIT FOR VOLTAGE AMPLIFICATION

Figure 13

REFERENCE IMPEDANCE





TEST CIRCUIT FOR REFERENCE IMPEDANCE

Figure 14



PULSE RESPONSE T_A = 25°C Input 5 Input and Output Voltage - V 3 Output 2 1 -1 0 1 2 3 5 6 $\textbf{t-Time}-\mu\textbf{s}$

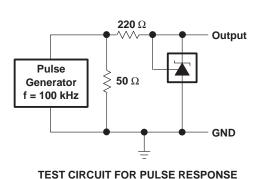
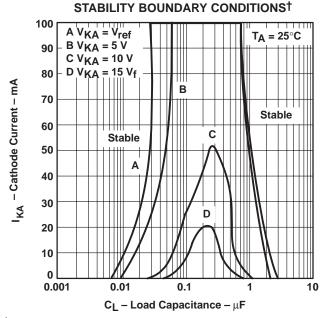
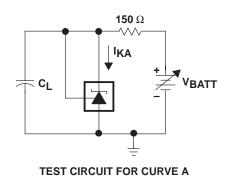
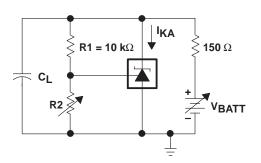


Figure 15



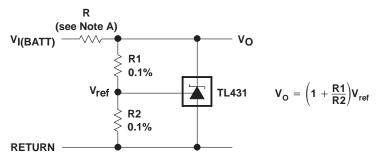
 $^{^\}dagger$ The areas under the curves represent conditions that may cause the device to oscillate. For curves B, C, and D, R2 and V+ were adjusted to establish the initial V_{KA} and I_{KA} conditions with C_L = 0. V_{BATT} and C_L then were adjusted to determine the ranges of stability.





TEST CIRCUIT FOR CURVES B, C, AND D

Figure 16



NOTE A: R should provide cathode current ≥1 mA to the TL431 at minimum V_{I(BATT)}.

Figure 17. Shunt Regulator

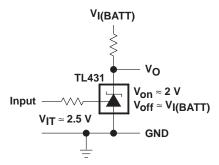
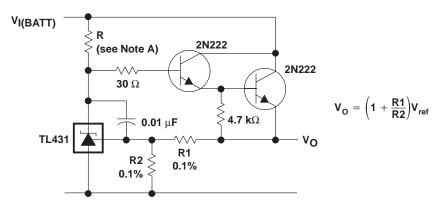


Figure 18. Single-Supply Comparator With Temperature-Compensated Threshold



NOTE A: R should provide cathode current ≥1 mA to the TL431 at minimum V_{I(BATT)}.

Figure 19. Precision High-Current Series Regulator

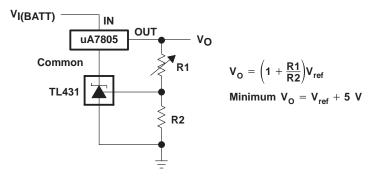


Figure 20. Output Control of a Three-Terminal Fixed Regulator

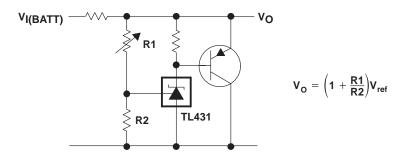
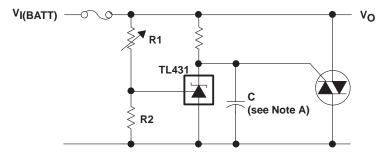


Figure 21. High-Current Shunt Regulator



NOTE A: Refer to the stability boundary conditions in Figure 16 to determine allowable values for C.

Figure 22. Crowbar Circuit

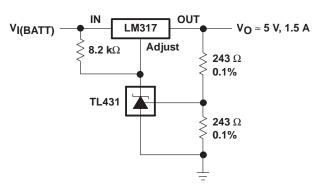
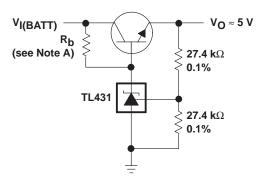


Figure 23. Precision 5-V 1.5-A Regulator



NOTE A: R_b should provide cathode current ≥ 1 mA to the TL431.

Figure 24. Efficient 5-V Precision Regulator

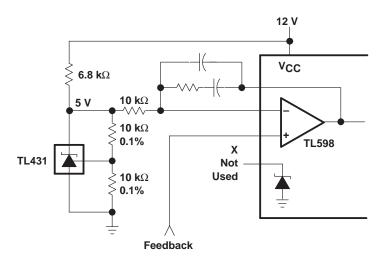
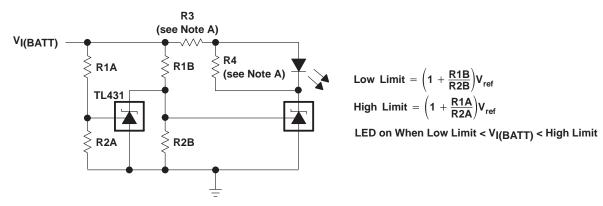


Figure 25. PWM Converter With Reference





NOTE A: R3 and R4 are selected to provide the desired LED intensity and cathode current ≥1 mA to the TL431 at the available V_{I(BATT)}.

Figure 26. Voltage Monitor

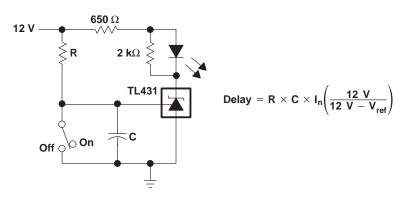


Figure 27. Delay Timer

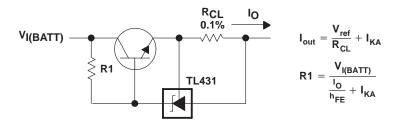


Figure 28. Precision Current Limiter

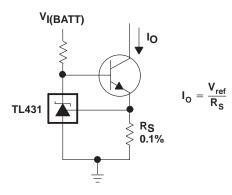


Figure 29. Precision Constant-Current Sink

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PRODUCT FOLDER | PRODUCT INFO: FEATURES | DESCRIPTION | DATASHEETS |
PRICING/AVAILABILITY | SAMPLES |
APPLICATION NOTES | BLOCK DIAGRAMS

PRODUCT SUPPORT: <u>APPLICATIONS</u>

TL431, Adjustable Precision Shunt Regulator

DEVICE STATUS: ACTIVE

PARAMETER NAME	TL431
Vref (V)	2.5
IZ (min) (uA)	1000
VO (max) (V)	36
Tolerance (%)	2
VI (max) (V)	36
Temp Coeff (typ) (ppm/ degree C)	30
IZ (max) (mA)	100

FEATURES Back to Top

- Equivalent Full-Range Temperature Coefficient...30 ppm/°C
- 0.2 Ω Typical Output Impedance
- Sink-Current Capability...1 mA to 100 mA
- Low Output Noise
- Adjustable Output Voltage...V_{ref} to 36 V
- Available in a Wide Range of High-Density Packages

DESCRIPTIONABack to Top

The TL431 and TL431A are three-terminal adjustable shunt regulators with specified thermal stability over applicable automotive, commercial, and military temperature ranges. The output voltage can be set to any value between V_{ref} (approximately 2.5 V) and 36 V, with two external resistors (see Figure 17). These devices have a typical output impedance of 0.2 Ω . Active output circuitry provides a very sharp turn-on characteristic, making these devices excellent replacements for Zener diodes in many applications, such as onboard regulation, adjustable power supplies, and switching power supplies.

The TL431C and TL431AC are characterized for operation from 0°C to 70°C, and the TL431I

and TL431AI are characterized for operation from -40°C to 85°C.

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APPLICATION NOTES

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• <u>PowerFLEX (TM) -- Surface-Mount Alternative For Through-Hole Power</u> Packages (SZZA015 - Updated: 04/08/1999)

BLOCK DIAGRAMS

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- Desktop PC
- Notebook PC

SAMPLES Back to Top

ORDERABLE DEVICE	<u>PACKAGE</u>	<u>PINS</u>	TEMP (°C)	<u>STATUS</u>	DSCC NUMBER	<u>SAMPLES</u>
TL431CLP	<u>LP</u>	3	0 TO 70	ACTIVE		Request Samples
TL431IPK	<u>PK</u>	3	-40 TO 85	ACTIVE		Request Samples

PRICING/AVAILABILITY Back to Top

				4		~		
ORDERABLE DEVICE	PACKAGE	<u>PINS</u>	TEMP (°C)	<u>STATUS</u>	BUDGETARY PRICE US\$/UNIT QTY=1000+	PACK OTY	DSCC NUMBER	PRICING/AVAILABILITY
TL431CD	<u>D</u>	8	0 TO 70	ACTIVE	0.27	75		Check stock or order
TL431CDBVR	<u>DBV</u>	5	0 TO 70	ACTIVE	0.30	3000		Check stock or order
TL431CDR	<u>D</u>	8	0 TO 70	ACTIVE	CTIVE 0.30 2500		Check stock or order	
TL431CKTPR	<u>KTP</u>	2	0 TO 70	ACTIVE	0.30	3000		Check stock or order
TL431CLP	<u>LP</u>	3	0 TO 70	ACTIVE	0.30	1000		Check stock or order
TL431CLPB- TDJ	<u>LP</u>	3	S	OBSOLETE				
TL431CLPM	<u>LP</u>	3	0 TO 70	ACTIVE	0.30	2000		Check stock or order
TL431CLPR	<u>LP</u>	3	0 TO 70	ACTIVE	0.33	2000		Check stock or order
TL431CP	<u>P</u>	8	0 TO 70	ACTIVE	0.27	50		Check stock or order
			0 TO					

3 01 3							
TL431CPK	<u>PK</u>	3	70	ACTIVE	0.52	1000	Check stock or order
TL431CPS	<u>PS</u>	8	0 TO 70	ACTIVE			Check stock or order
TL431CPSLE	<u>PS</u>	8		OBSOLETE			Replaced by TL431CPSR
TL431CPSR	<u>PS</u>	8	0 TO 70	ACTIVE	0.67	2000	Check stock or order
TL431CPWLE	<u>PW</u>	8		OBSOLETE			Replaced by TL431CPWR
TL431CPWR	<u>PW</u>	8	0 TO 70	ACTIVE	0.30	2000	Check stock or order
TL431ID	<u>D</u>	8	-40 TO 85	ACTIVE	0.42	75	Check stock or order
TL431IDBVR	<u>DBV</u>	5	-40 TO 85	ACTIVE	0.45	3000	Check stock or order
TL431IDR	<u>D</u>	8	-40 TO 85	ACTIVE	0.45	2500	Check stock or order
TL431ILP	<u>LP</u>	3	-40 TO 85	ACTIVE	0.42	1000	Check stock or order
TL431ILPM	<u>LP</u>	3	-40 TO 85	OBSOLETE			
TL431ILPR	<u>LP</u>	3	-40 TO 85	ACTIVE	0.45	2000	Check stock or order
TL431IP	<u>P</u>	8	-40 TO 85	ACTIVE	0.42	50	Check stock or order
TL431IPK	<u>PK</u>	3	-40 TO 85	ACTIVE	1.00	1000	Check stock or order
TL431MFKB	<u>FK</u>	20	-55 TO 125	OBSOLETE			
TL431MJG	<u>JG</u>	8	-55 TO 125	OBSOLETE			
TL431MJGB	<u>JG</u>	8	-55 TO 125	OBSOLETE			
TL431MJGB	<u>JG</u>	8		OBSOLETE			

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PRODUCT SUPPORT: <u>APPLICATIONS</u>

TL431A, Adjustable Precision Shunt Regulator

DEVICE STATUS: ACTIVE

PARAMETER NAME	TL431A
Vref (V)	2.5
IZ (min) (uA)	1000
VO (max) (V)	36
Tolerance (%)	1
VI (max) (V)	36
Temp Coeff (typ) (ppm/ degree C)	30
IZ (max) (mA)	100

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- Equivalent Full-Range Temperature Coefficient...30 ppm/°C
- 0.2 Ω Typical Output Impedance
- Sink-Current Capability...1 mA to 100 mA
- Low Output Noise
- Adjustable Output Voltage...V_{ref} to 36 V
- Available in a Wide Range of High-Density Packages

adjustable power supplies, and switching power supplies.

DESCRIPTIONABack to Top

The TL431 and TL431A are three-terminal adjustable shunt regulators with specified thermal stability over applicable automotive, commercial, and military temperature ranges. The output voltage can be set to any value between V_{ref} (approximately 2.5 V) and 36 V, with two external resistors (see Figure 17). These devices have a typical output impedance of 0.2 Ω . Active output circuitry provides a very sharp turn-on characteristic, making these devices excellent replacements for Zener diodes in many applications, such as onboard regulation,

The TL431C and TL431AC are characterized for operation from 0°C to 70°C, and the TL431I and TL431AI are characterized for operation from -40°C to 85°C.

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• PowerFLEX (TM) -- Surface-Mount Alternative For Through-Hole Power Packages (SZZA015 - Updated: 04/08/1999)

PRICING/AVAILABILITY

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ORDERABLE DEVICE	PACKAGE	<u>PINS</u>	TEMP (°C)	<u>STATUS</u>	BUDGETARY PRICE US\$/UNIT QTY=1000+	PACK QTY	PRICING/AVAILABILITY
TL431ACD	<u>D</u>	8	0 TO 70	ACTIVE	0.33	75	Check stock or order
TL431ACDR	<u>D</u>	8	0 TO 70	ACTIVE	0.33	2500	Check stock or order
TL431ACLP	<u>LP</u>	3	0 TO 70	ACTIVE	0.33	1000	Check stock or order
TL431ACLPM	<u>LP</u>	3	S	ACTIVE	0.37	2000	Check stock or order
TL431ACLPR	<u>LP</u>	3	0 TO 70	ACTIVE	0.37	2000	Check stock or order
TL431ACP	<u>P</u>	8	0 TO 70	ACTIVE	0.33	50	Check stock or order
TL431ACPS	<u>PS</u>	8	0 TO 70	ACTIVE			Check stock or order
TL431ACPW	<u>PW</u>	8	0 TO 70	OBSOLETE			
TL431ACPWR	<u>PW</u>	8	0 TO 70	ACTIVE	0.33	2000	Check stock or order
TL431AID	<u>D</u>	8	-40 TO 85	ACTIVE	0.42	75	Check stock or order
TL431AIDR	<u>D</u>	8	-40 TO 85	ACTIVE	0.45	2500	Check stock or order
TL431AILP	<u>LP</u>	3	-40 TO 85	ACTIVE	0.42	1000	Check stock or order
TL431AILPR	<u>LP</u>	3	-40 TO 85	ACTIVE	0.45	2000	Check stock or order
TL431AIP	<u>P</u>	8	-40 TO 85	ACTIVE	0.42	50	Check stock or order

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