

P90FG5R5SL

Power MOSFETs

55V, 90A, N-channel

Feature

- N-channel
- SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Pb free terminal
- RoHS:Yes

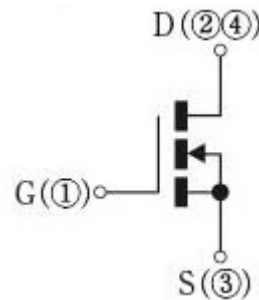
OUTLINE

Package (House Name): FG

Package (JEDEC Code): TO-263AB



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Channel temperature	T _{ch}		150	°C
Drain-source voltage	V _{DSS}		55	V
Gate-source voltage	V _{GSS}		±20	V
Continuous drain current(DC)	I _D		90	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10μs, duty=1/100	360	A
Total power dissipation	P _T		128	W
Single avalanche current	I _{AS}	Starting T _{ch} =25°C T _{ch} ≤150°C	52	A
Single avalanche energy	E _{AS}	Starting T _{ch} =25°C T _{ch} ≤150°C	272	mJ

※ :See the original Specifications

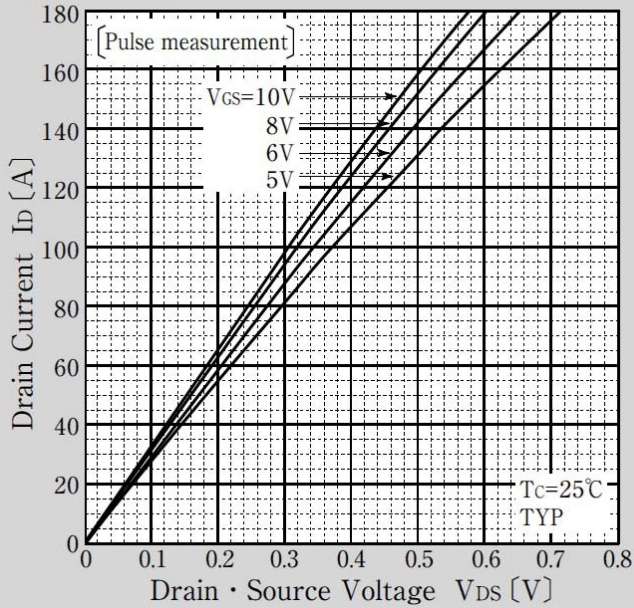
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	55			V
Zero gate voltage drain current	I_{DSS}	VDS=55V, VGS=0V			1	μA
Gate-source leakage current	I_{GSS}	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=45A, VDS=10V	25			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=45A, VGS=10V		0.003	0.0038	Ω
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=45A, VGS=4.5V		0.004	0.0054	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V_{SD}	IS=90A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case			0.97	°C/W
Total gate charge	Q_g	VDD=44V, VGS=10V, ID=90A		106		nC
Gate to source charge	Q_{gs}	VDD=44V, VGS=10V, ID=90A		19		nC
Gate to drain charge	Q_{gd}	VDD=44V, VGS=10V, ID=90A		34		nC
Input capacitance	C_{iss}	VDS=25V, VGS=0V, f=1MHz		5130		pF
Reverse transfer capacitance	C_{rss}	VDS=25V, VGS=0V, f=1MHz		320		pF
Output capacitance	C_{oss}	VDS=25V, VGS=0V, f=1MHz		600		pF
Turn-on delay time	$t_{d(on)}$	ID=45A, RL=0.61Ω, VDD=27.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		8		ns
Rise time	t_r	ID=45A, RL=0.61Ω, VDD=27.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		40		ns
Turn-off delay time	$t_{d(off)}$	ID=45A, RL=0.61Ω, VDD=27.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		103		ns
Fall time	t_f	ID=45A, RL=0.61Ω, VDD=27.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		67		ns
Diode reverse recovery time	t_{rr}	IF=90A, VGS=0V, di/dt=100A/μs		47		ns
Diode reverse recovery charge	Q_{rr}	IF=90A, VGS=0V, di/dt=100A/μs		70		nC

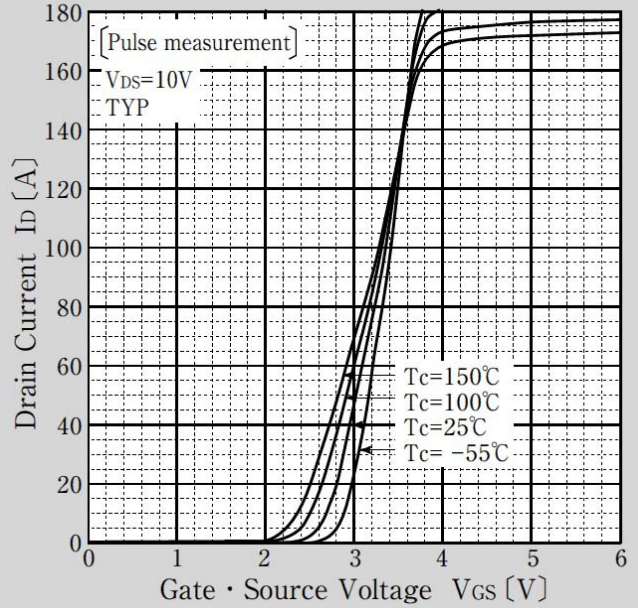
* : See the original Specifications

CHARACTERISTIC DIAGRAMS

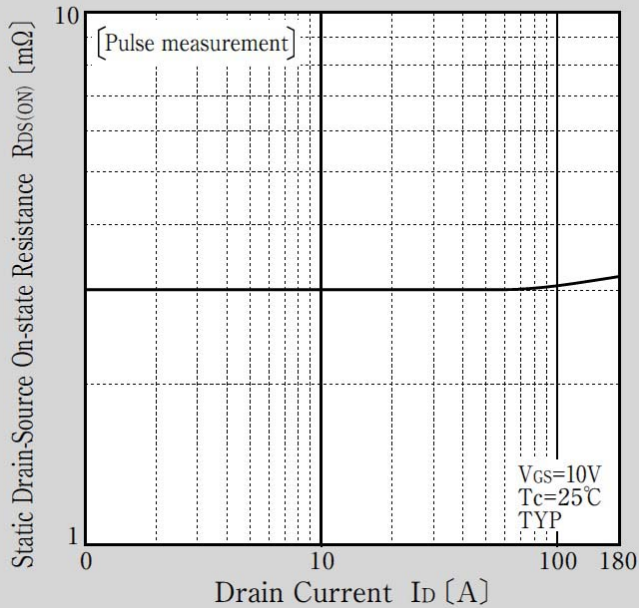
Typical Output Characteristics



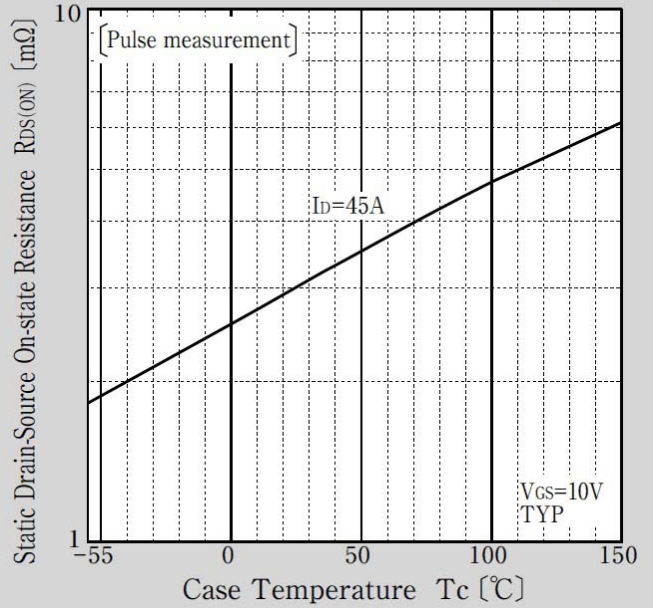
Transfer Characteristics



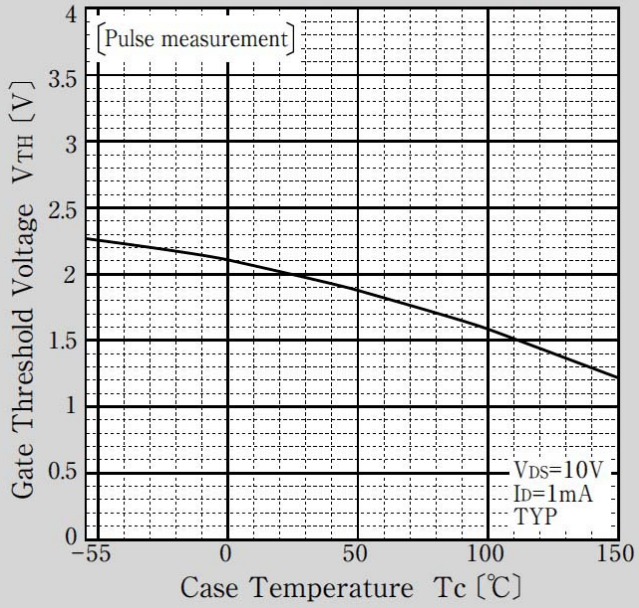
Static Drain-Source On-state Resistance vs Drain Current



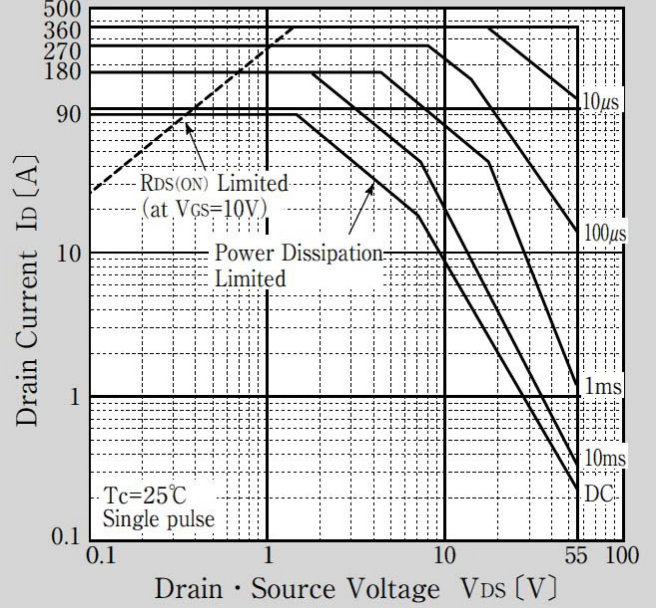
Static Drain-Source On-state Resistance vs Case Temperature



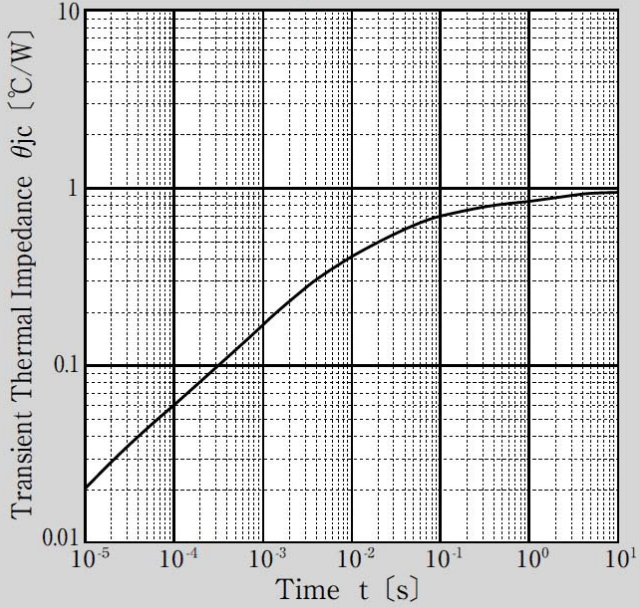
Gate Threshold Voltage vs Case Temperature



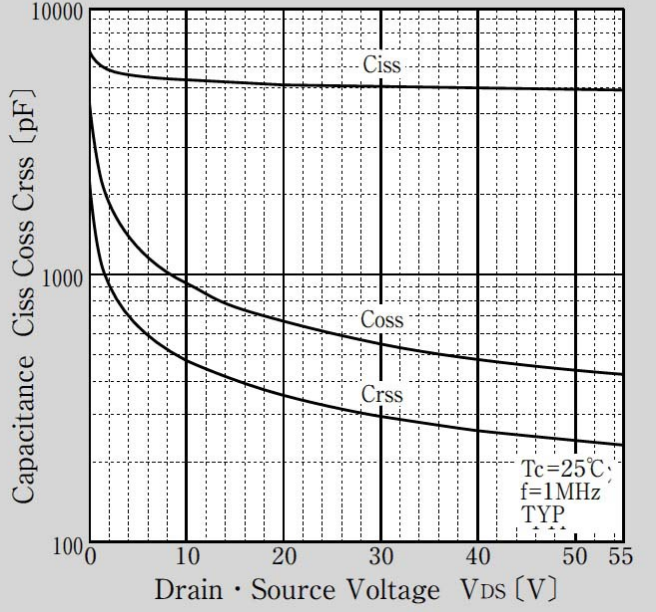
Safe Operating Area



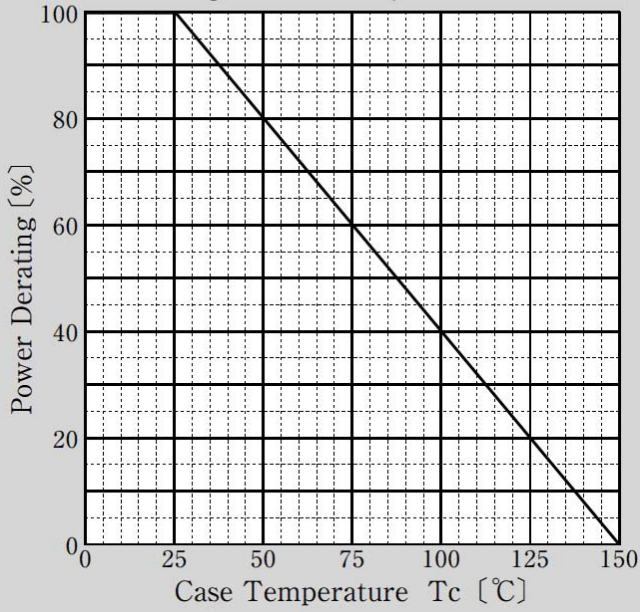
Transient Thermal Impedance



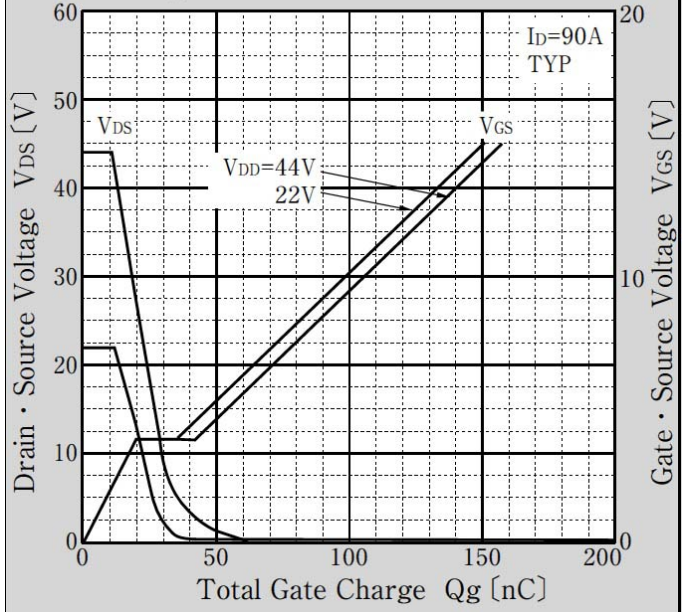
Capacitance Characteristics



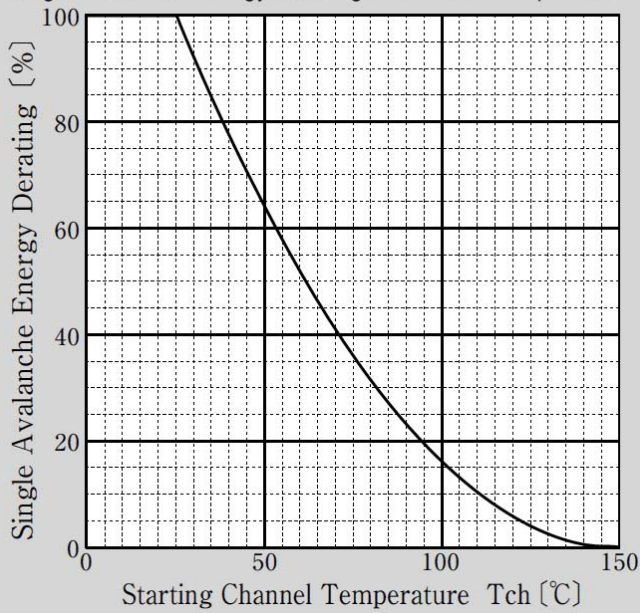
Power Derating - Case Temperature



Gate Charge Characteristics

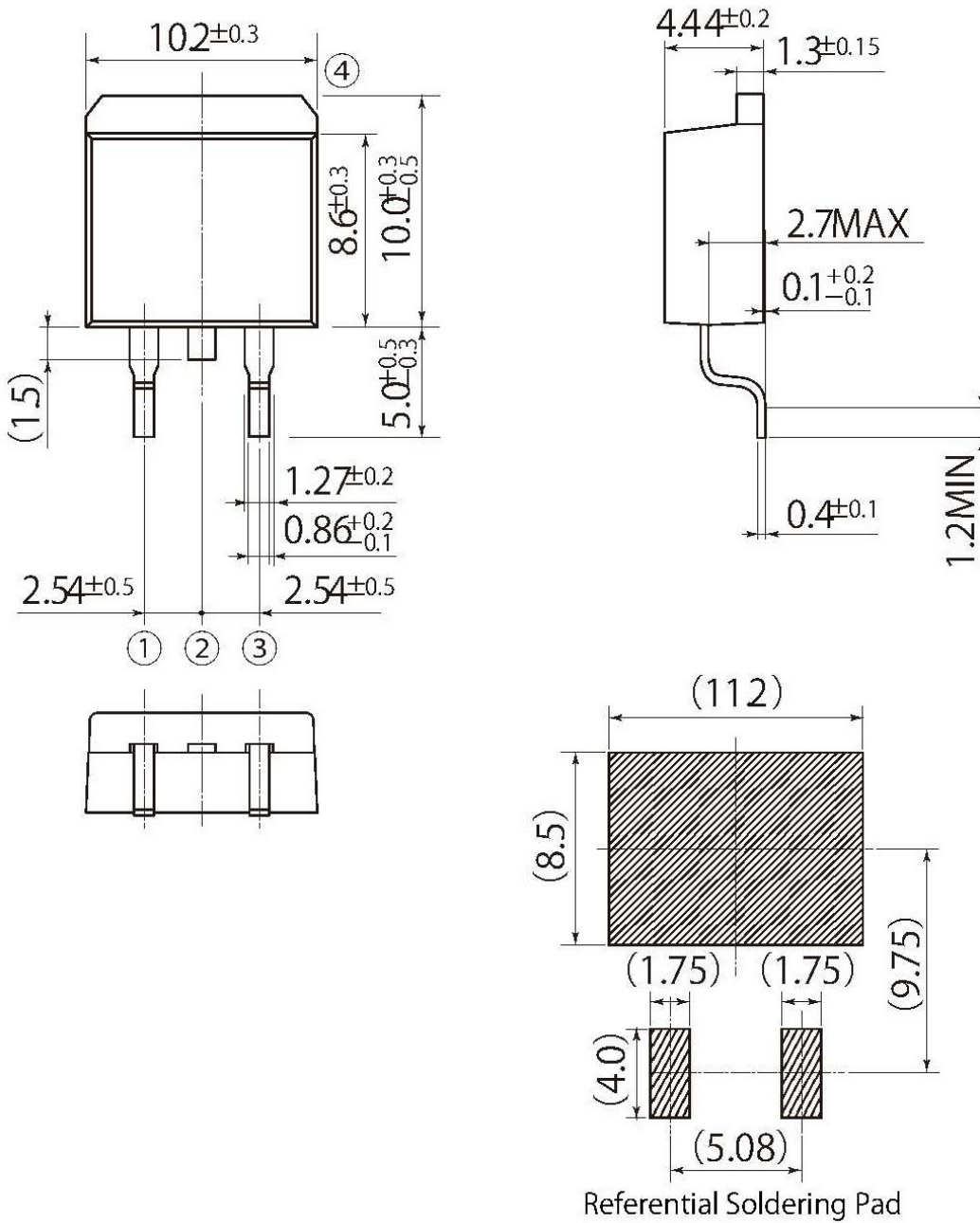


Single Avalanche Energy Derating vs Channel Temperature



H4

JEDEC Code	TO-263AB
JEITA Code	-
House Name	FG



• Optimize soldering pad to the board design and soldering condition.

Notes

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