

P88FP10SNK

Power MOSFETs
100V, 88A, N-channel

Feature

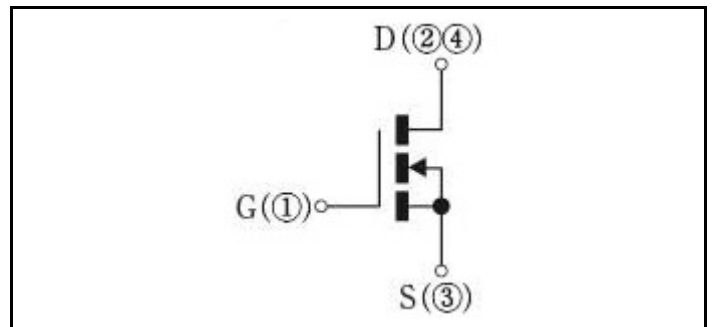
- N-channel
- SMD
- Large Current
- Low Ron
- 10V Gate Drive
- Low Capacitance
- Available for automotive use
- Halogen free
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FP
Package (JEITA Code): SC-83 similar



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 175	°C
Channel temperature	T _{ch}		-55 to 175	°C
Drain-source voltage	V _{DSS}		100	V
Gate-source voltage	V _{GSS}		±20	V
Continuous drain current(DC)	I _D		88	A
Continuous drain current(Peak)	I _{DP}	Pulse width 10µs, duty=1/100	352	A
Total power dissipation	P _T		178	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≤150°C	44	A
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≤150°C	219	mJ

※ :See the original Specifications

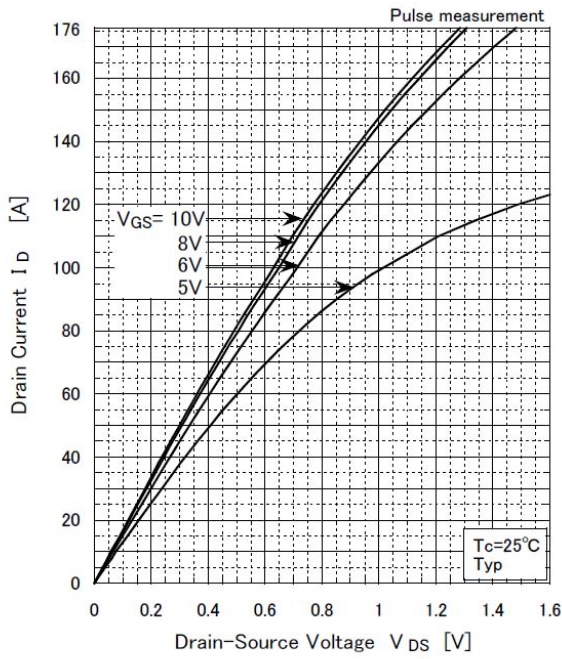
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	100			V
Zero gate voltage drain current	I_{DSS}	VDS=100V, VGS=0V			1	μ A
Gate-source leakage current	I_{GSS}	VGS= \pm 20V, VDS=0V			\pm 0.1	μ A
Forward transconductance	g_{fs}	ID=44A, VDS=10V	26			S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=44A, VGS=10V		0.0061	0.0077	Ω
Gate threshold voltage	V_{th}	ID=1mA, VDS=10V	2	3	4	V
Source-drain diode forward voltage	V_{SD}	IS=88A, VGS=0V			1.5	V
Thermal resistance	$R_{th(j-c)}$	Junction to case, with heatsink			0.84	$^{\circ}$ C/W
Total gate charge	Q_g	VDD=80V, VGS=10V, ID=88A		108		nC
Gate to source charge	Q_{gs}	VDD=80V, VGS=10V, ID=88A		30		nC
Gate to drain charge	Q_{gd}	VDD=80V, VGS=10V, ID=88A		41		nC
Input capacitance	C_{iss}	VDS=25V, VGS=0V, f=1MHz		6100		pF
Reverse transfer capacitance	C_{rss}	VDS=25V, VGS=0V, f=1MHz		255		pF
Output capacitance	C_{oss}	VDS=25V, VGS=0V, f=1MHz		530		pF
Turn-on delay time	$t_{d(on)}$	ID=44A, RL=1.14 Ω , VDD=50V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		9.5		ns
Rise time	t_r	ID=44A, RL=1.14 Ω , VDD=50V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		27		ns
Turn-off delay time	$t_{d(off)}$	ID=44A, RL=1.14 Ω , VDD=50V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		62		ns
Fall time	t_f	ID=44A, RL=1.14 Ω , VDD=50V, Rg=0 Ω , VGS(+)=10V, VGS(-)=0V		34		ns
Diode reverse recovery time	t_{rr}	IF=88A, VGS=0V, di/dt=100A/ μ s		58		ns
Diode reverse recovery charge	Q_{rr}	IF=88A, VGS=0V, di/dt=100A/ μ s		125		nC

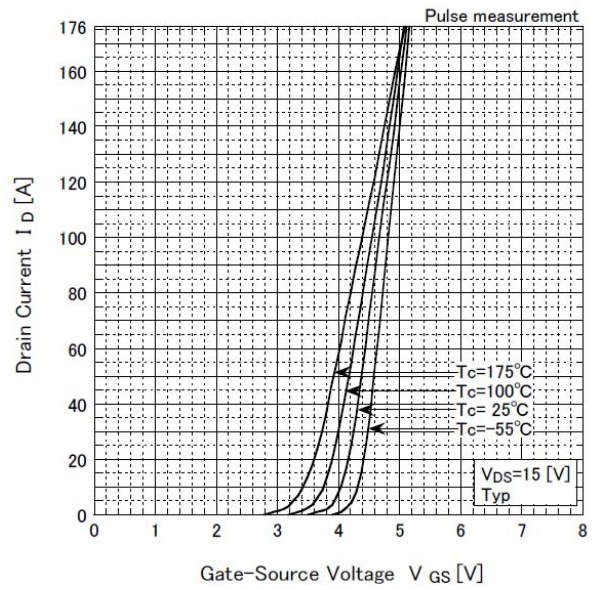
※ : See the original Specifications

CHARACTERISTIC DIAGRAMS

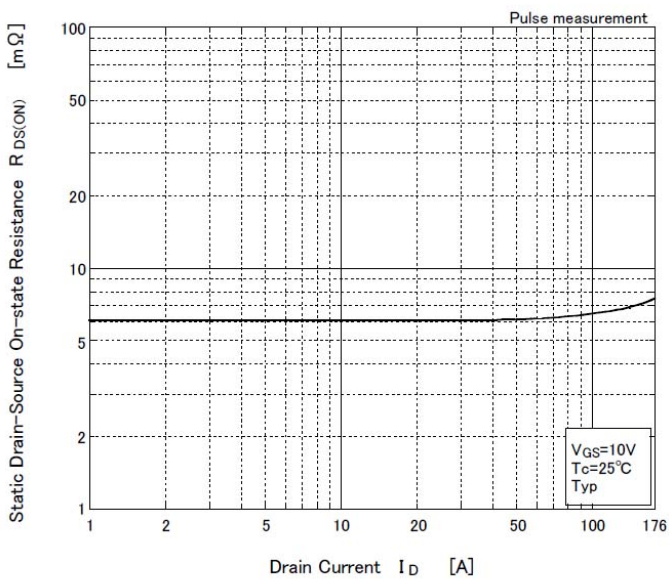
Typical Output Characteristics



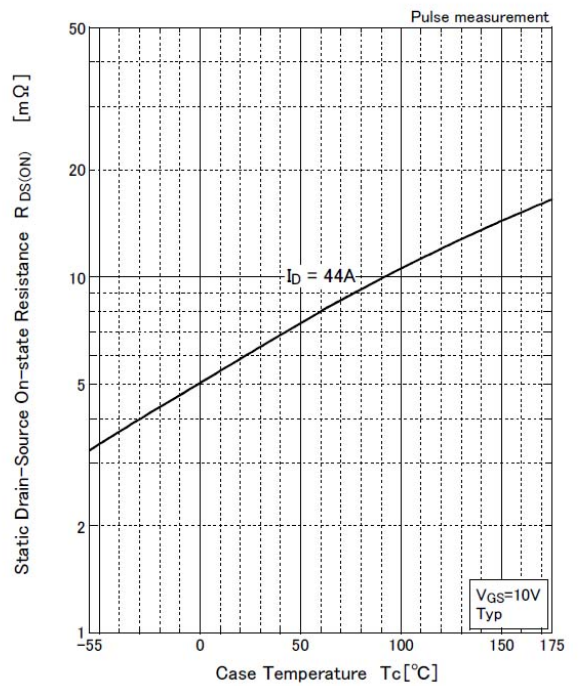
Transfer Characteristics



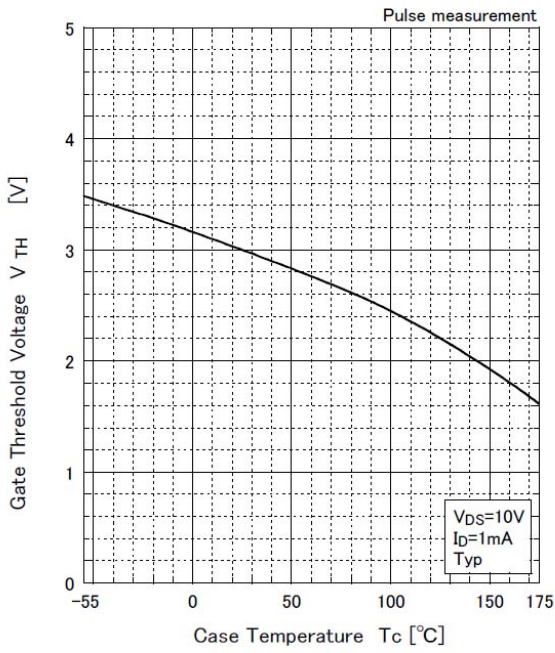
Static Drain-Source On-state Resistance vs Drain Current



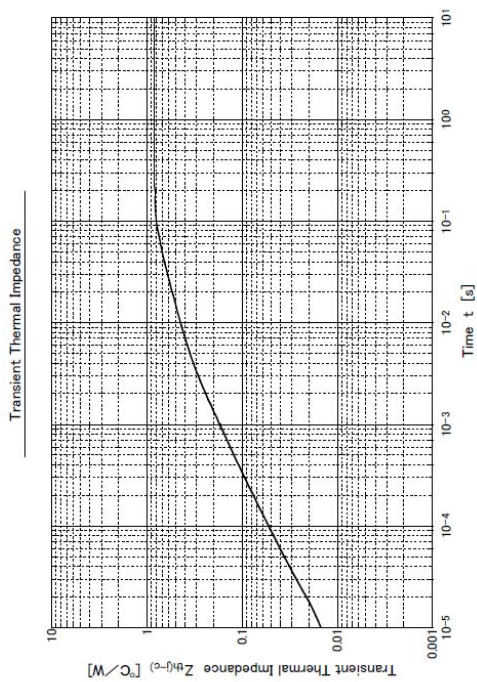
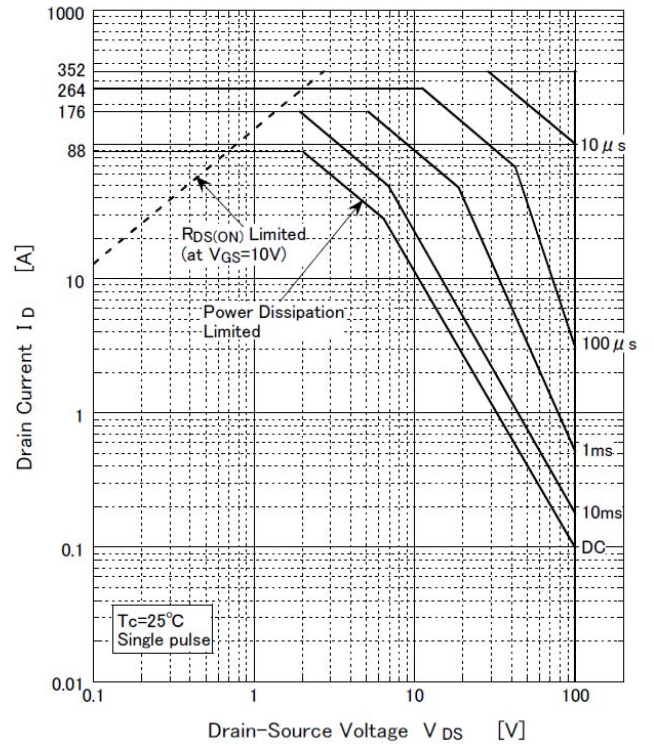
Static Drain-Source On-state Resistance vs Case Temperature



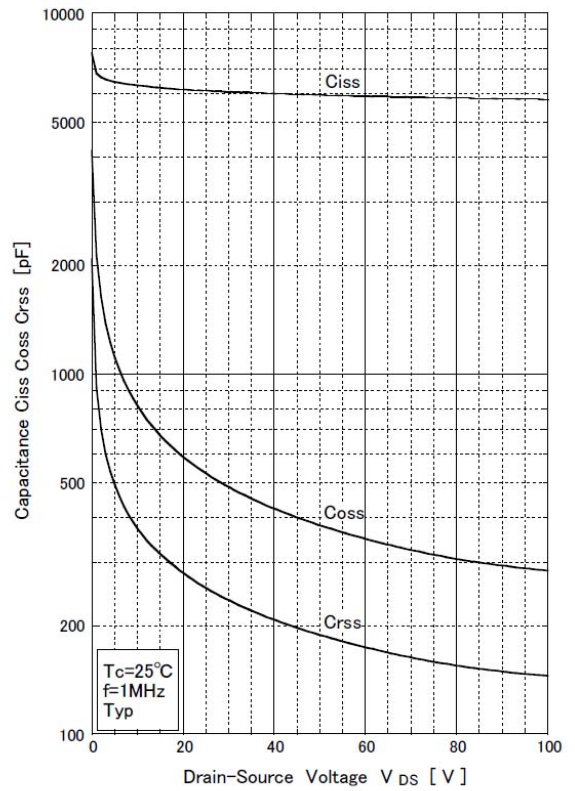
Gate Threshold Voltage vs Case Temperature



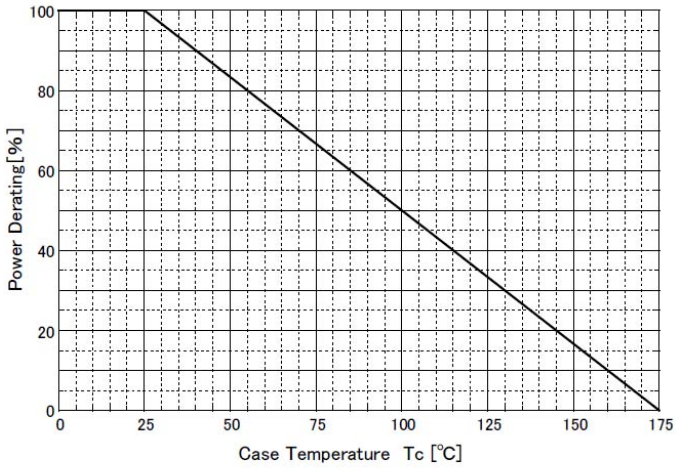
Safe Operating Area



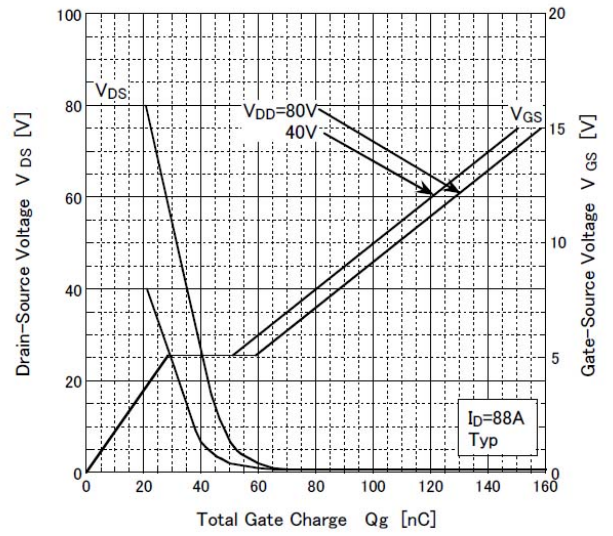
Capacitance Characteristics



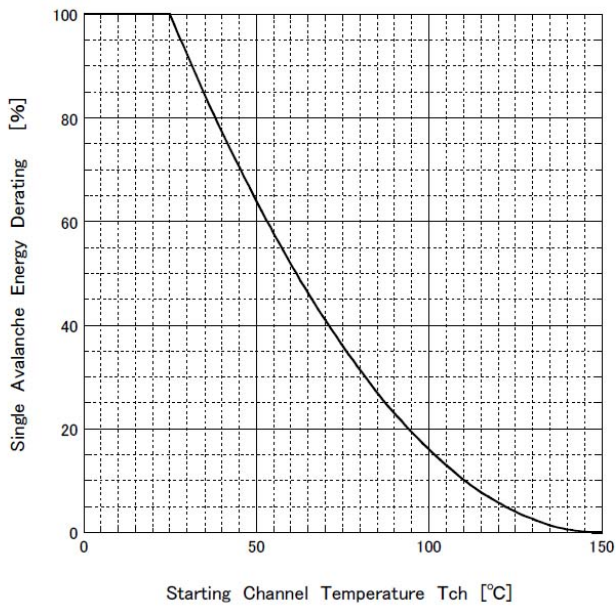
Power Derating - Case Temperature



Gate Charge Characteristics

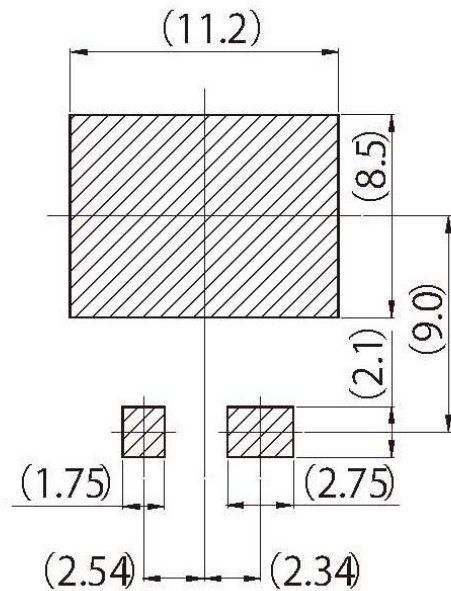
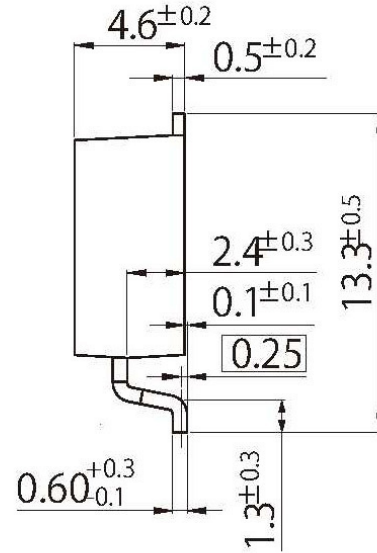
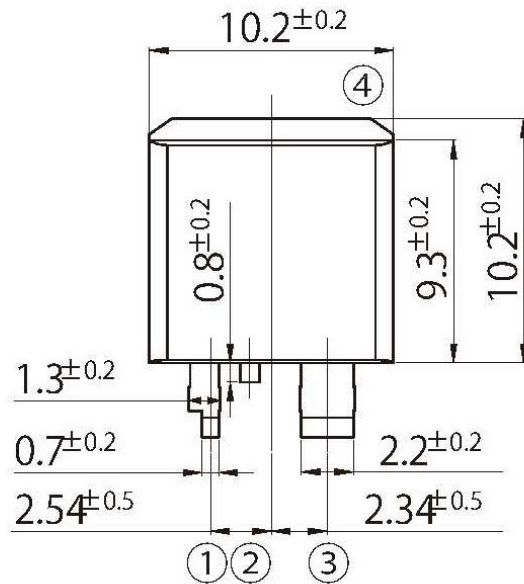


Single Avalanche Energy Derating vs Channel Temperature



H5

JEDEC Code	-
JEITA Code	SC-83 similar
House Name	FP



• Optimize soldering pad to the board design and soldering condition.

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