

P64LF6QL

Power MOSFETs

60V, 64A, N-channel

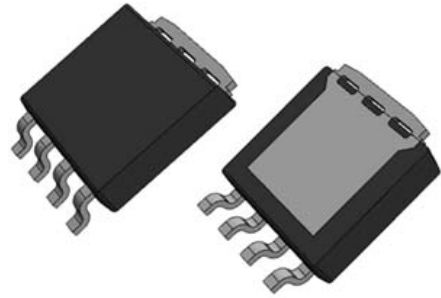
Feature

- N-channel
- Small SMD
- Large Current
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Halogen free
- Pb free terminal
- RoHS:Yes

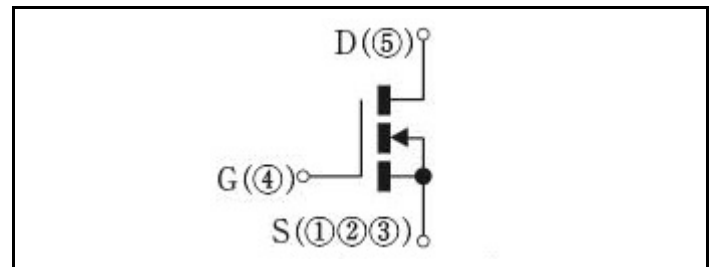
OUTLINE

Package (House Name): LF

Package (JEDEC Code): MO-235B similar



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

| Item | Symbol | Conditions | Ratings | Unit |
|--------------------------------|------------------|------------------------------|------------|------|
| Storage temperature | Tstg | | -55 to 175 | °C |
| Channel temperature | Tch | | -55 to 175 | °C |
| Drain-source voltage | V _{DSS} | | 60 | V |
| Gate-source voltage | V _{GSS} | | ±20 | V |
| Continuous drain current(DC) | I _D | | 64 | A |
| Continuous drain current(Peak) | I _{DP} | Pulse width 10μs, duty=1/100 | 192 | A |
| Total power dissipation | P _T | | 168 | W |
| Single avalanche current | I _{AS} | Starting Tch=25°C Tch≤150°C | 34 | A |
| Single avalanche energy | E _{AS} | Starting Tch=25°C Tch≤150°C | 128 | mJ |

* : See the original Specifications

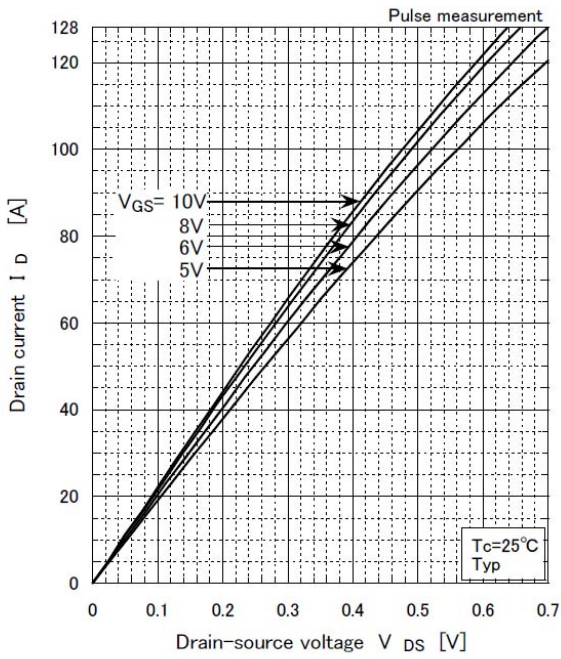
Electrical Characteristics (unless otherwise specified : Tc=25°C)

| Item | Symbol | Conditions | Ratings | | | Unit |
|-----------------------------------------|---------------|---------------------------------------------------------|---------|--------|--------|------|
| | | | MIN | TYP | MAX | |
| Drain-Source breakdown voltage | $V_{(BR)DSS}$ | ID=1mA, VGS=0V | 60 | | | V |
| Zero gate voltage drain current | I_{DSS} | VDS=60V, VGS=0V | | | 1 | μA |
| Gate-source leakage current | I_{GSS} | VGS=±20V, VDS=0V | | | ±0.1 | μA |
| Forward transconductance | g_{fs} | ID=32A, VDS=10V | 20 | | | S |
| Static drain-source on-state resistance | $R_{DS(ON)}$ | ID=32A, VGS=10V | | 0.0045 | 0.0057 | Ω |
| Static drain-source on-state resistance | $R_{DS(ON)}$ | ID=32A, VGS=4.5V | | 0.0056 | 0.0075 | Ω |
| Gate threshold voltage | V_{th} | ID=1mA, VDS=10V | 1.5 | 2 | 2.5 | V |
| Source-drain diode forward voltage | V_{SD} | IS=64A, VGS=0V | | | 1.5 | V |
| Thermal resistance | $R_{th(j-c)}$ | Junction to case, with heatsink | | | 0.89 | °C/W |
| Total gate charge | Q_g | VDD=48V, VGS=10V, ID=64A | | 77 | | nC |
| Gate to source charge | Q_{gs} | VDD=48V, VGS=10V, ID=64A | | 15.5 | | nC |
| Gate to drain charge | Q_{gd} | VDD=48V, VGS=10V, ID=64A | | 21 | | nC |
| Input capacitance | C_{iss} | VDS=25V, VGS=0V, f=1MHz | | 4260 | | pF |
| Reverse transfer capacitance | C_{rss} | VDS=25V, VGS=0V, f=1MHz | | 170 | | pF |
| Output capacitance | C_{oss} | VDS=25V, VGS=0V, f=1MHz | | 351 | | pF |
| Turn-on delay time | $t_{d(on)}$ | ID=32A, RL=0.94Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V | | 5.5 | | ns |
| Rise time | t_r | ID=32A, RL=0.94Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V | | 19.5 | | ns |
| Turn-off delay time | $t_{d(off)}$ | ID=32A, RL=0.94Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V | | 80 | | ns |
| Fall time | t_f | ID=32A, RL=0.94Ω, VDD=30V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V | | 25 | | ns |
| Diode reverse recovery time | t_{rr} | IF=64A, VGS=0V, di/dt=100A/μs | | 41 | | ns |
| Diode reverse recovery charge | Q_{rr} | IF=64A, VGS=0V, di/dt=100A/μs | | 49 | | nC |

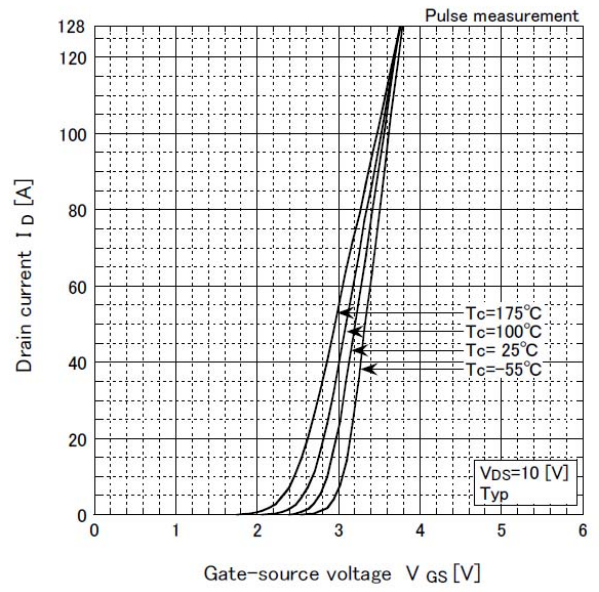
* : See the original Specifications

CHARACTERISTIC DIAGRAMS

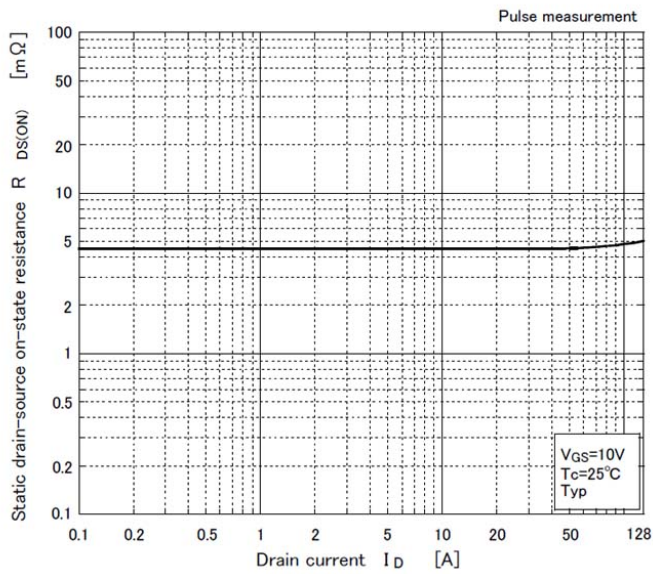
Typical output characteristics



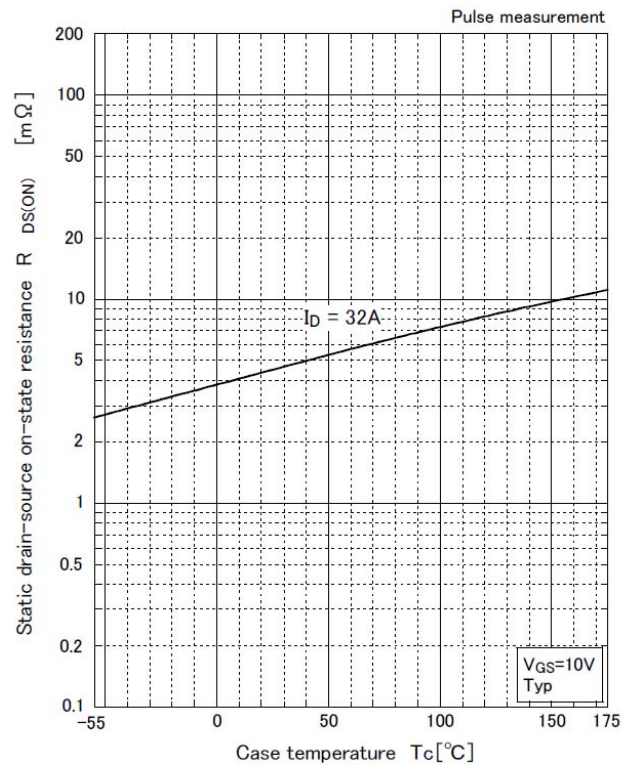
Transfer characteristics

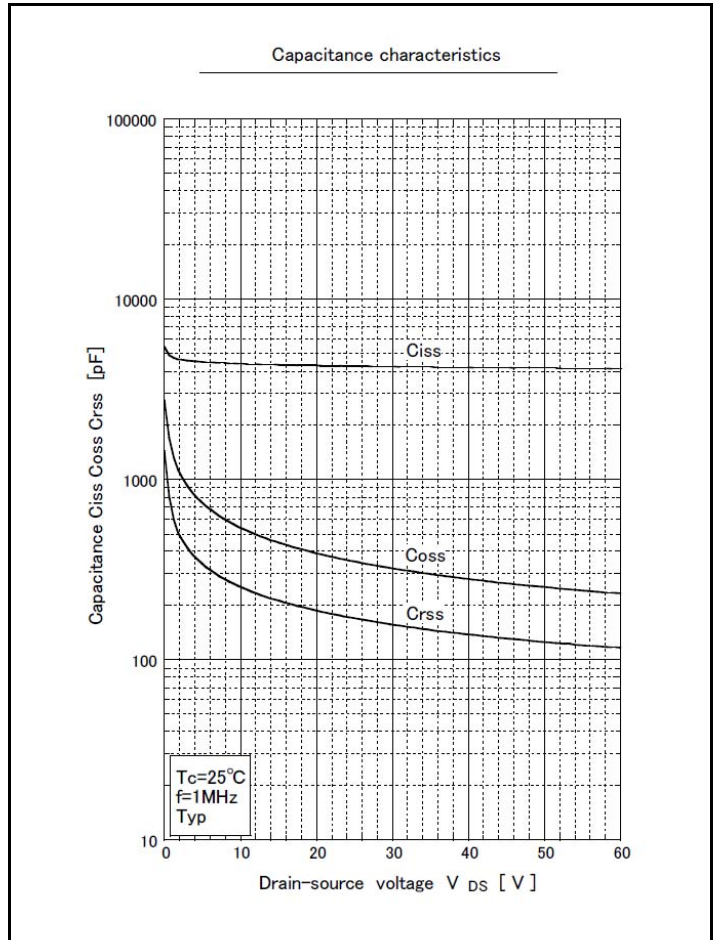
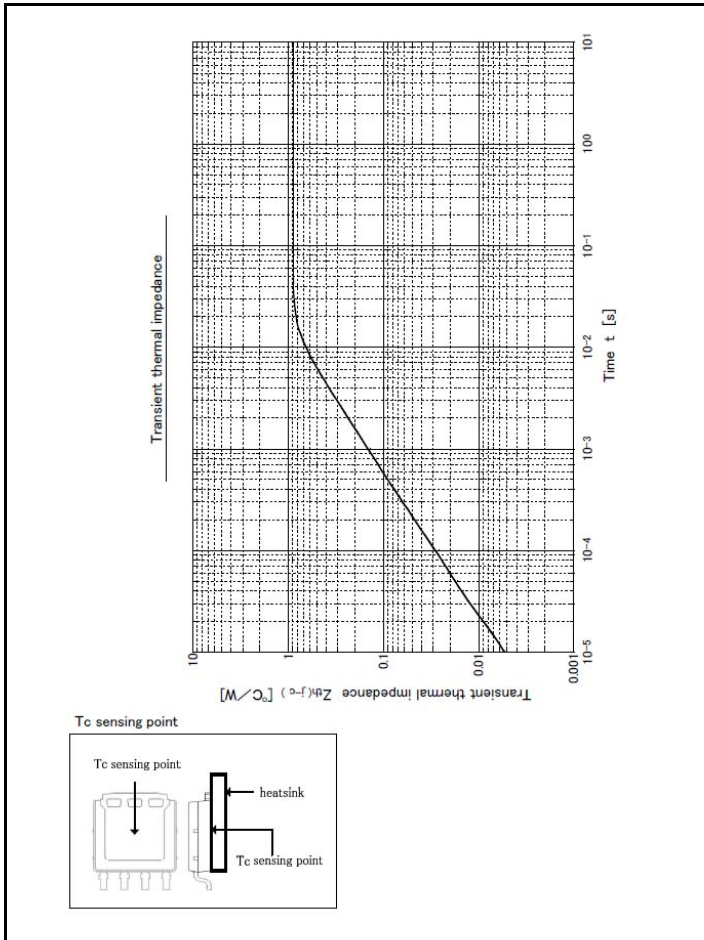
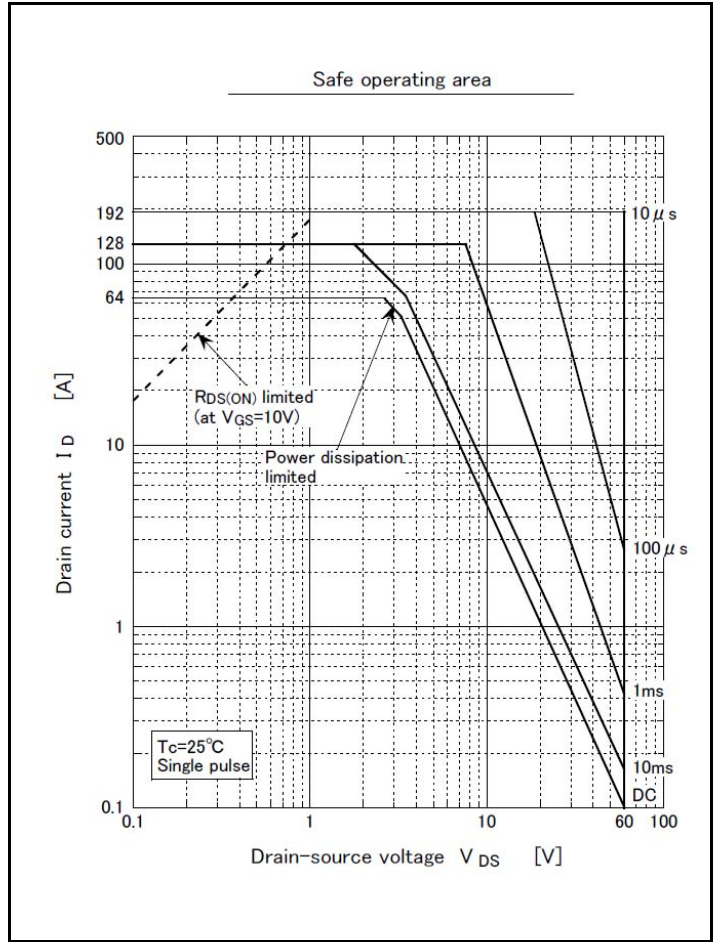
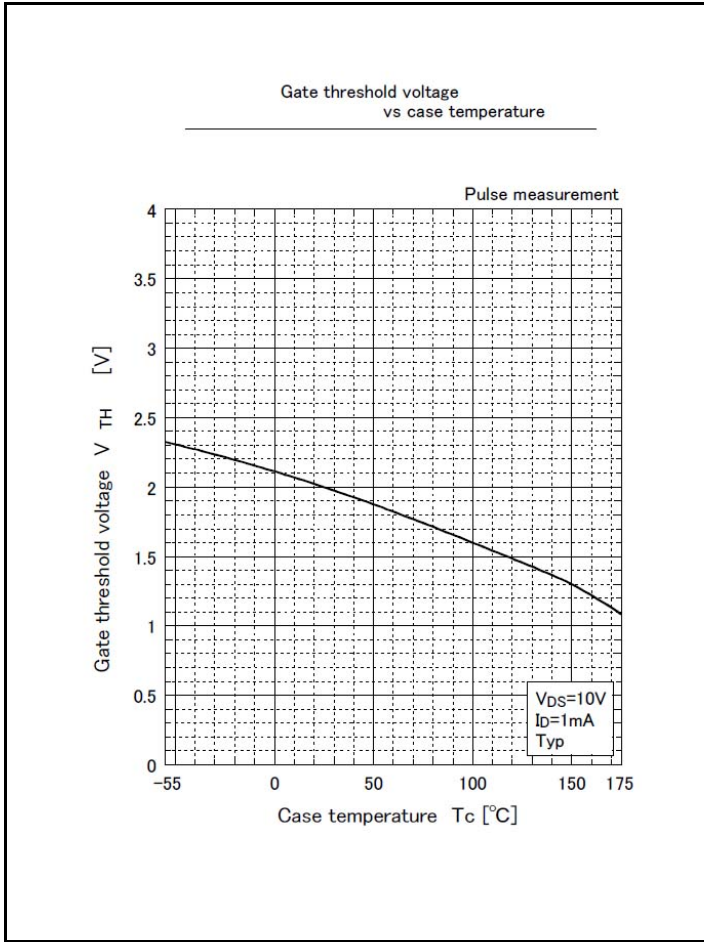


Static drain-source on-state resistance vs drain current

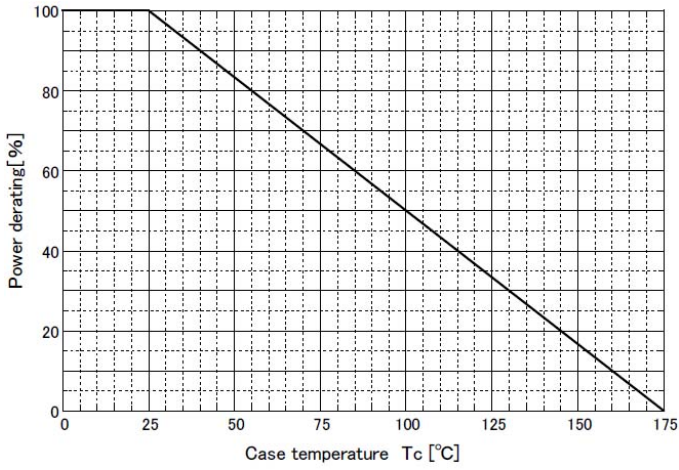


Static drain-source on-state resistance vs case temperature

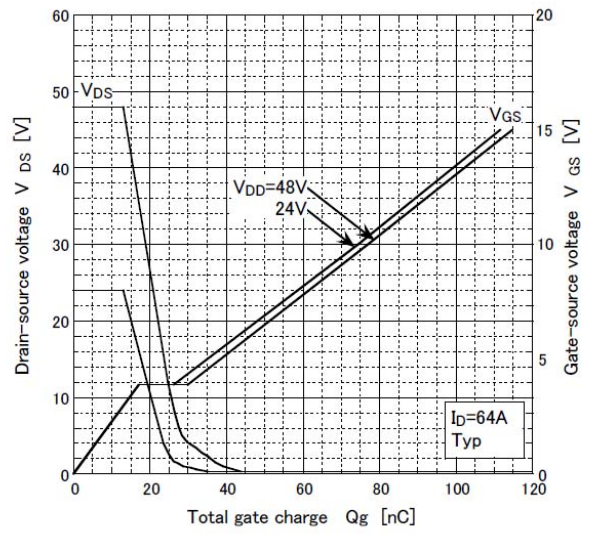




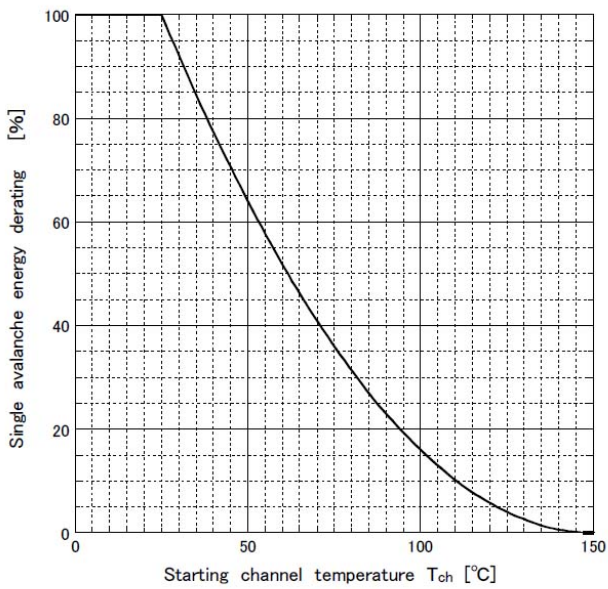
Power derating - case temperature



Gate charge characteristics

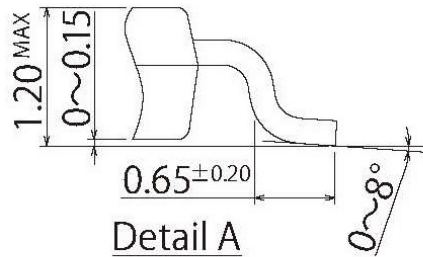
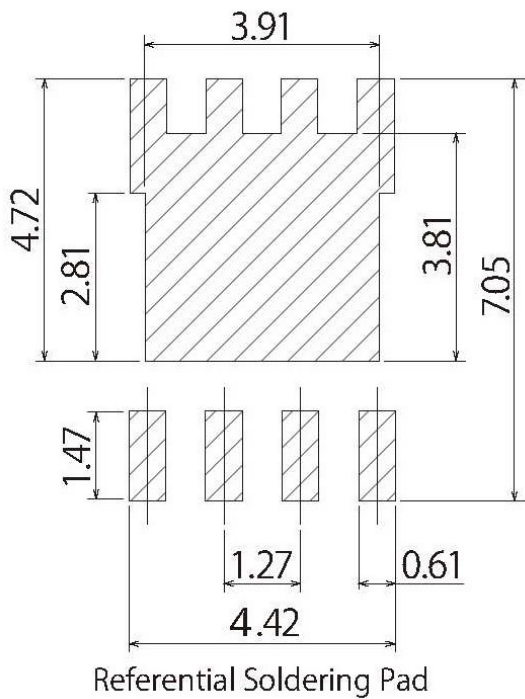
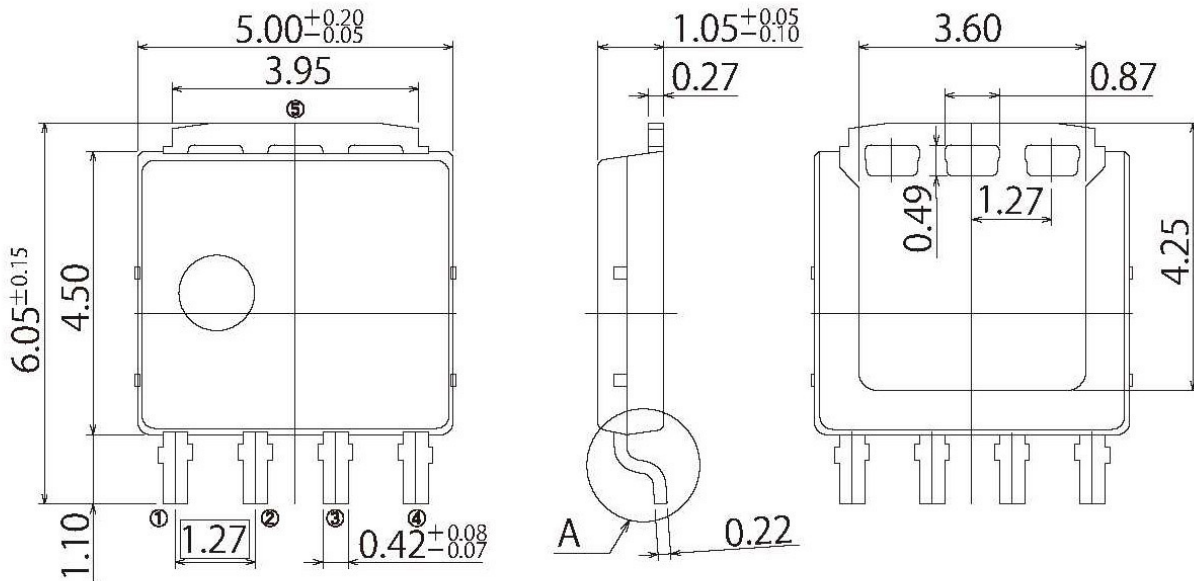


Single avalanche energy derating vs channel temperature



G7

| | |
|------------|-----------------|
| JEDEC Code | MO-235B similar |
| JEITA Code | — |
| House Name | LF |



• Optimize soldering pad to the board design and soldering condition.

Notes

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