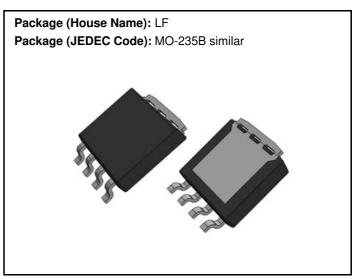
# P46LF7R5SLK Power MOSFETs

75V, 46A, N-channel

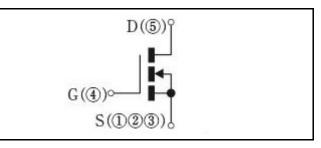
## Feature

- N-channel
- Small SMD
- Large Current
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- · Based on AEC-Q101
- Halogen free
- Pb free terminal
- RoHS:Yes

### OUTLINE



## Equivalent circuit



## Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

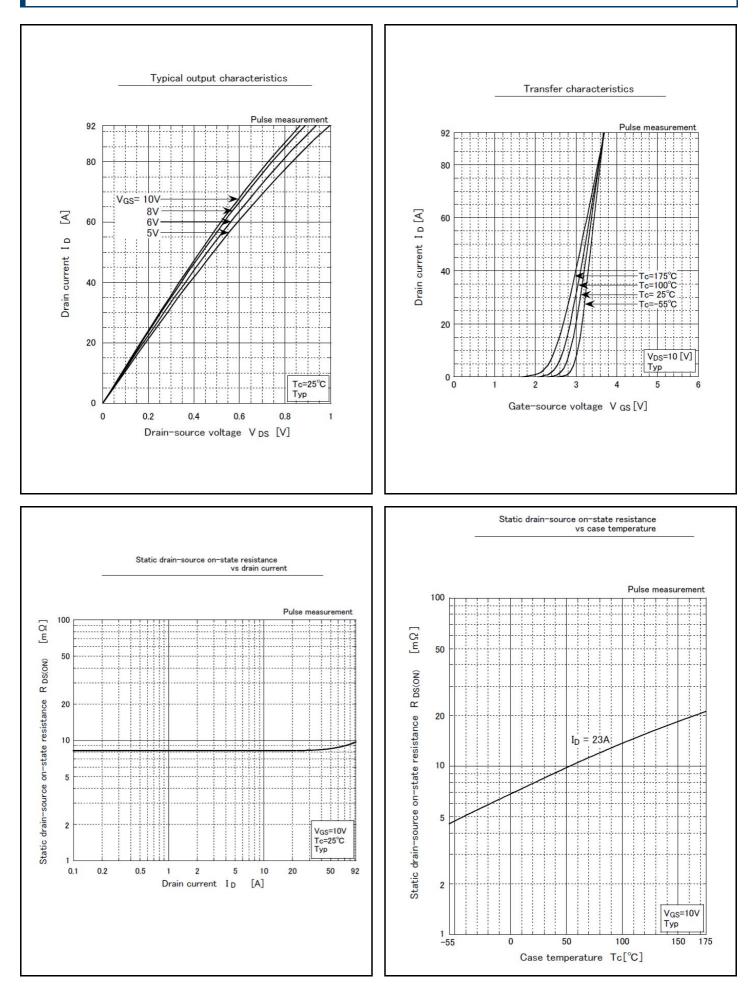
Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		-55 to 175	°C
Drain-source voltage	V <sub>DSS</sub>		75	V
Gate-source voltage	V <sub>GSS</sub>		±20	V
Continuous drain current(DC)	I <sub>D</sub>		46	Α
Continuous drain current(Peak)	I <sub>DP</sub>	Pulse width 10µs, duty=1/100	138	Α
Total power dissipation	P <sub>T</sub>		168	W
Single avalanche current	I <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	28	Α
Single avalanche energy	E <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	88	mJ

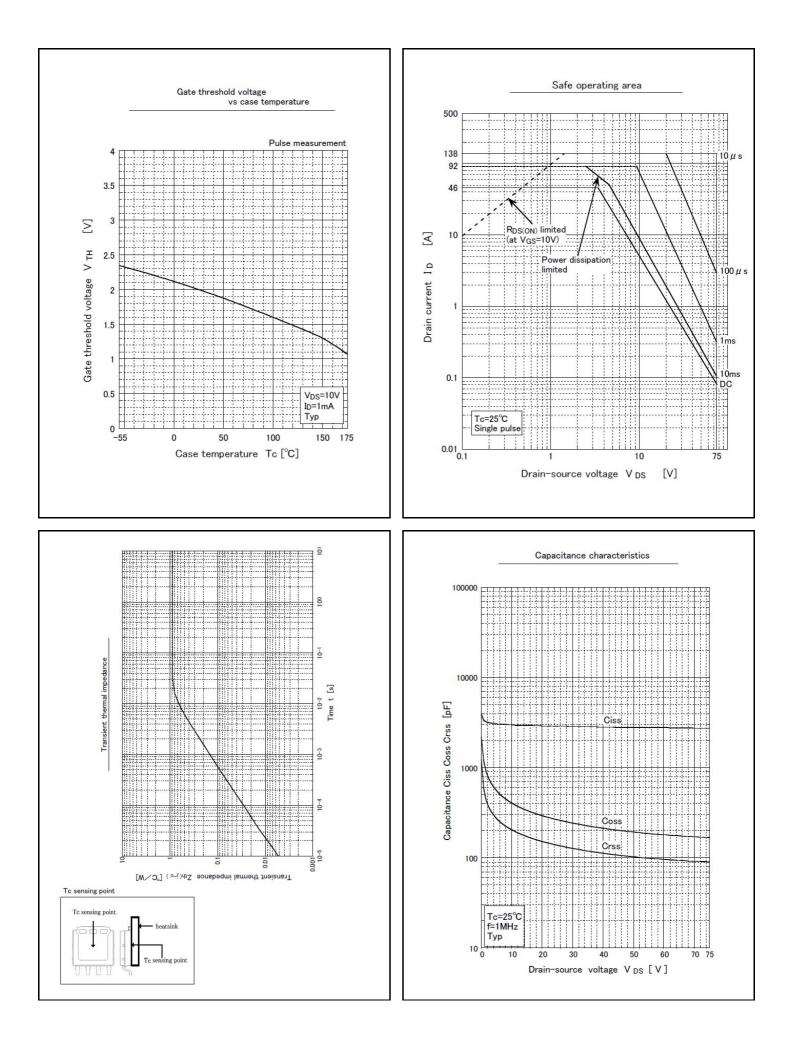
\* : See the original Specifications

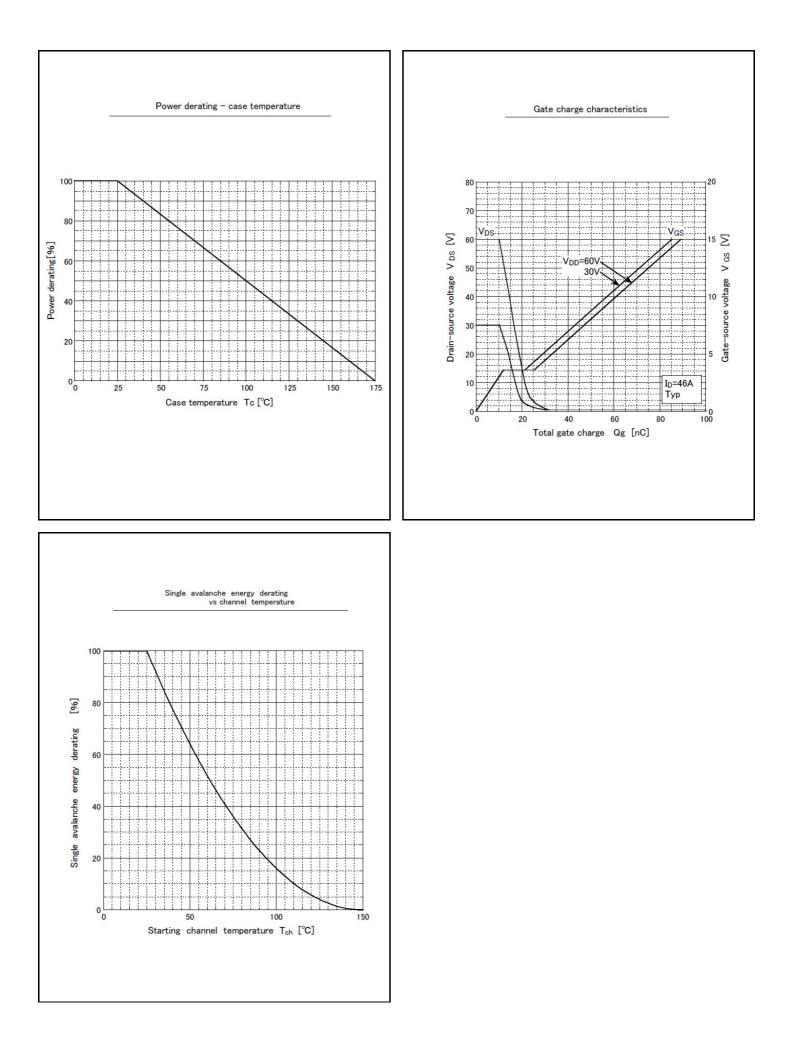
Item	Symbol	Conditions		Ratings		
			MIN	ТҮР	MAX	Unit
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	ID=1mA, VGS=0V	75			V
Zero gate voltage drain current	I <sub>DSS</sub>	VDS=75V, VGS=0V			1	μA
Gate-source leakage current	I <sub>GSS</sub>	VGS=±20V, VDS=0V			±0.1	μA
Forward transconductance	g <sub>fs</sub>	ID=23A, VDS=10V	10			S
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=23A, VGS=10V		0.0082	0.0103	Ω
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=23A, VGS=4.5V		0.0096	0.0128	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	V <sub>SD</sub>	IS=46A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case, with heatsink			0.89	°C/W
Total gate charge	Qg	VDD=60V, VGS=10V, ID=46A		61		nC
Gate to source charge	Qgs	VDD=60V, VGS=10V, ID=46A		11.5		nC
Gate to drain charge	Qgd	VDD=60V, VGS=10V, ID=46A		17		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		2890		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		138		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		265		pF
Turn-on delay time	td(on)	ID=23A, RL=1.63Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		5		ns
Rise time	tr	ID=23A, RL=1.63Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		11		ns
Turn-off delay time	td(off)	ID=23A, RL=1.63Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		47		ns
Fall time	tf	ID=23A, RL=1.63Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		19		ns
Diode reverse recovery time	trr	IF=46A, VGS=0V, di/dt=100A/µs		47		ns
Diode reverse recovery charge	Qrr	IF=46A, VGS=0V, di/dt=100A/µs		67		nC

\* : See the original Specifications

## **CHARACTERISTIC DIAGRAMS**

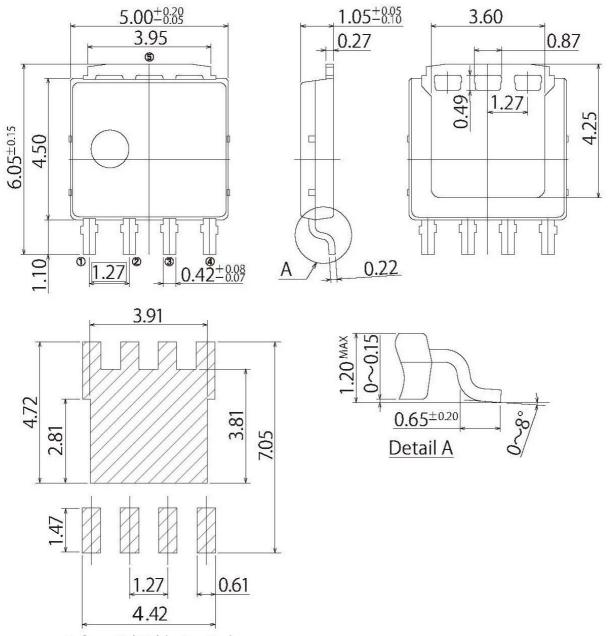






unit:mm

G7	JEDEC Code	MO-235B similar
	JEITA Code	
	House Name	LF



**Referential Soldering Pad** 

Optimize soldering pad to the board design and soldering condition.

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#### [Special applications]

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#### [Specific applications]

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