ShinDengen

P36F28HP2

Power MOSFETs
280V, 36A, N-channel

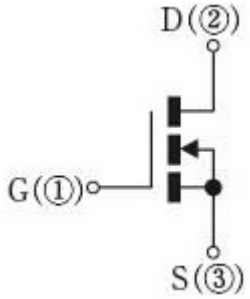
Feature

- N-channel
- High Voltage
- High Speed Switching
- Low Ron
- Low Capacitance
- High Avalanche Durability, High di/dt Durability
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FTO-220AG
Package (JEITA Code): SC-91

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 150	℃
Channel tempertature	Tch		150	℃
Drain-source voltage	V_{DSS}		280	V
Gate-source voltage	V_{GSS}		±30	V
Continuous drain current(DC)	I_D		36	A
Continuous drain current(Peak)	I_{DP}	Pulse width 10μs, duty=1/100	144	A
Continuous source current(DC)	Is		36	A
Total power dissipation	P_T		95	W
Repetitive avalanche current	I_{AR}	Starting Tch=25℃ Tch≦150℃	36	A
Single avalanche energy	E_{AS}	Starting Tch=25℃ Tch≦150℃	65	mJ
Repetitive avalanche energy	E_{AR}	Starting Tch=25℃ Tch≦150℃	6.5	mJ
Drain-source diode di/dt strength	di/dt	Is=36A, Tc=25℃	350	A/μs
Dielectric strenght	Vdis	Terminals to case, AC1min	2	kV
Mounting torque	TOR	(Recommended torque：0.3N･m)	0.5	N･m

※ ：See the original Specifications

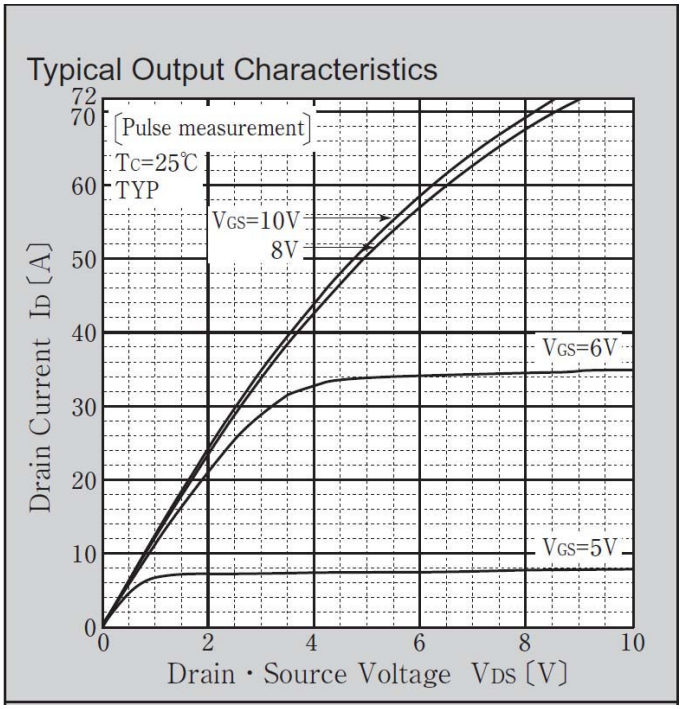
Electrical Characteristics (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	280			V
Zero gate voltage drain current	I_{DSS}	VDS=280V, VGS=0V			100	μA
Gate-source leakage current	I_{GSS}	VGS=±30V, VDS=0V			±0.1	μA
Forward transconductance	g_{fs}	ID=18A, VDS=10V	13	26		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=18A, VGS=10V		0.08	0.12	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	3	3.75	4.5	V
Source-drain diode forward voltage	V_{SD}	IS=18A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			1.32	℃/W
Total gate charge	Qg	VDD=200V, VGS=10V, ID=36A		35		nC
Input capacitance	Ciss	VDS=50V, VGS=0V, f=1MHz		1730		pF
Reverce transfer capacitnce	Crss	VDS=50V, VGS=0V, f=1MHz		13.5		pF
Output capacitance	Coss	VDS=50V, VGS=0V, f=1MHz		250		pF
Turn-on delay time	td(on)	ID=18A, RL=8.3Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		35		ns
Rise time	tr	ID=18A, RL=8.3Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		83		ns
Turn-off delay time	td(off)	ID=18A, RL=8.3Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		120		ns
Fall time	tf	ID=18A, RL=8.3Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		59		ns

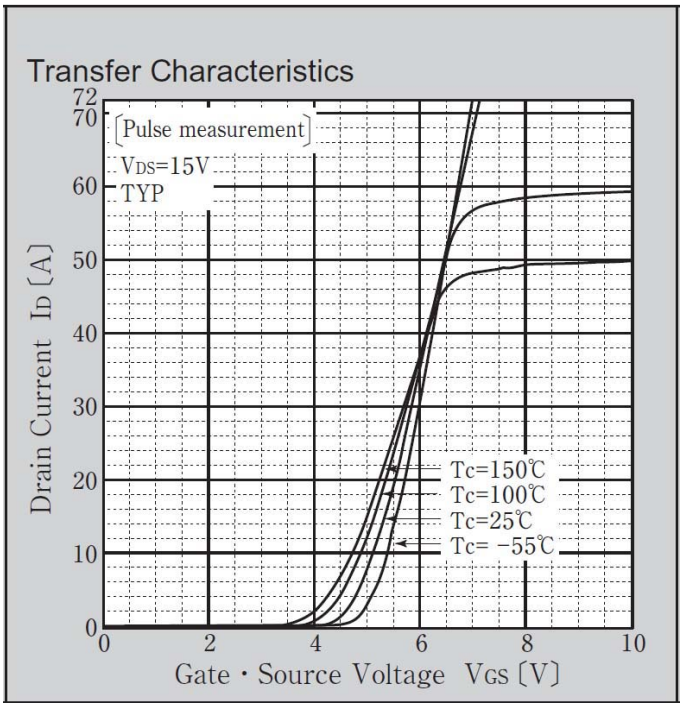
※ ：See the original Specifications

CHARACTERISTIC DIAGRAMS

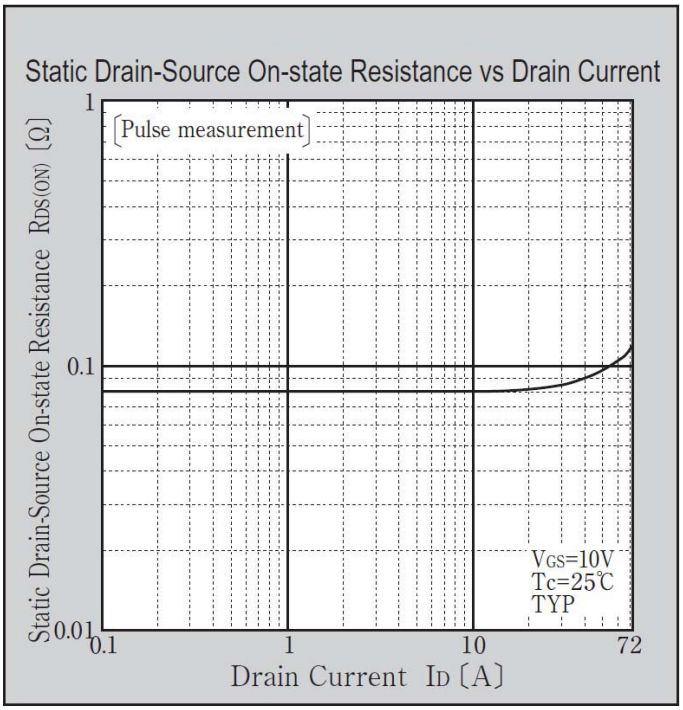
Typical Output Characteristics



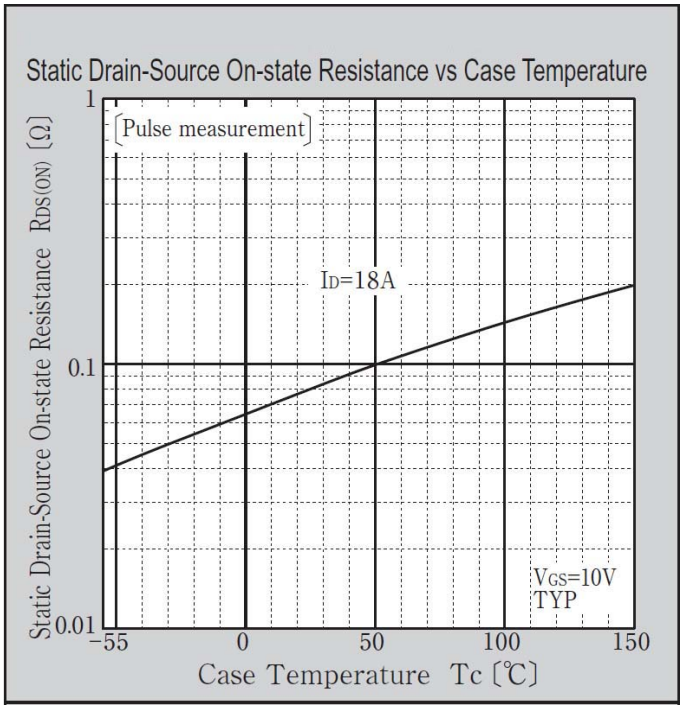
Transfer Characteristics



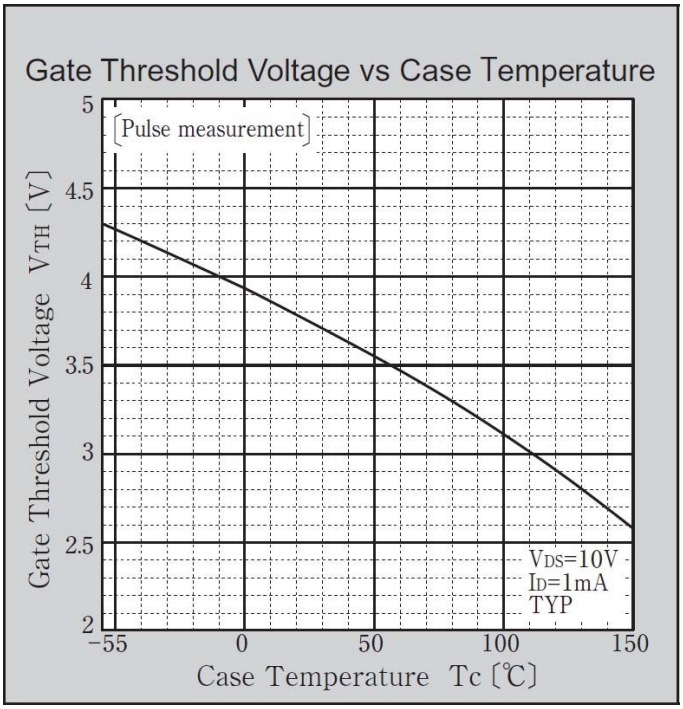
Static Drain-Source On-state Resistance vs Drain Current



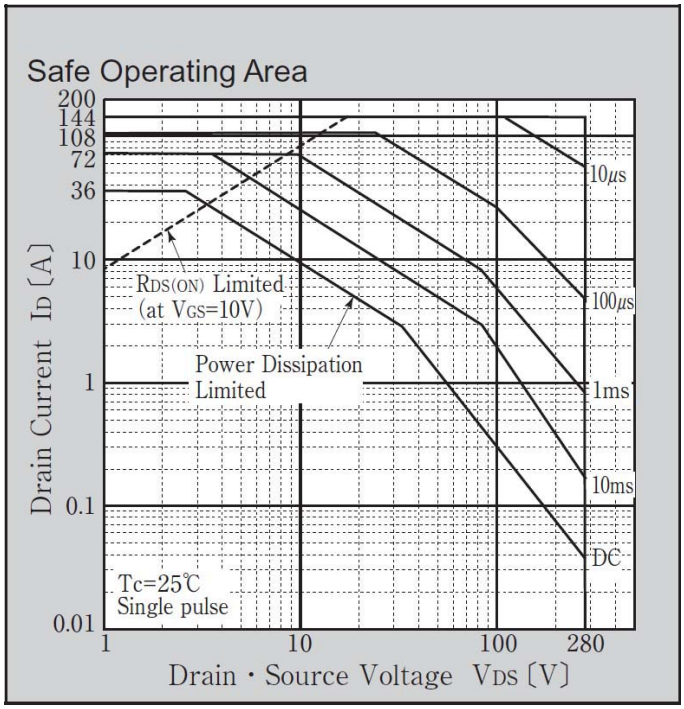
Static Drain-Source On-state Resistance vs Case Temperature



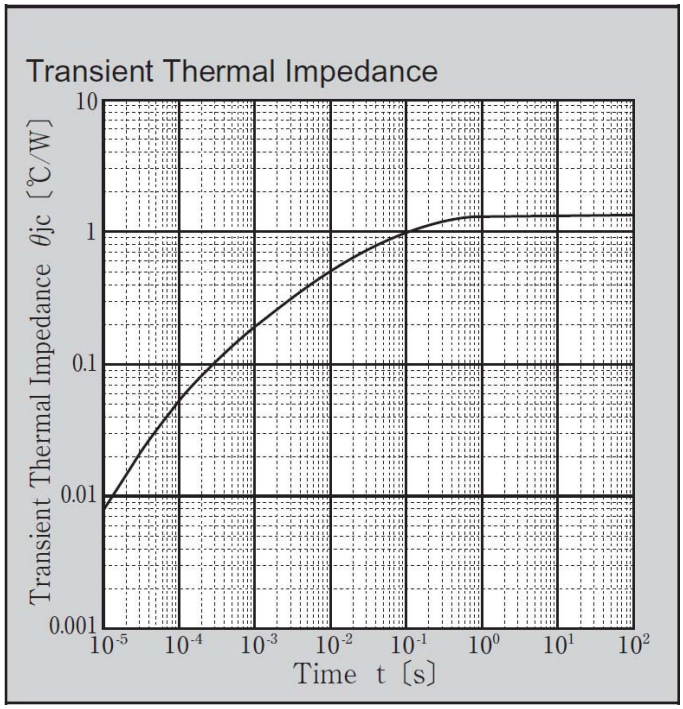
Gate Threshold Voltage vs Case Temperature



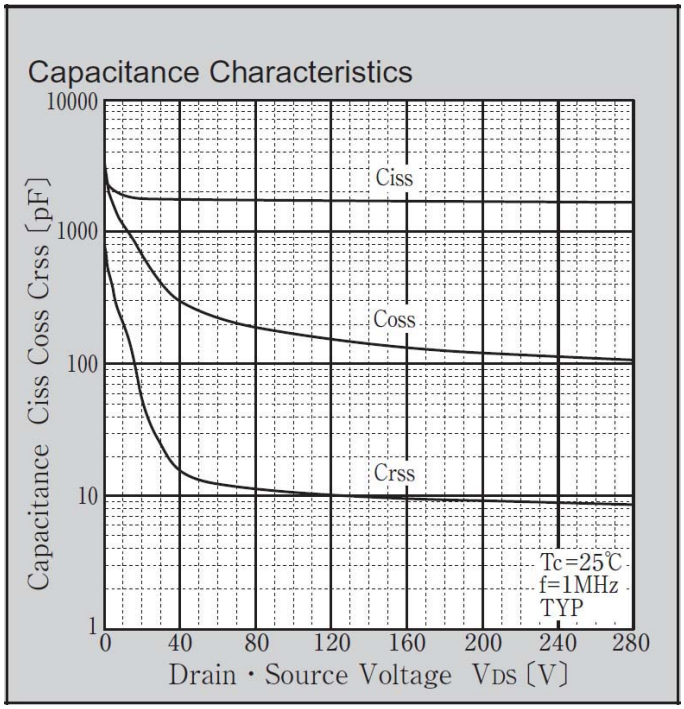
Safe Operating Area



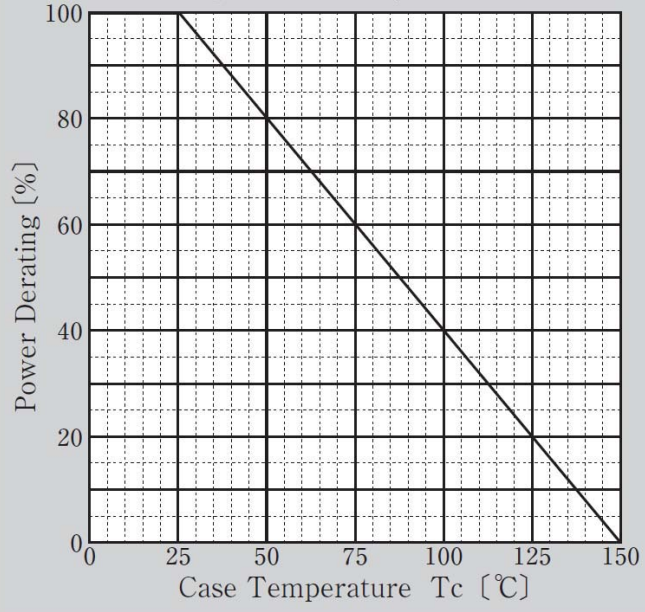
Transient Thermal Impedance



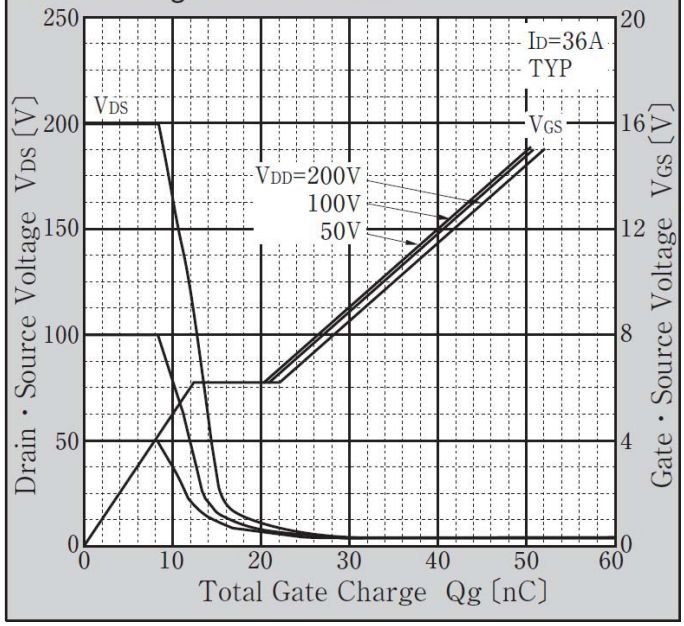
Capacitance Characteristics



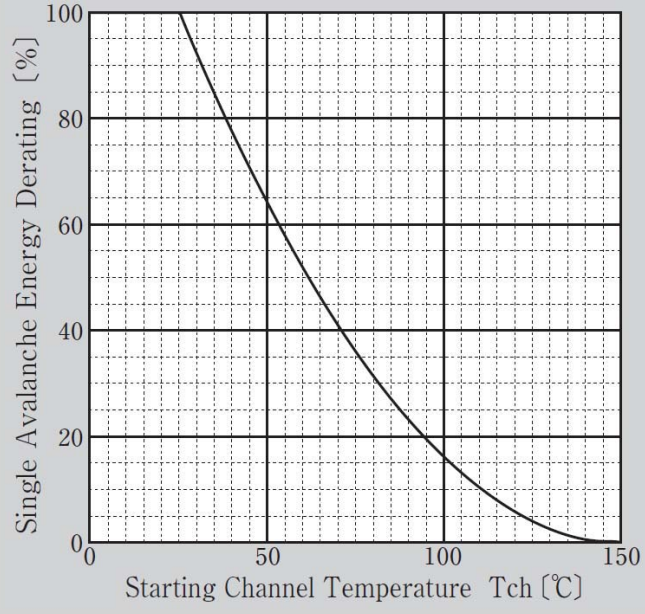
Power Derating - Case Temperature



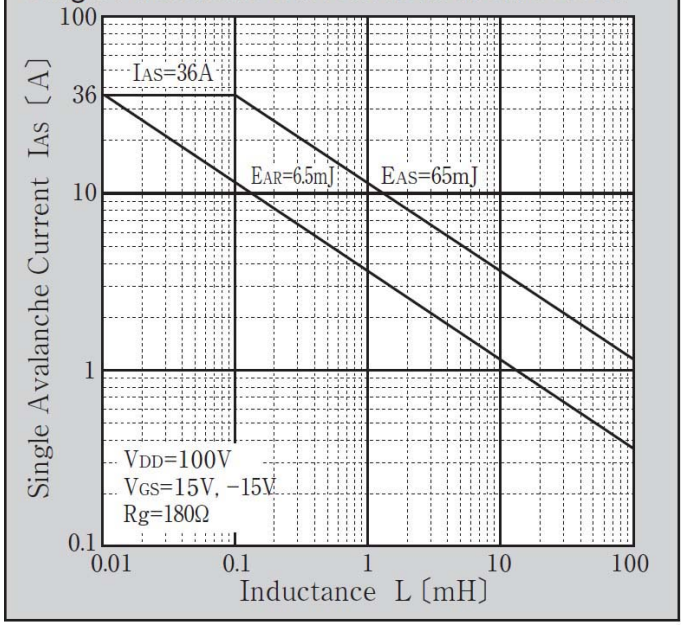
Gate Charge Characteristics



Single Avalanche Energy Derating vs Channel Temperature



Single Avalanche Current vs Inductive Load



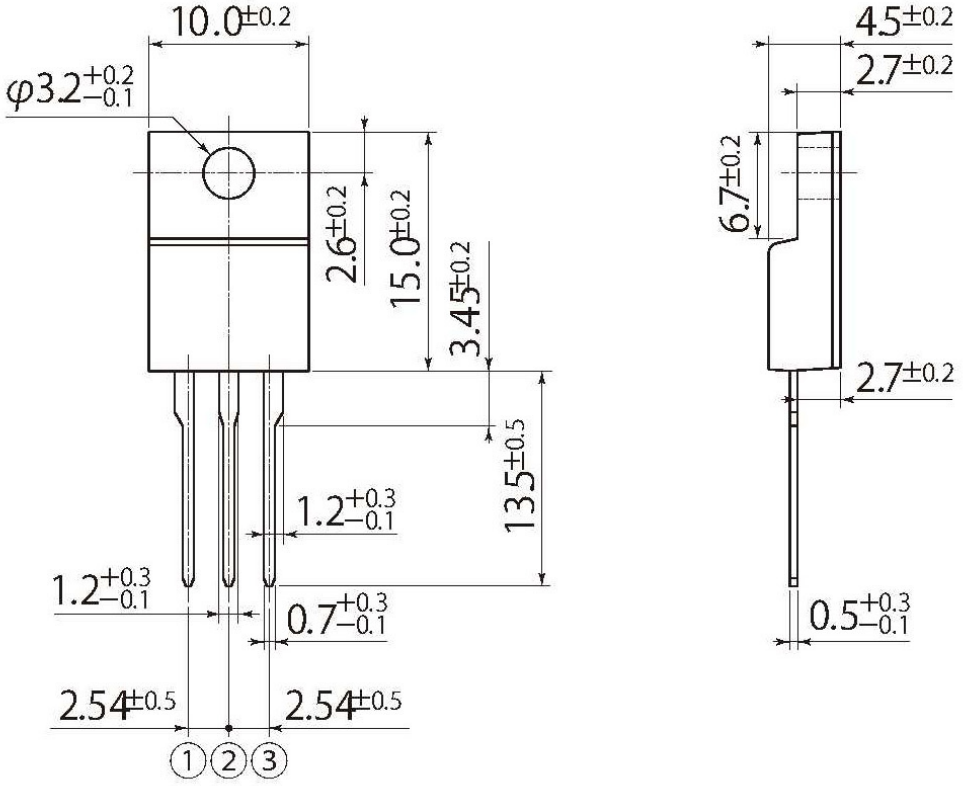
Outline Dimensions

unit:mm

scale: 2/1

J8

JEDEC Code	–
JEITA Code	SC-91
House Name	FTO-220AG(3pin)



Notes

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