

## P30FE4SLK

# Power MOSFETs 40V, 30A, N-channel

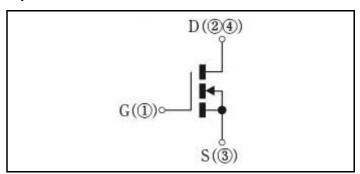
#### **Feature**

- N-channel
- SMD
- Low Ron
- 4.5V Gate Drive
- Low Capacitance
- Based on AEC-Q102
- · Pb free terminal
- RoHS:Yes

## **OUTLINE**

Package (House Name): FE
Package (JEDEC Code): TO-252AB similar
Package (JEITA Code): SC-63

## **Equivalent circuit**



## Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 175	°C
Channel tempertature	Tch		175	°C
Drain-source voltage	$V_{DSS}$		40	٧
Gate-source voltage	$V_{GSS}$		±20	V
Continuous drain current(DC)	I <sub>D</sub>		30	Α
Continuous drain current(Peak)	I <sub>DP</sub>	Pulse width 10µs, duty=1/100	90	Α
Total power dissipation	P <sub>T</sub>		44	W
Single avalanche current	I <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	27	Α
Single avalanche energy	E <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	36	mJ

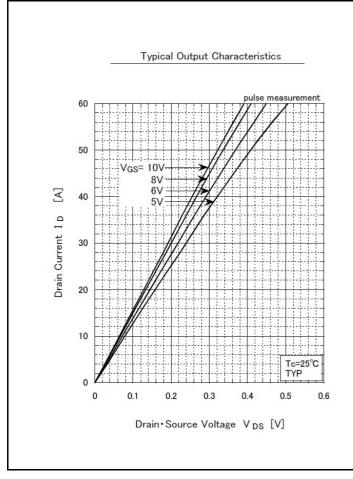
<sup>\* :</sup> See the original Specifications

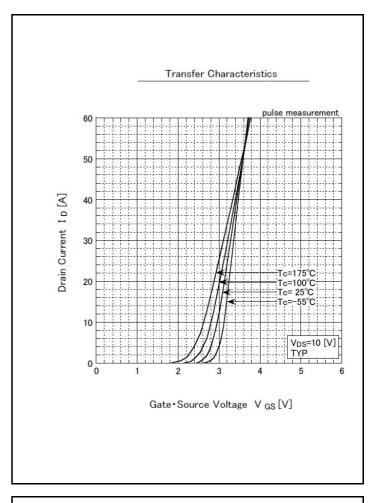
## **Electrical Characteristics** (unless otherwise specified : Tc=25°C)

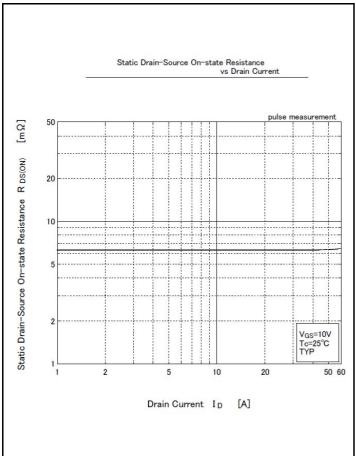
Item	Symbol	Conditions		Ratings		
			MIN	TYP	MAX	Unit
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	ID=1mA, VGS=0V	40			V
Zero gate voltage drain current	I <sub>DSS</sub>	VDS=40V, VGS=0V			1	μΑ
Gate-source leakage current	I <sub>GSS</sub>	VGS=±20V, VDS=0V			±0.1	μΑ
Forward transconductance	9fs	ID=15A, VDS=10V	10			S
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=15A, VGS=10V		0.0063	0.008	Ω
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=15A, VGS=4.5V		0.0084	0.0113	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	1.5	2	2.5	V
Source-drain diode forward voltage	$V_{SD}$	IS=30A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			3.4	°C/W
Total gate charge	Qg	VDD=32V, VGS=10V, ID=30A		44		nC
Gate to source charge	Qgs	VDD=32V, VGS=10V, ID=30A		9		nC
Gate to drain charge	Qgd	VDD=32V, VGS=10V, ID=30A		12		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		2020		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		159		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		300		pF
Turn-on delay time	td(on)	ID=15A, RL=1.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		7		ns
Rise time	tr	ID=15A, RL=1.33 $\Omega$ , VDD=20V, Rg=0 $\Omega$ , VGS(+)=10V, VGS(-)=0V		19		ns
Turn-off delay time	td(off)	ID=15A, RL=1.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		27		ns
Fall time	tf	ID=15A, RL=1.33Ω, VDD=20V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		11		ns
Diode reverse recovery time	trr	IF=30A, VGS=0V, di/dt=100A/μs		38		ns
Diode reverse recovery charge	Qrr	IF=30A, VGS=0V, di/dt=100A/μs		35		nC

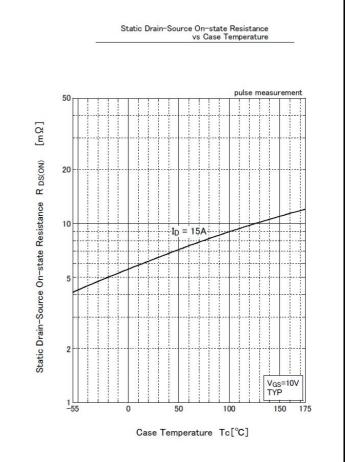
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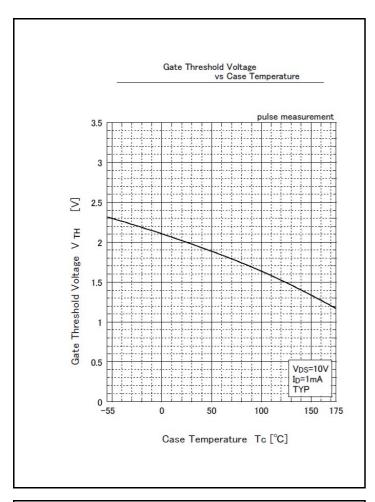
## **CHARACTERISTIC DIAGRAMS**

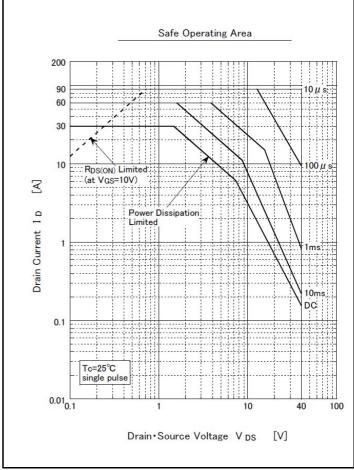


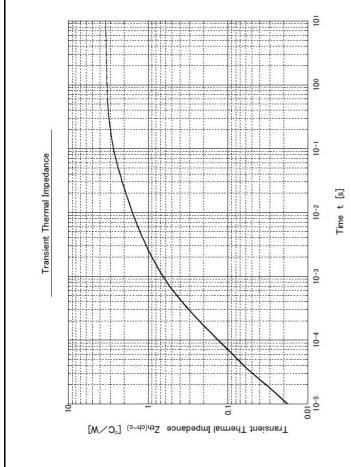


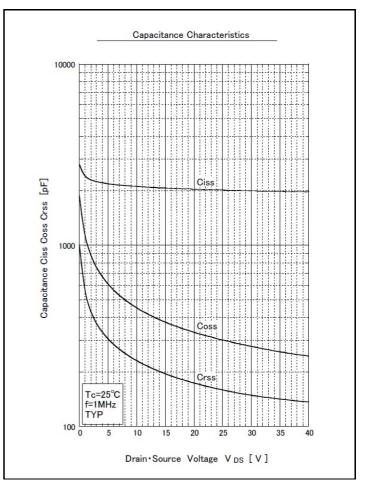


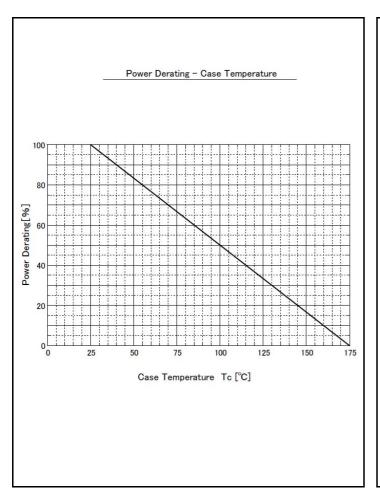


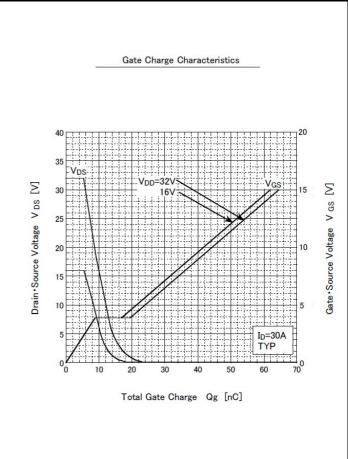


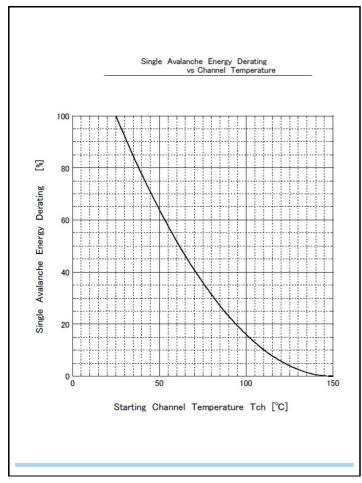








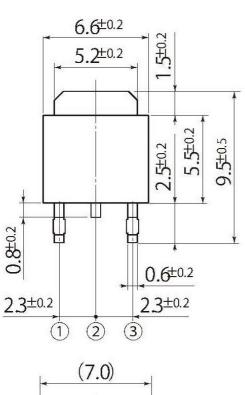


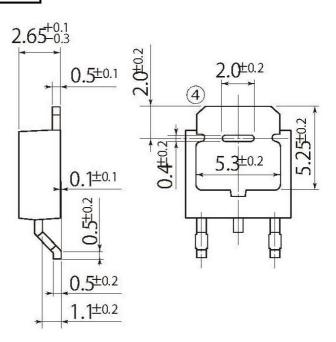


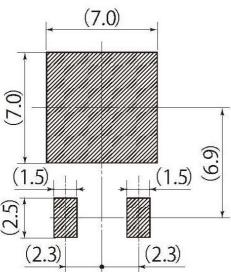
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G3

JEDEC Code	TO-252AB similar		
JEITA Code	SC-63		
House Name	FE		







Referential Soldering Pad

 $<sup>\</sup>bullet$  Optimize soldering pad to the board design and soldering condition.

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