ShinDengen

P0R5B60HP2

Power MOSFETs
600V, 0.5A, N-channel

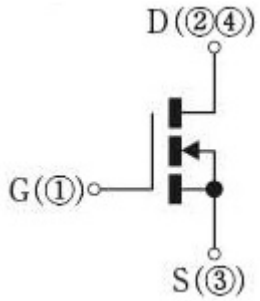
Feature

- N-channel
- SMD
- High Voltage
- Low Capacitance
- High Avalanche Durability, High di/dt Durability
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FB
Package (JEDEC Code): TO-252AA

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 150	℃
Channel tempertature	Tch		150	℃
Drain-source voltage	V_{DSS}		600	V
Gate-source voltage	V_{GSS}		±30	V
Continuous drain current(DC)	I_D		0.5	A
Continuous drain current(Peak)	I_{DP}	Pulse width 10μs, duty=1/100	2	A
Continuous source current(DC)	Is		0.5	A
Total power dissipation	P_T		35	W
Repetitive avalanche current	I_{AR}	Starting Tch=25℃ Tch≦150℃	0.5	A
Single avalanche energy	E_{AS}	Starting Tch=25℃ Tch≦150℃	5	mJ
Repetitive avalanche energy	E_{AR}	Starting Tch=25℃ Tch≦150℃	0.5	mJ
Drain-source diode di/dt strength	di/dt	Is=0.5A, Tc=25℃	350	A/μs

※ ：See the original Specifications

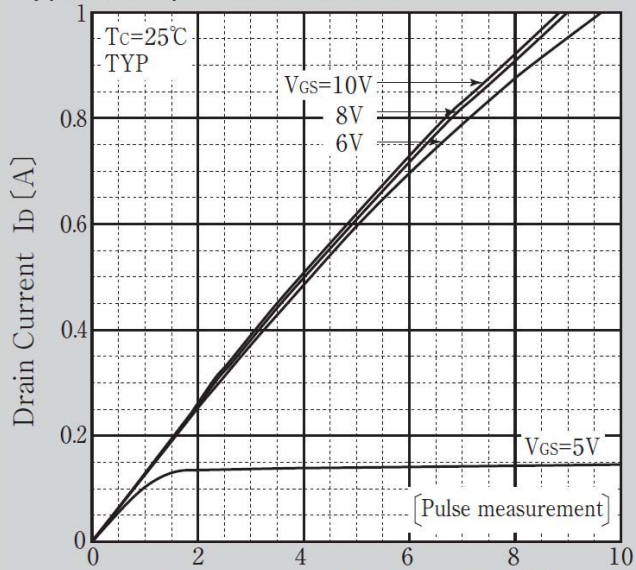
Electrical Characteristics (unless otherwise specified : Tc=25℃)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Drain-Source breakdown voltage	$V_{(BR)DSS}$	ID=1mA, VGS=0V	600			V
Zero gate voltage drain current	I_{DSS}	VDS=600V, VGS=0V			100	μA
Gate-source leakage current	I_{GSS}	VGS=±25V, VDS=0V			±10	μA
Forward transconductance	g_{fs}	ID=0.25A, VDS=10V	0.4	0.8		S
Static drain-source on-state resistance	$R_{DS(ON)}$	ID=0.25A, VGS=10V		8.3	10	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	3	3.75	4.5	V
Source-drain diode forward voltage	V_{SD}	IS=0.25A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			3.55	℃/W
Total gate charge	Qg	VDD=400V, VGS=10V, ID=0.5A		4.3		nC
Input capacitance	Ciss	VDS=50V, VGS=0V, f=1MHz		120		pF
Reverce transfer capacitnce	Crss	VDS=50V, VGS=0V, f=1MHz		3.3		pF
Output capacitance	Coss	VDS=50V, VGS=0V, f=1MHz		18		pF
Turn-on delay time	td(on)	ID=0.25A, RL=600Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		9		ns
Rise time	tr	ID=0.25A, RL=600Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		5		ns
Turn-off delay time	td(off)	ID=0.25A, RL=600Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		26		ns
Fall time	tf	ID=0.25A, RL=600Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		30		ns

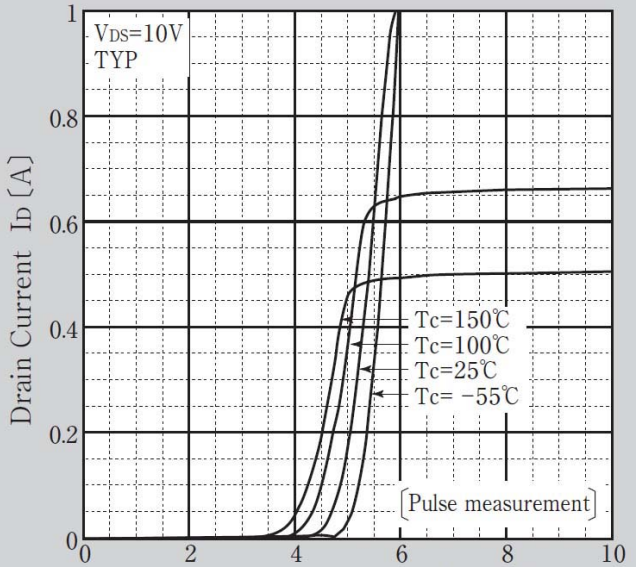
※ ：See the original Specifications

CHARACTERISTIC DIAGRAMS

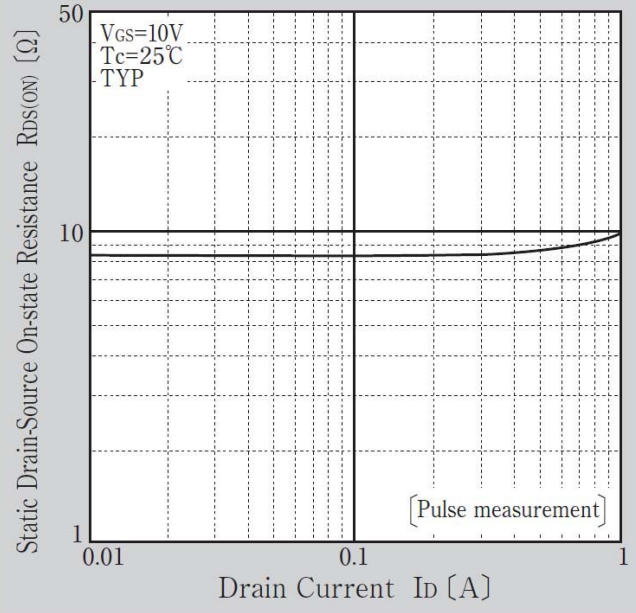
Typical Output Characteristics



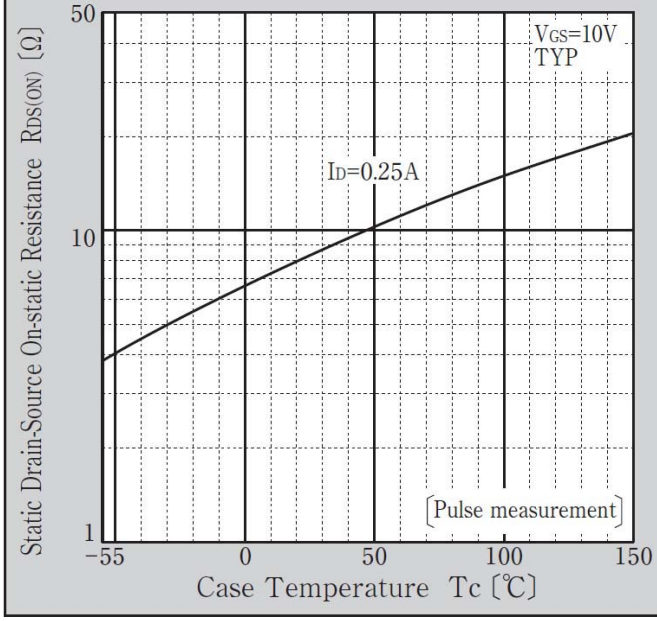
Transfer Characteristics



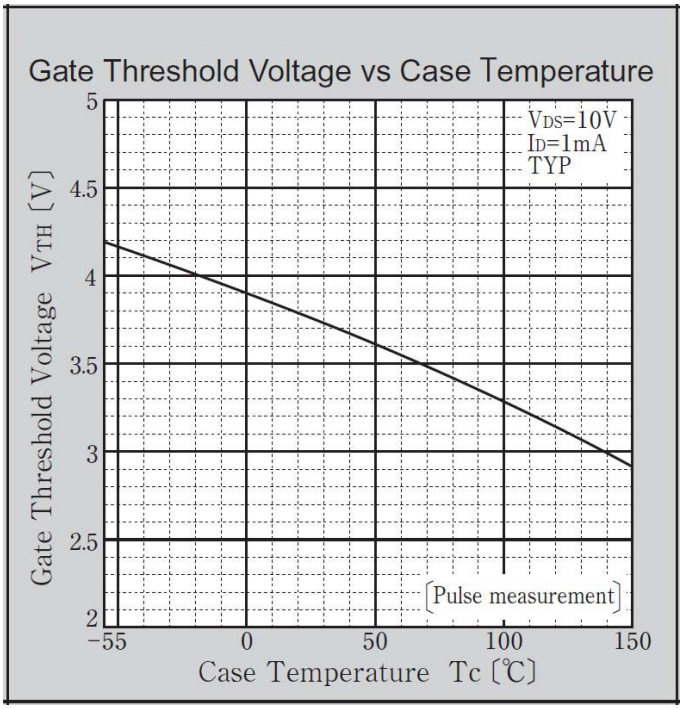
Static Drain-Source On-state Resistance vs Drain Current



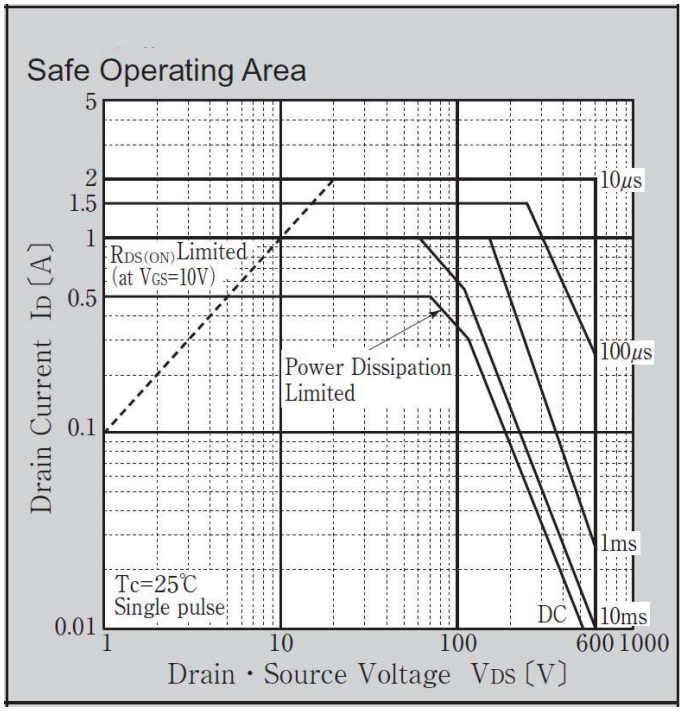
Static Drain-Source On-static Resistance vs Case Temperature



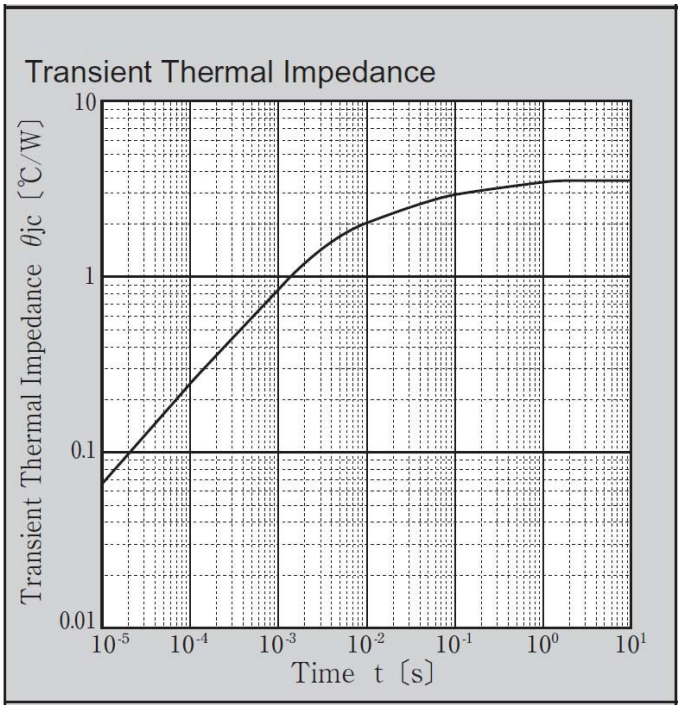
Gate Threshold Voltage vs Case Temperature



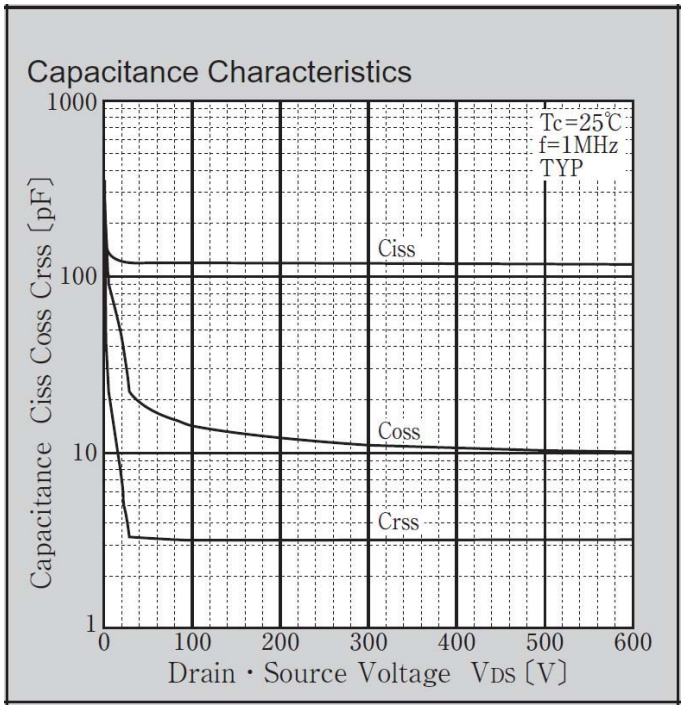
Safe Operating Area



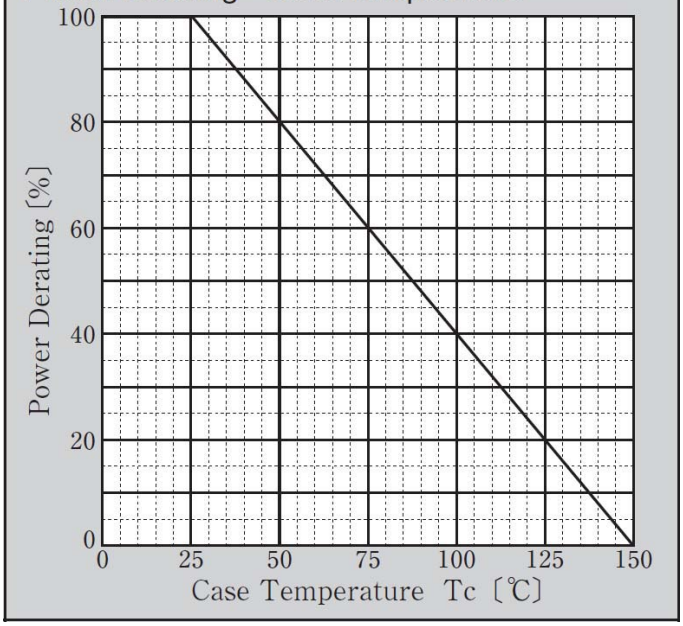
Transient Thermal Impedance



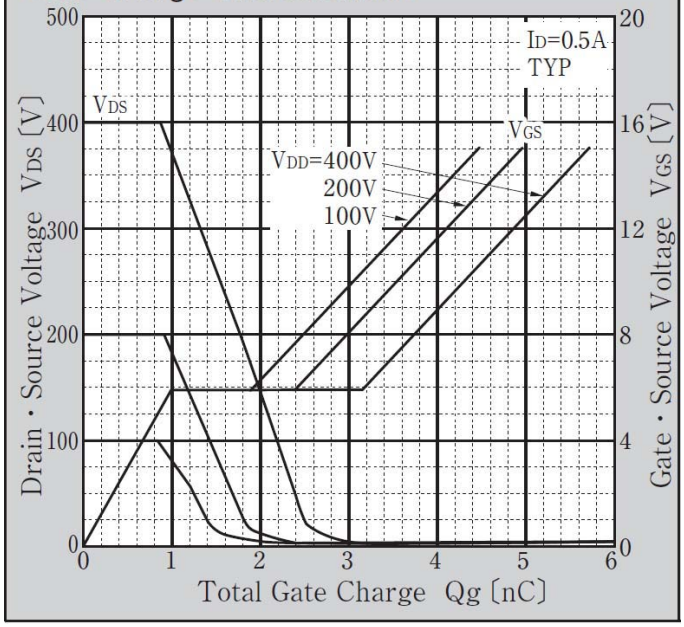
Capacitance Characteristics



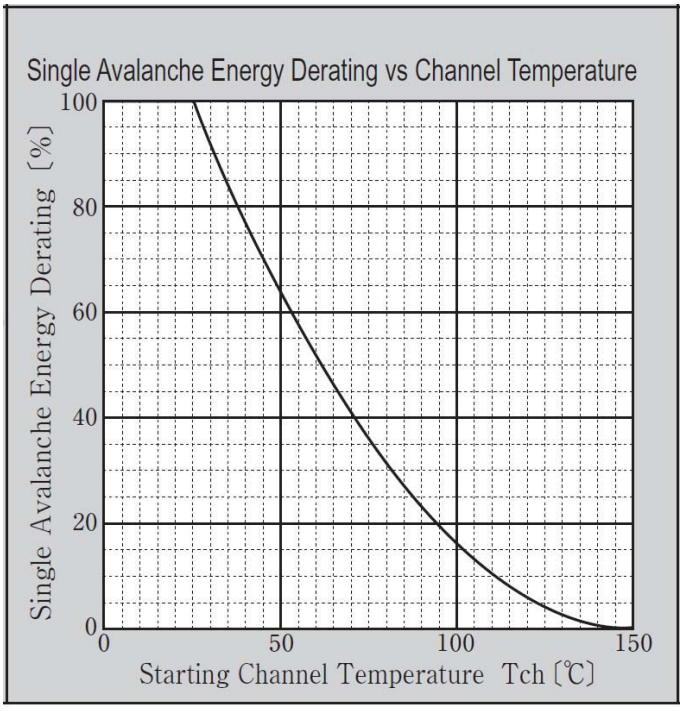
Power Derating - Case Temperature



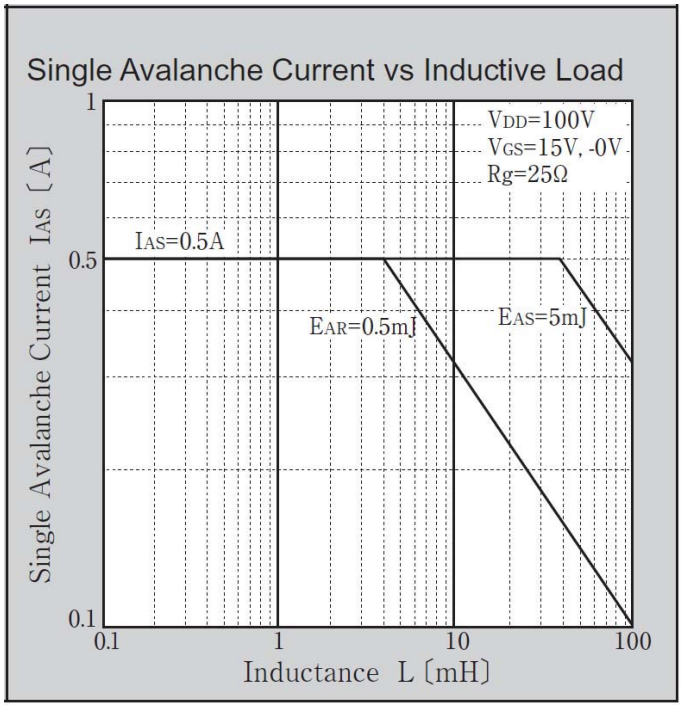
Gate Charge Characteristics



Single Avalanche Energy Derating vs Channel Temperature



Single Avalanche Current vs Inductive Load



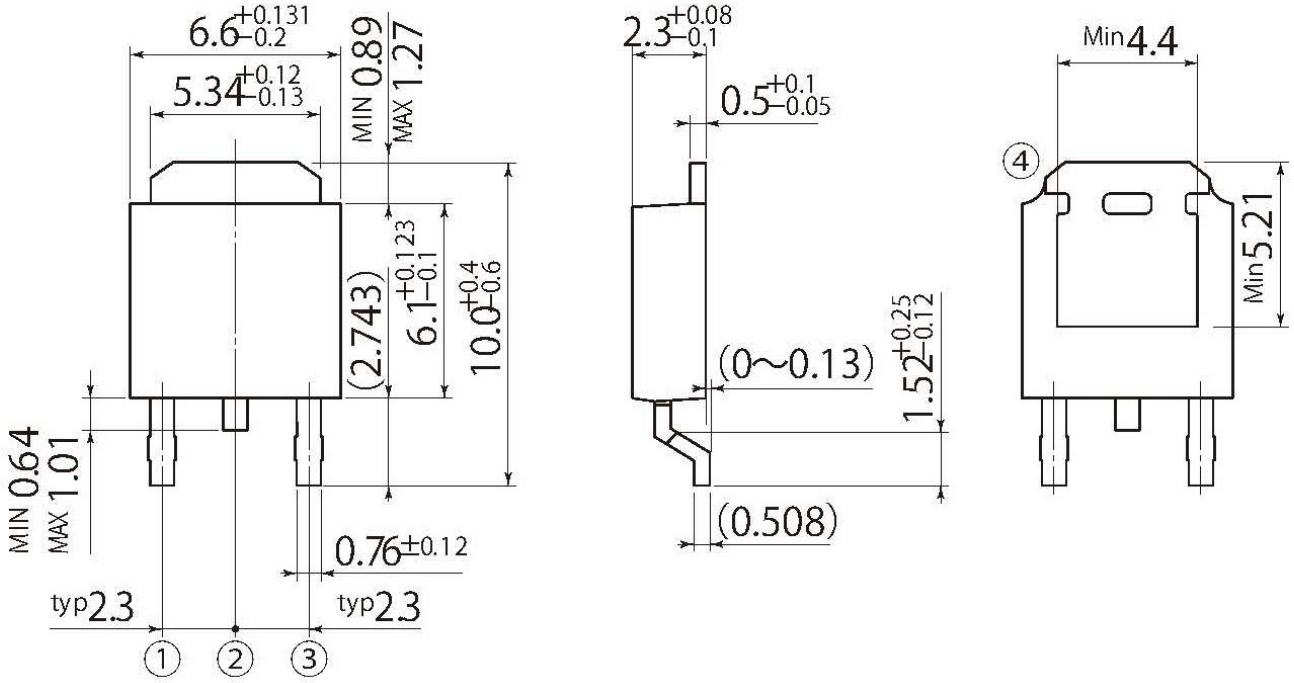
Outline Dimensions

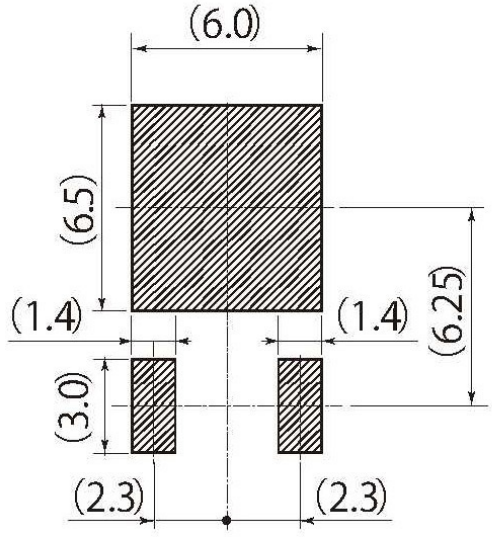
unit:mm

scale: 4/1

G2

JEDEC Code	TO-252AA
JEITA Code	–
House Name	FB





Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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