

# **DG1M3**

# **Schottky Barrier Diodes 30V, 1A**

## **Feature**

- Ultra-small SMD
- · Ultra thin PKG
- · Low V<sub>F</sub>
- Low I<sub>R</sub>
- · Based on AEC-Q101
- · Pb free terminal
- RoHS:Yes

# **OUTLINE**

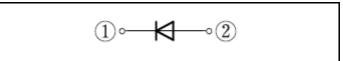
Package (House Name): G1F

Package (JEDEC Code): DO-219AB similar

Package (JEITA Code): SC-109



# **Equivalent circuit**



**Absolute Maximum Ratings** (unless otherwise specified : TI=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		150	°C
Repetitive peak reverse voltage	$V_{RRM}$		30	V
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, Ta=27°C *	1	Α
Surge forward current	I <sub>FSM</sub>	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C	20	А

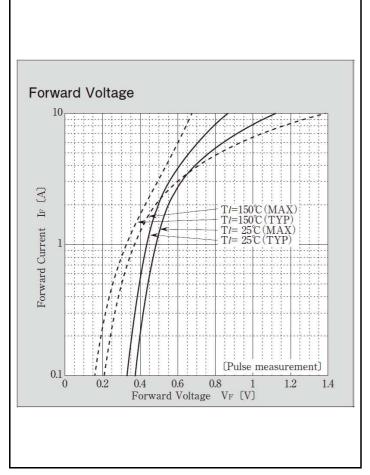
<sup>\* :</sup> See the original Specifications

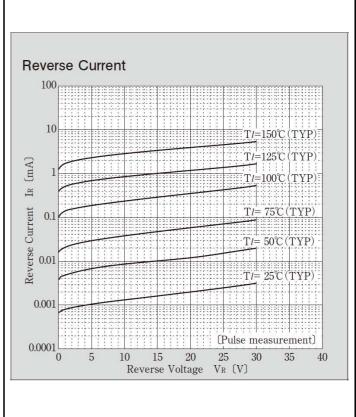
# **Electrical Characteristics** (unless otherwise specified : TI=25°C)

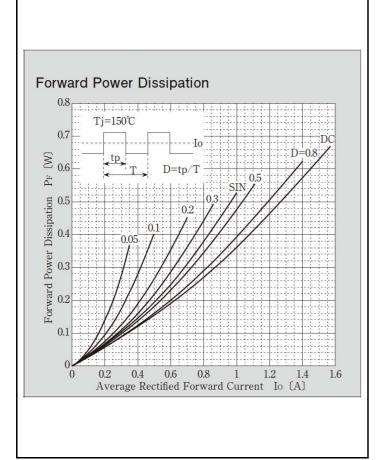
Item	Symbol	Conditions	Ratings			Unit
		Conditions		TYP	MAX	Cilit
Forward voltage	V <sub>F</sub>	IF=0.7A, Pulse measurement			0.46	V
Forward voltage	V <sub>F</sub>	IF=0.2A, Pulse measurement			0.4	V
Reverse current	I <sub>R</sub>	VR=30V, Pulse measurement			0.05	mA
Total capacitance	Ct	f=1MHz, VR=10V		36		pF
Thermal resistance	Rth(j-l)	Junction to lead, On alumina substrate *			20	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On alumina substrate *			70	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On paper phenol substrate *		120	°C/W	
Thermal resistance	Rth(j-a)	Junction to ambient, On paper phenol substrate *			210	°C/W

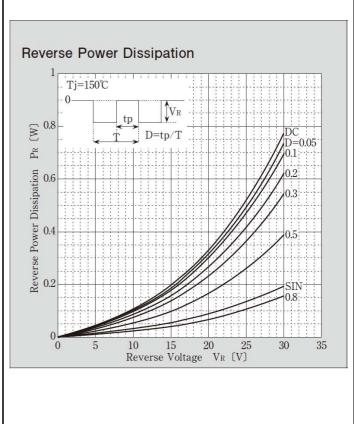
<sup>\* :</sup>See the original Specifications

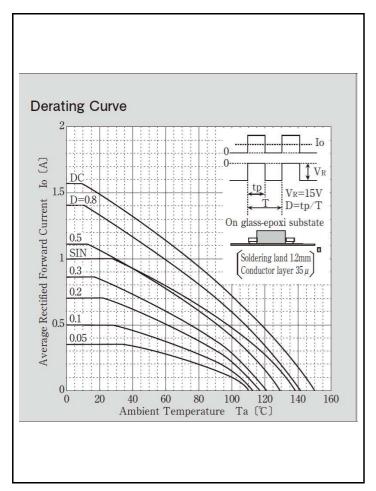
# **CHARACTERISTIC DIAGRAMS**

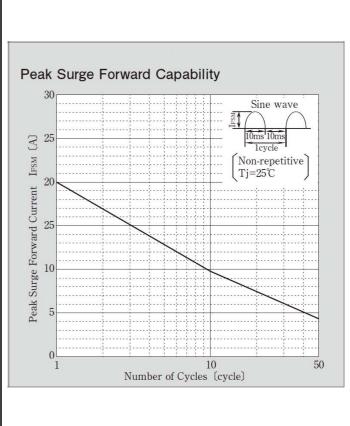


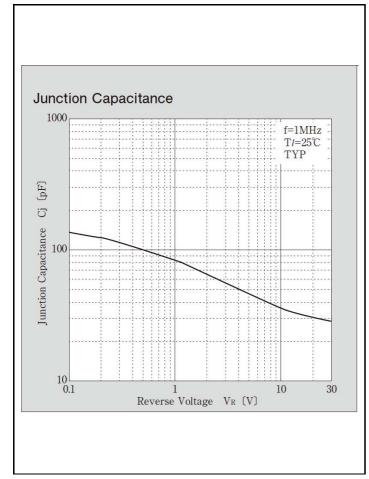








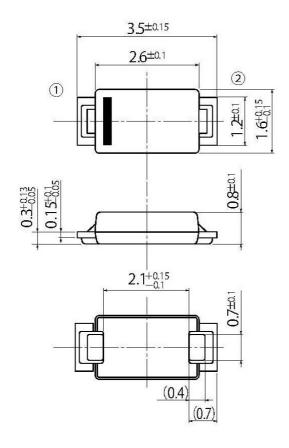




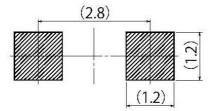
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JEDEC Code	DO-219AB similar		
JEITA Code	SC-109		
House Name	G1F		







Referential Soldering Pad

<sup>•</sup> Optimize soldering pad to the board design and soldering condition.

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#### (Special applications)

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