

DE3L20UA

Fast Recovery Diodes

200V, 3A

Feature

- SMD
- High Recovery Speed
- Pb free terminal
- RoHS:Yes

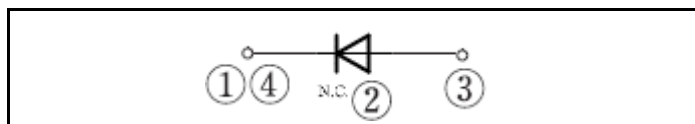
OUTLINE

Package (House Name): E-pack

Package (JEITA Code): SC-63



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Junction temperature	T _j		150	°C
Repetitive peak reverse voltage	V _{RRM}		200	V
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, T _c =137°C	3	A
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, On alumina substrate, T _a =25°C ※	2.4	A
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive 1 cycle, Peak value, T _j =25°C	60	A

※ :See the original Specifications

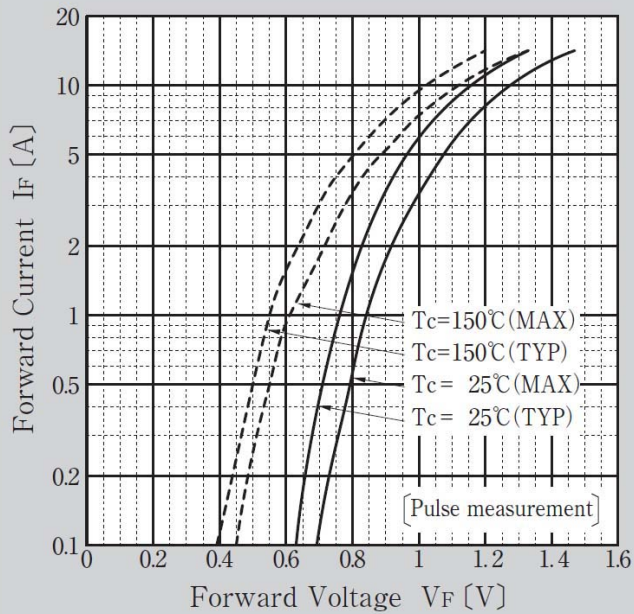
Electrical Characteristics (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V _F	I _F =3A, Pulse measurement			0.98	V
Reverse current	I _R	V _R =200V, Pulse measurement			10	μA
Reverse recovery time	t _{rr}	I _F =0.5A, I _R =1A			35	ns
Total capacitance	C _t	f=1MHz, V _R =10V		56		pF
Thermal resistance	R _{th(j-c)}	Junction to case			4	°C/W
Thermal resistance	R _{th(j-a)}	Junction to ambient			55	°C/W

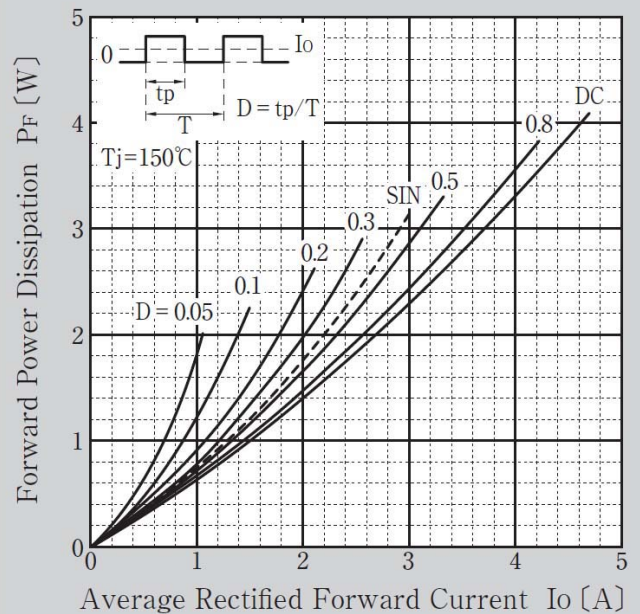
* :See the original Specifications

CHARACTERISTIC DIAGRAMS

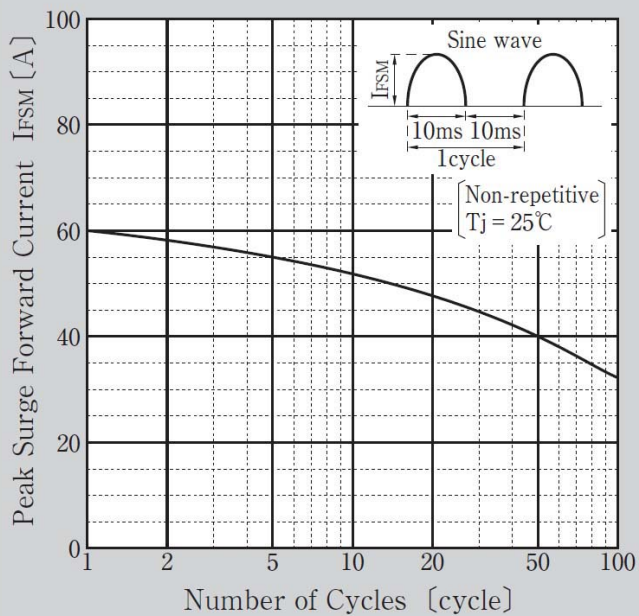
Forward Voltage



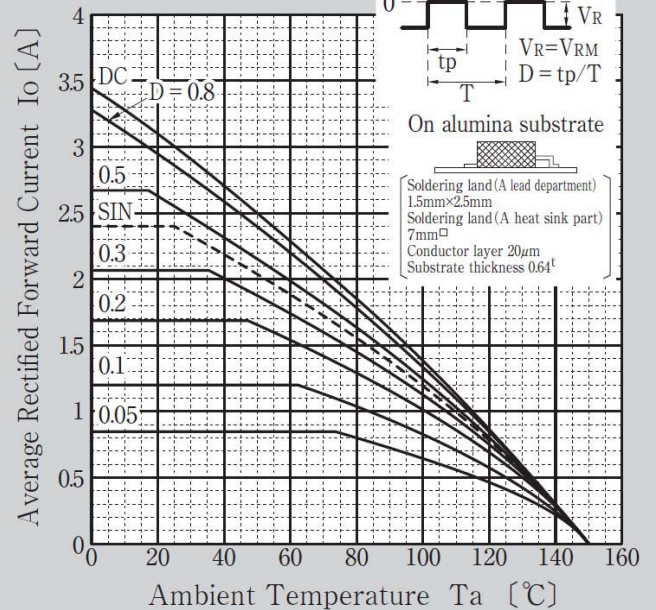
Forward Power Dissipation



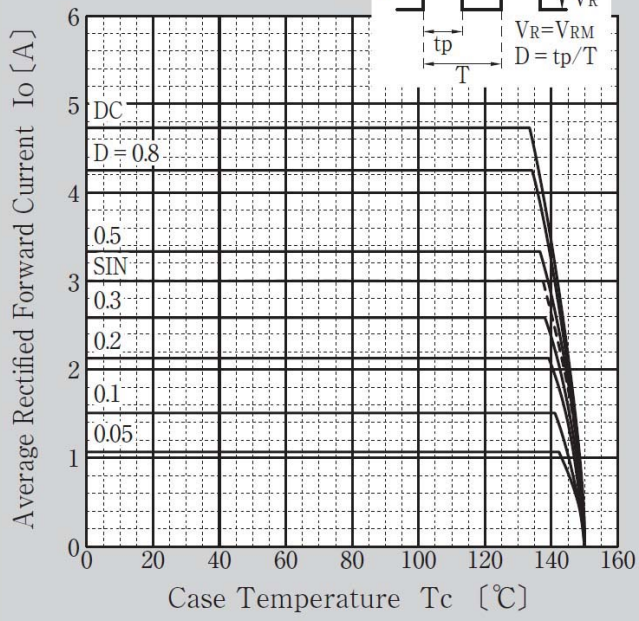
Peak Surge Forward Current Capability



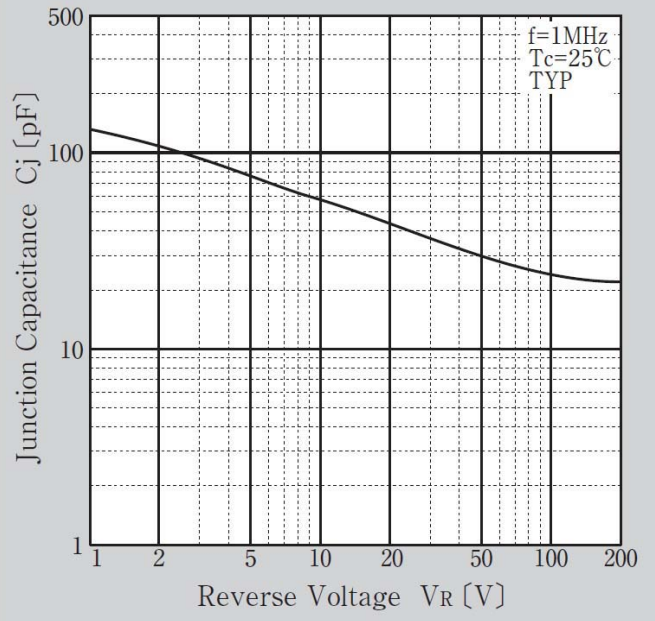
Derating Curve



Derating Curve

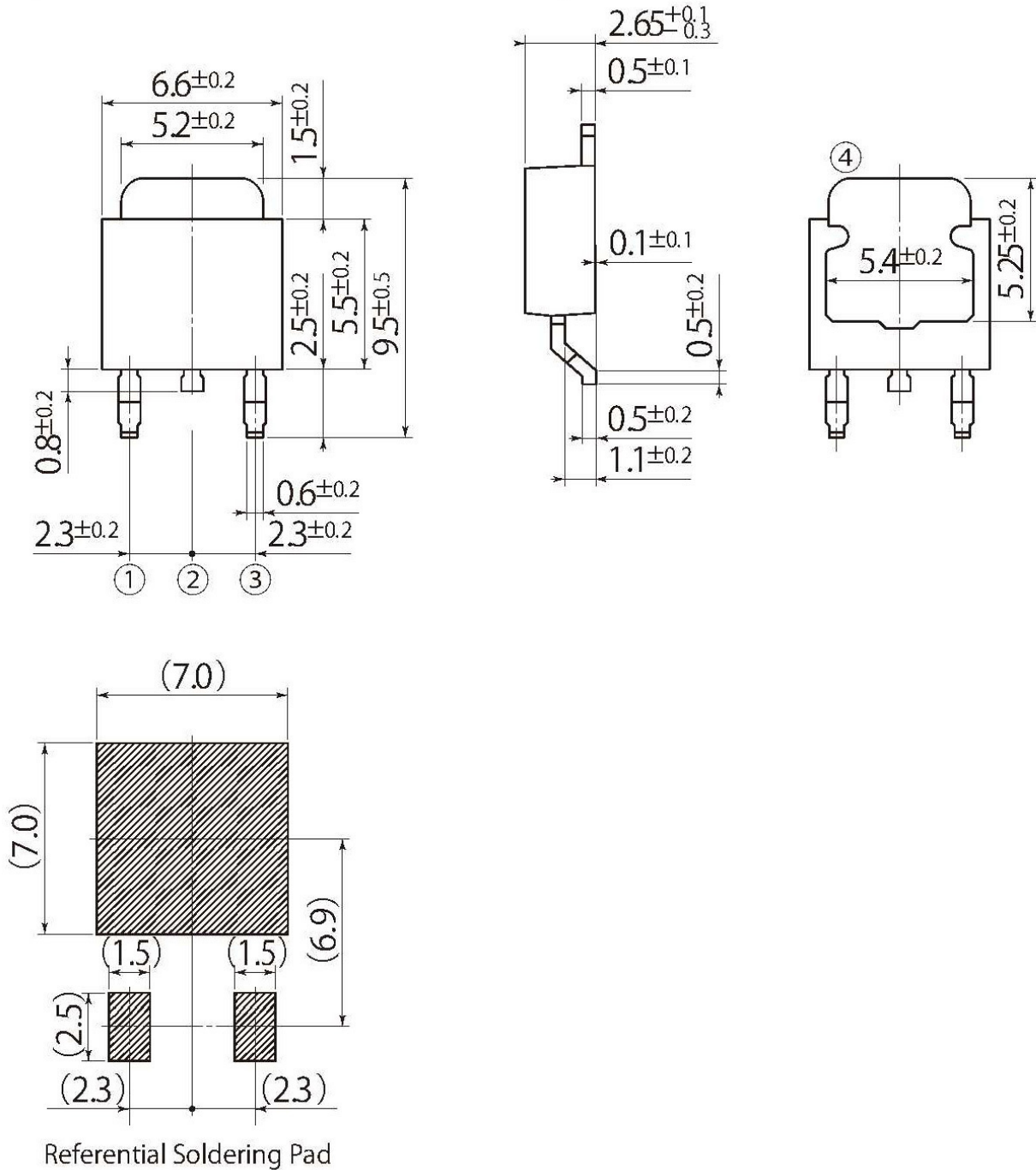


Junction Capacitance



G1

JEDEC Code	-
JEITA Code	SC-63
House Name	E-pack



• Optimize soldering pad to the board design and soldering condition.

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