

D3CE6S

Schottky Barrier Diodes 60V, 3A

Feature

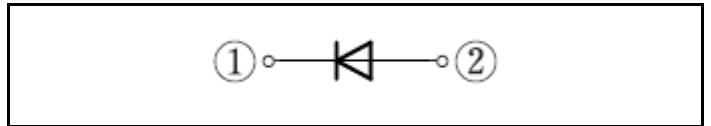
- Ultra-small SMD
- Ultra thin PKG
- Low V_F
- Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): CE
Package (JEITA Code): SC-110B



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	T _{stg}		-55 to 150	°C
Junction temperature	T _j		-55 to 150	°C
Repetitive peak reverse voltage	V _{RRM}		60	V
Repetitive peak surge reverse voltage	V _{RRSM}	Pulse width 0.5ms, duty=1/40	65	V
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, Tl=112°C	3	A
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C ※	1.4	A
Average forward current	I _{F(AV)}	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C ※	0.9	A
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C	100	A

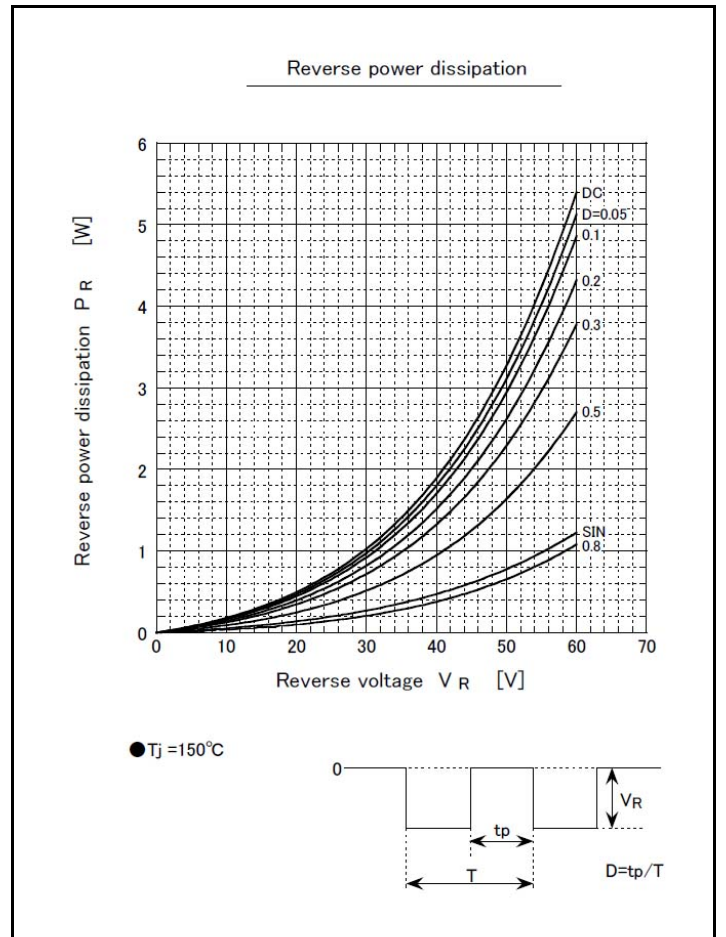
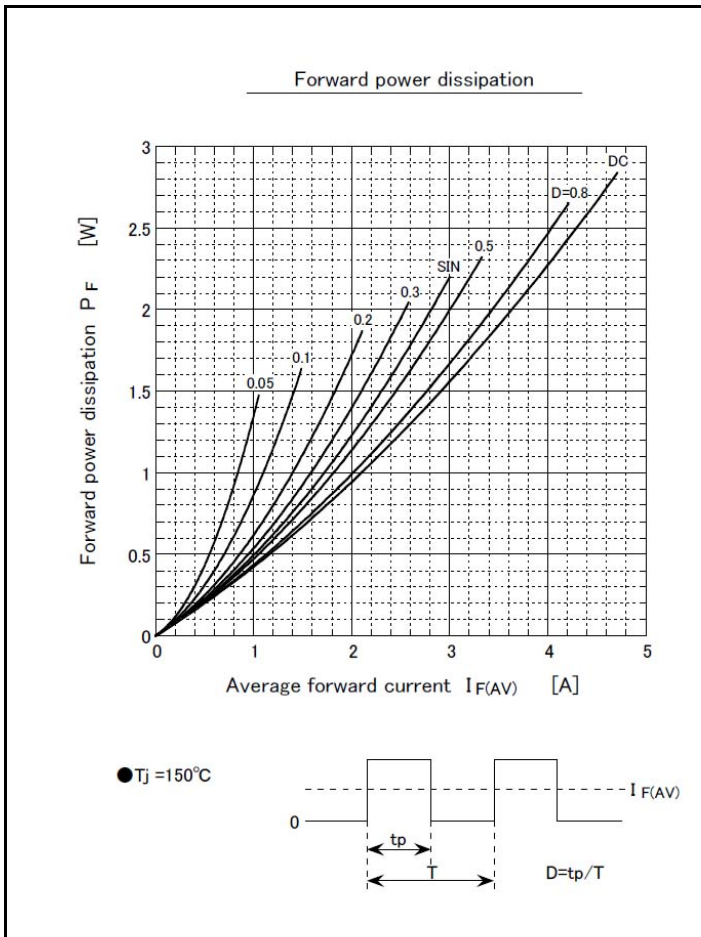
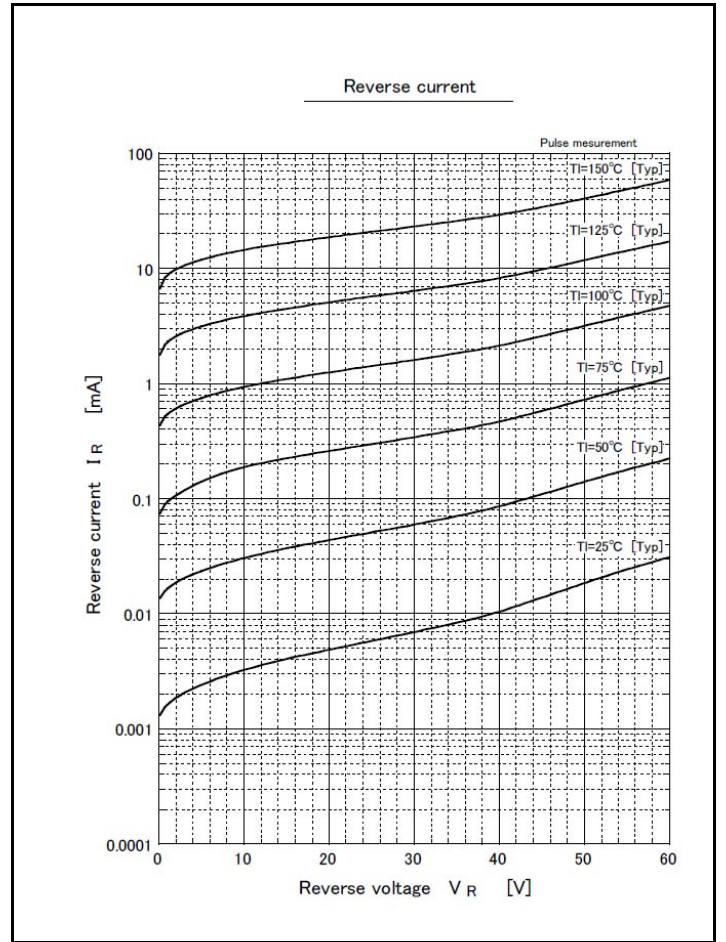
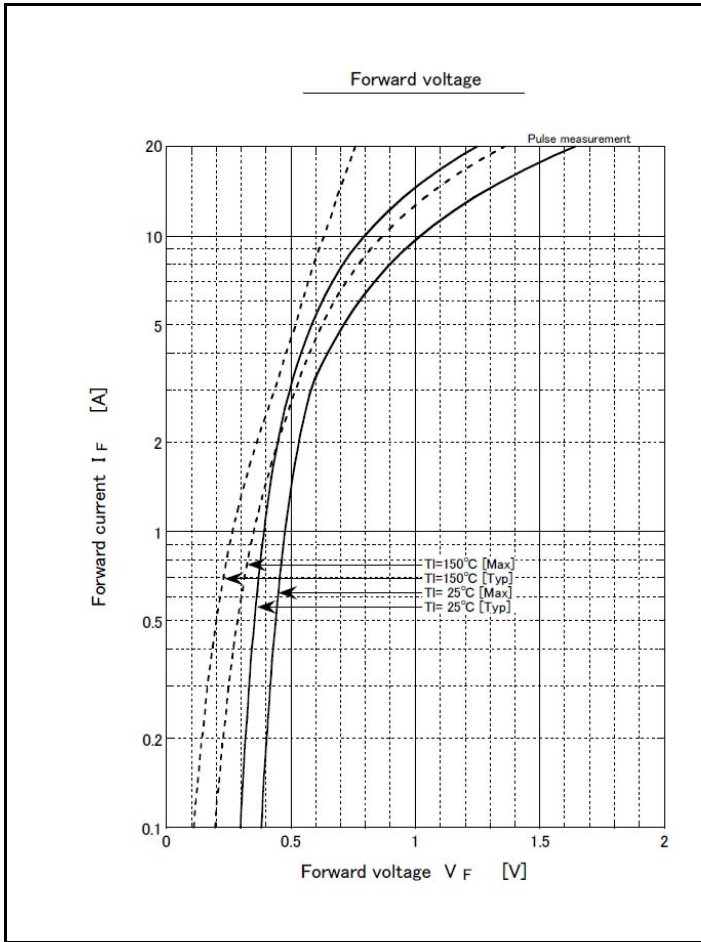
※ : See the original Specifications

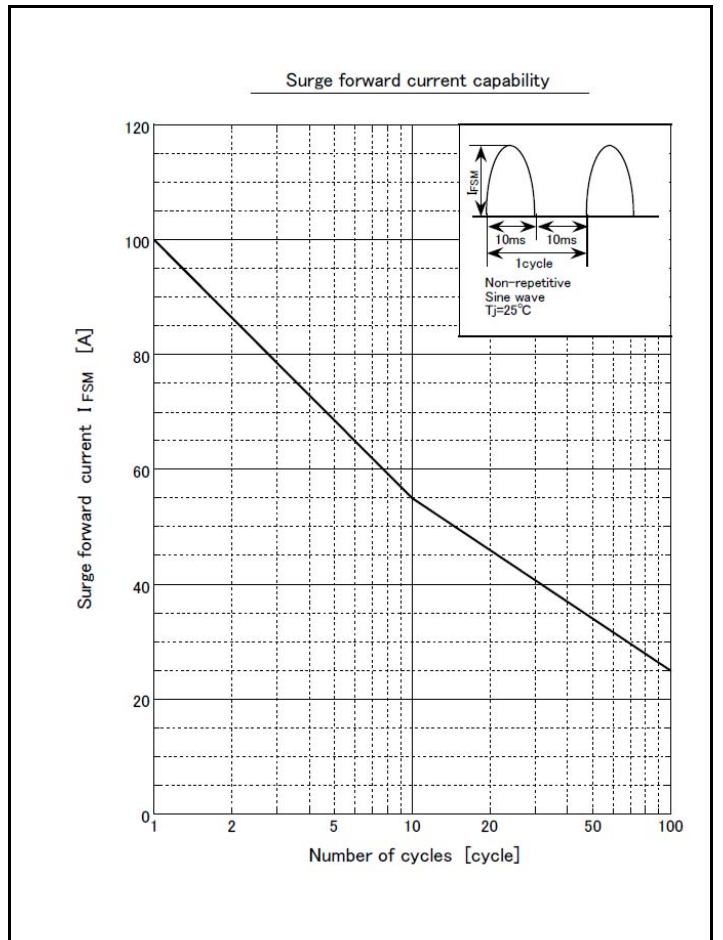
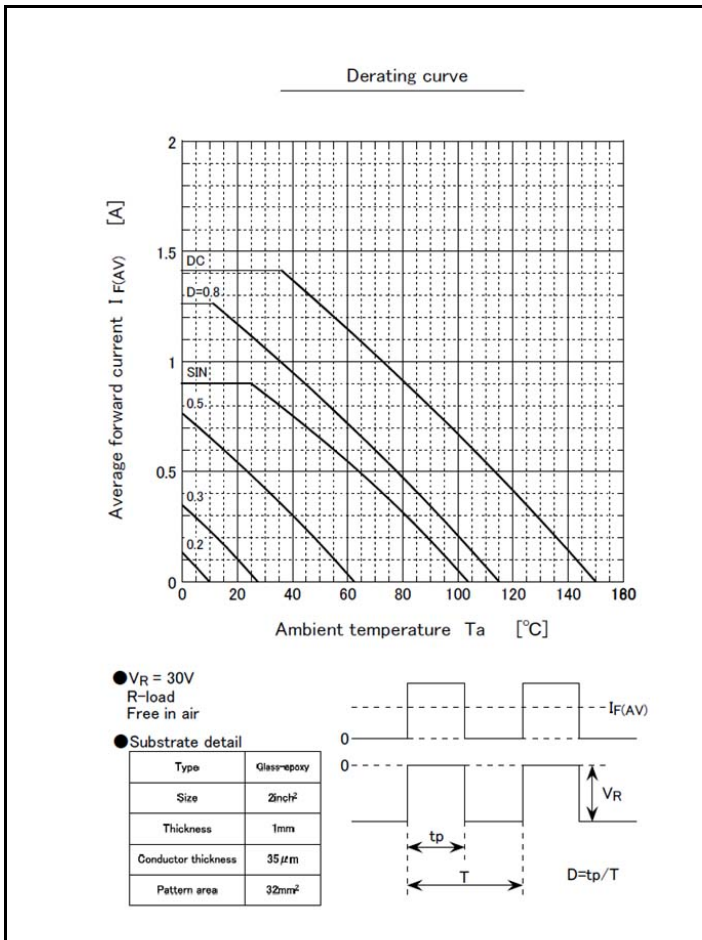
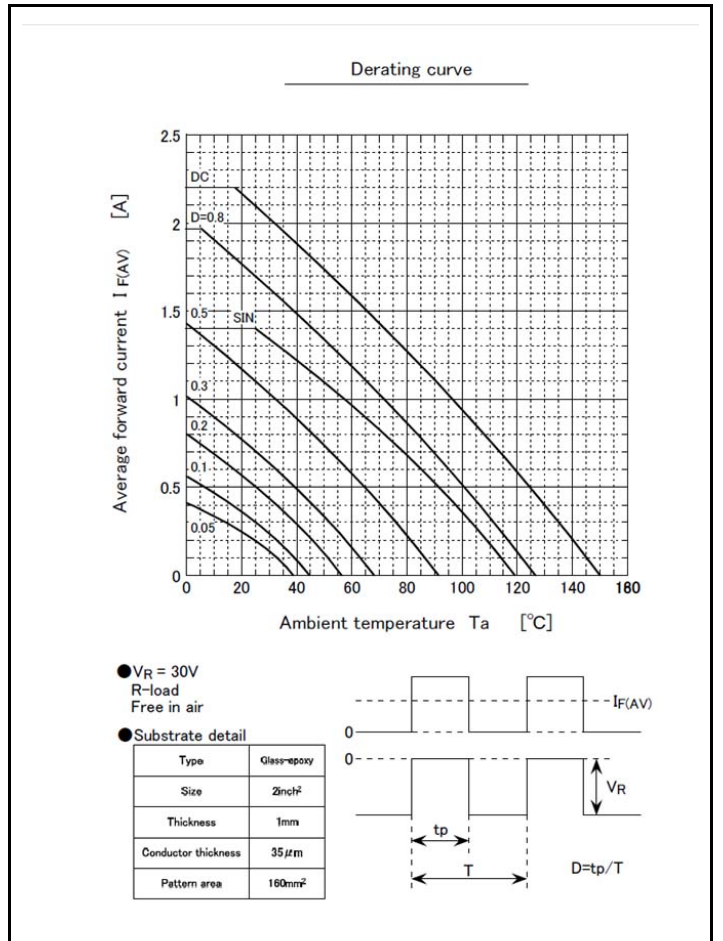
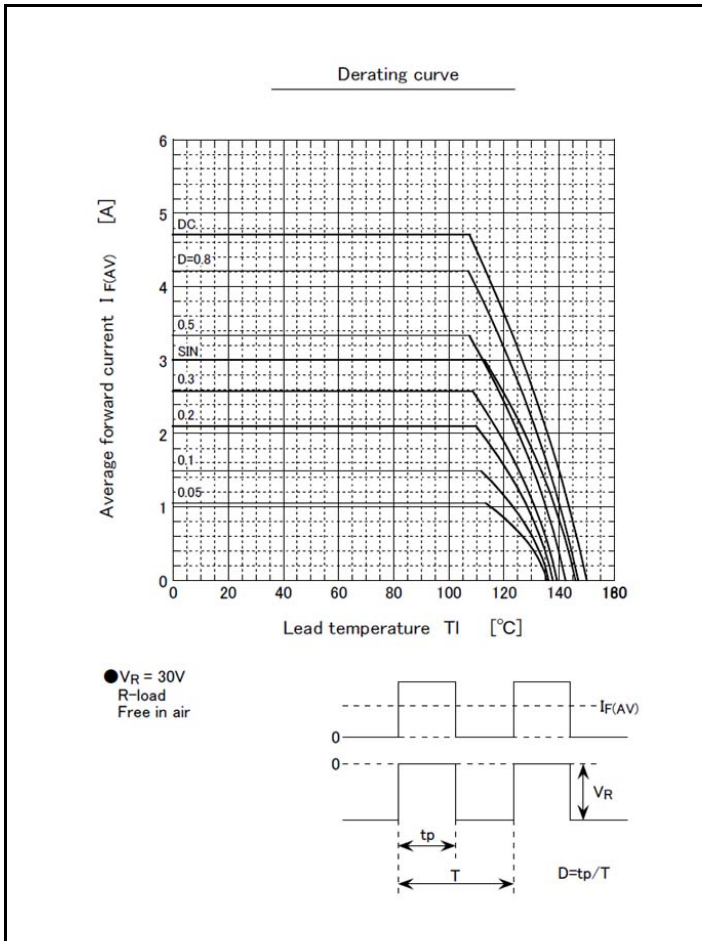
Electrical Characteristics (unless otherwise specified : Tl=25°C)

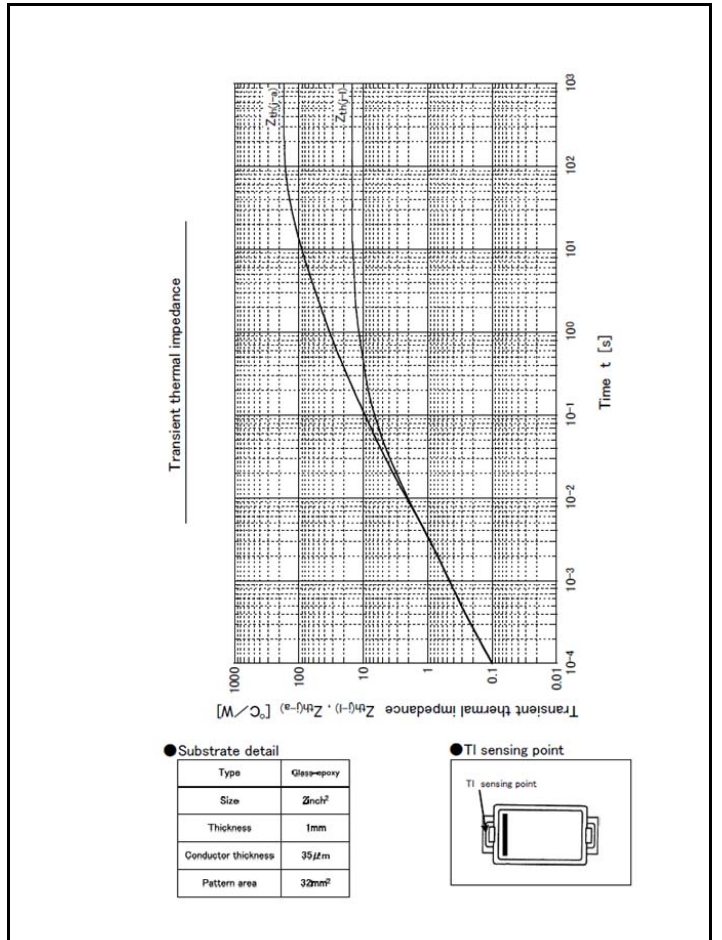
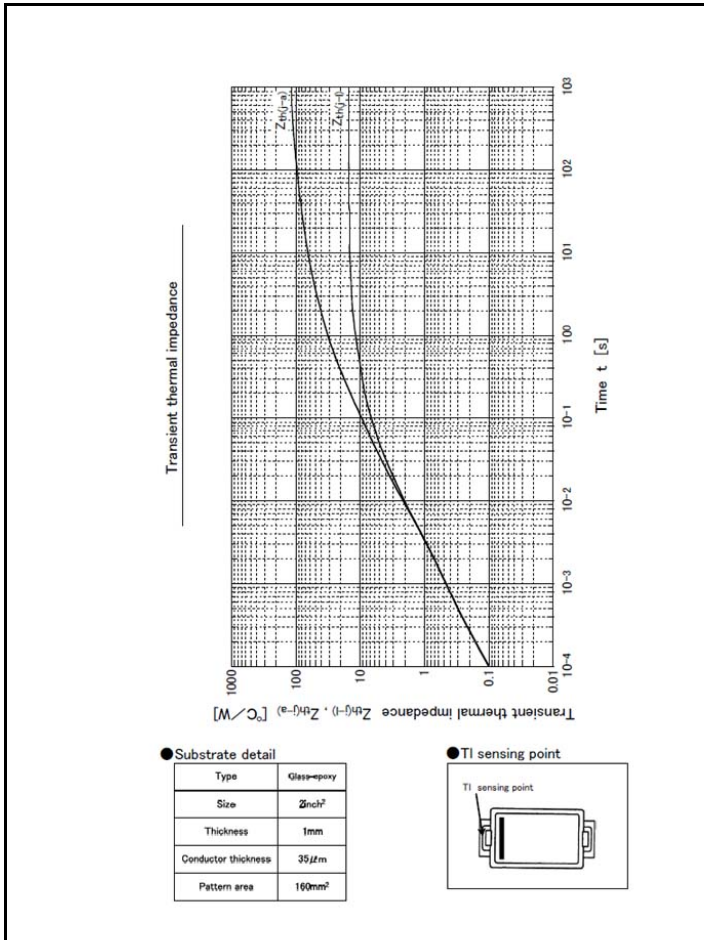
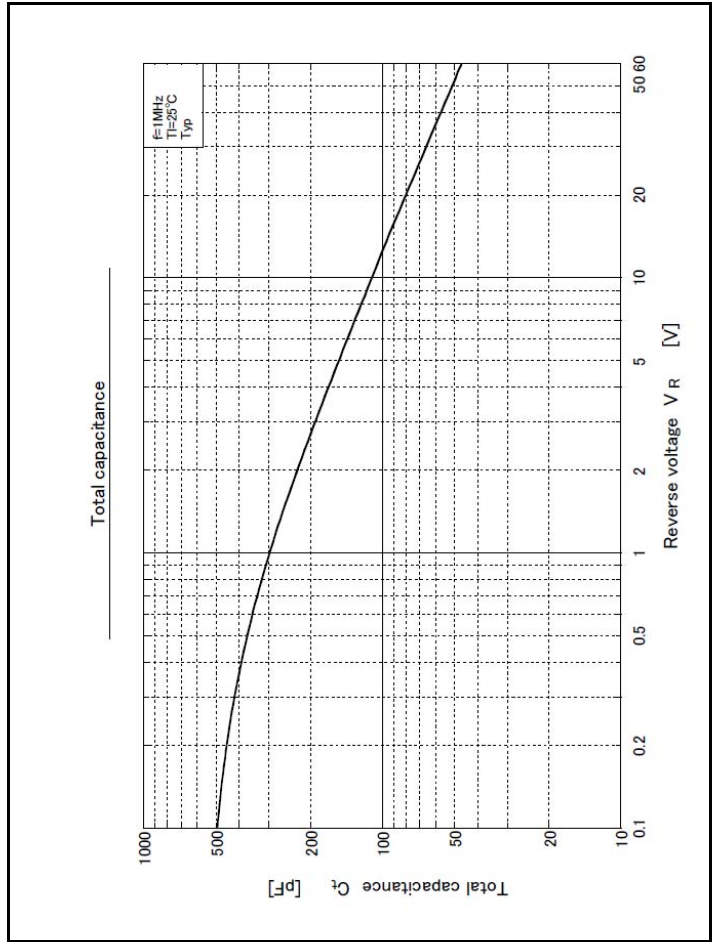
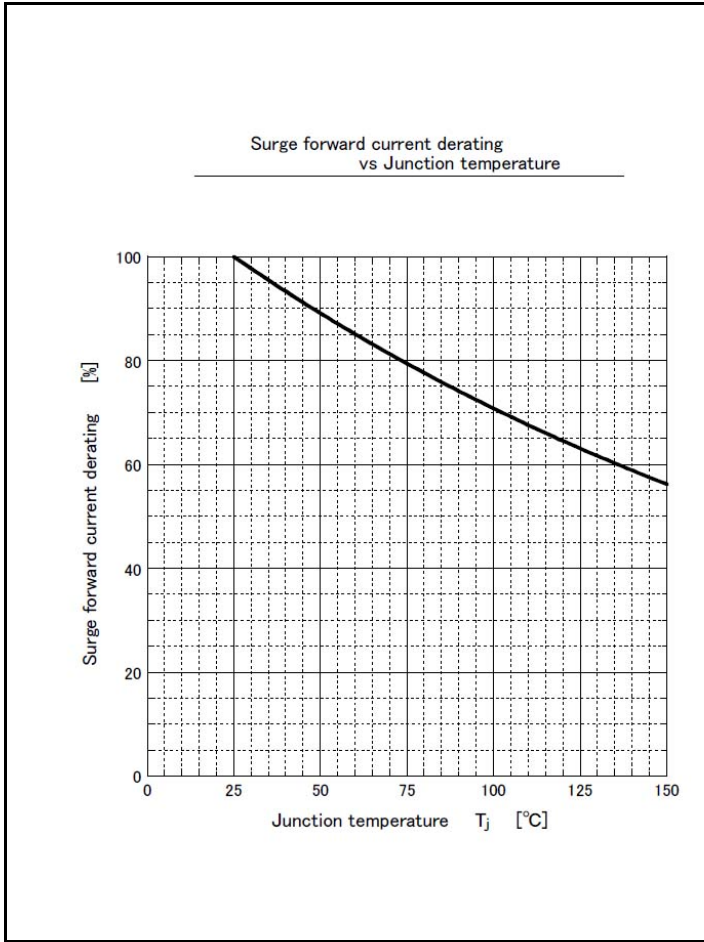
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Forward voltage	V_F	$I_F=3A$, Pulse measurement			0.58	V
Reverse current	I_R	$V_R=60V$, Pulse measurement			0.3	mA
Total capacitance	C_t	$f=1MHz$, $V_R=10V$		110		pF
Thermal resistance	$R_{th(j-l)}$	Junction to lead			15	°C/W
Thermal resistance	$R_{th(j-a)}$	Junction to ambient, On glass-epoxy substrate ※			115	°C/W
Thermal resistance	$R_{th(j-a)}$	Junction to ambient, On glass-epoxy substrate ※			172	°C/W

※ :See the original Specifications

CHARACTERISTIC DIAGRAMS

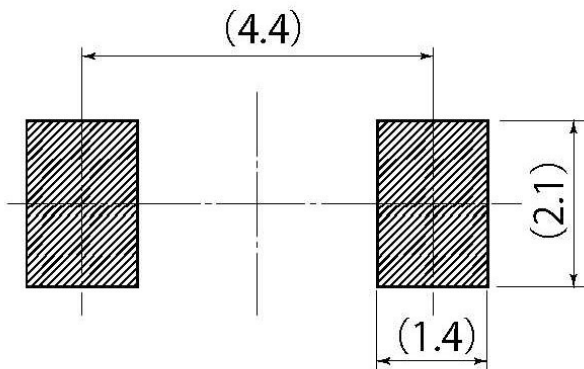
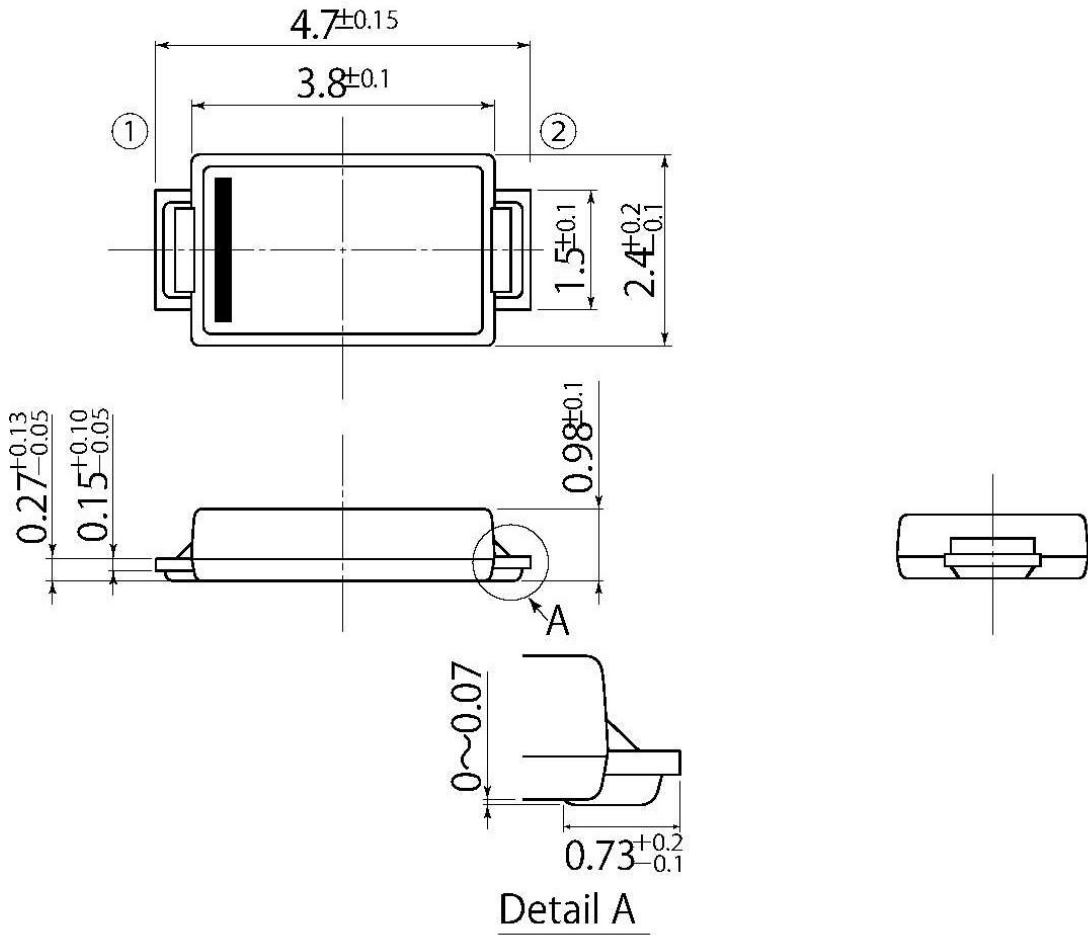






B5

JEDEC Code	—
JEITA Code	SC-110B
House Name	CE



Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

Notes

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