

D3CE4S

Schottky Barrier Diodes 40V, 3A

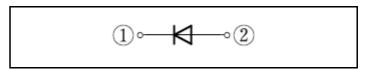
Feature

- Ultra-small SMD
- · Ultra thin PKG
- Low V_F
- · Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): CE
Package (JEITA Code): SC-110B

Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		-55 to 150	°C
Repetitive peak reverse voltage	V_{RRM}		40	V
Repetitive peak surge reverse voltage	V _{RRSM}	Pulse width 0.5ms, duty=1/40	45	V
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, TI=106°C	3	Α
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C *	1.7	Α
Average forward current	I _F (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C *	1.3	Α
Surge forward current	I _{FSM}	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C	80	Α

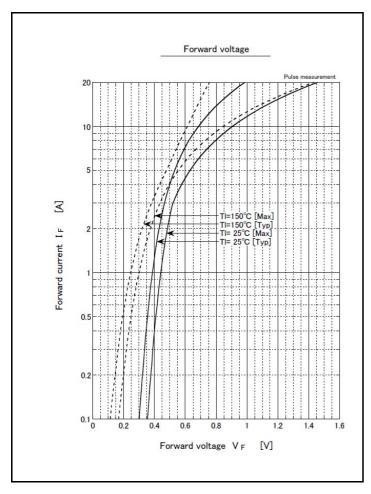
st :See the original Specifications

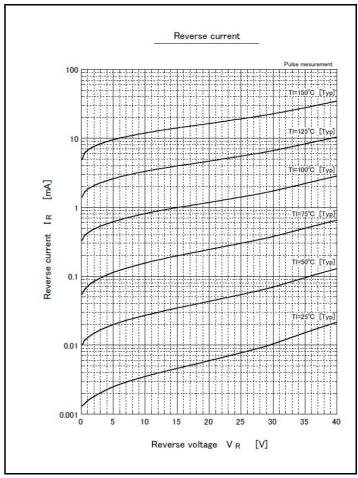
Electrical Characteristics (unless otherwise specified : TI=25°C)

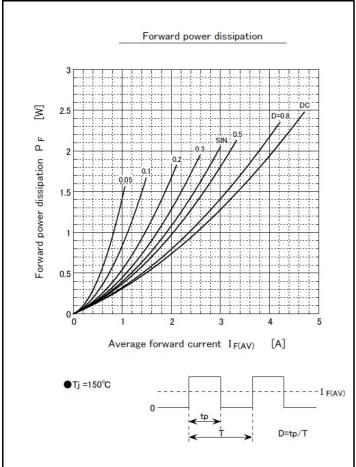
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	O.I.I.
Forward voltage	V _F	IF=3A, Pulse measurement			0.52	V
Reverse current	I _R	VR=40V, Pulse measurement			0.3	mA
Total capacitance	Ct	f=1MHz, VR=10V		97		pF
Thermal resistance	Rth(j-l)	Junction to lead			15	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			115	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			172	°C/W

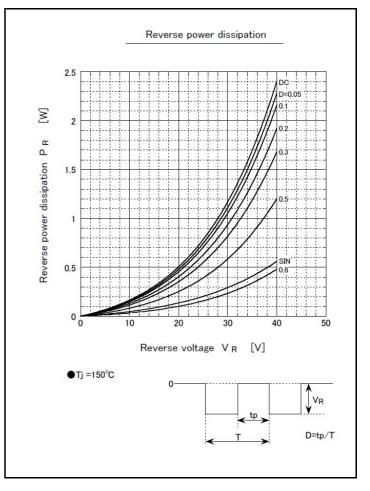
^{*} :See the original Specifications

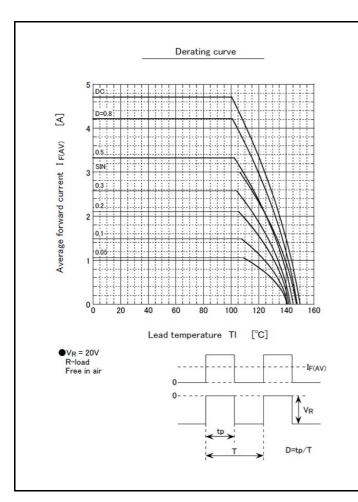
CHARACTERISTIC DIAGRAMS

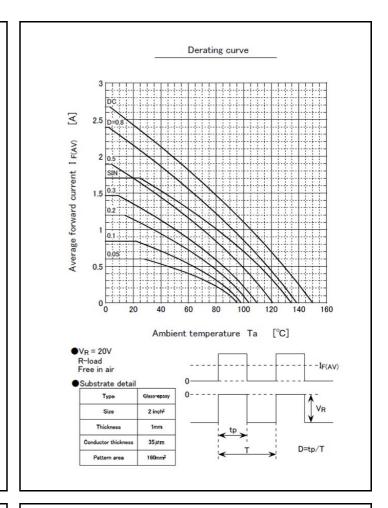


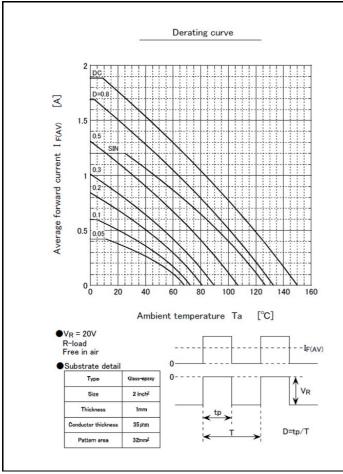


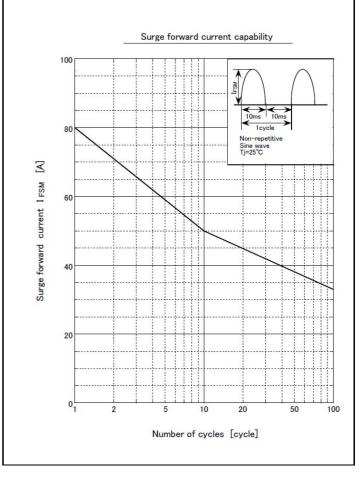


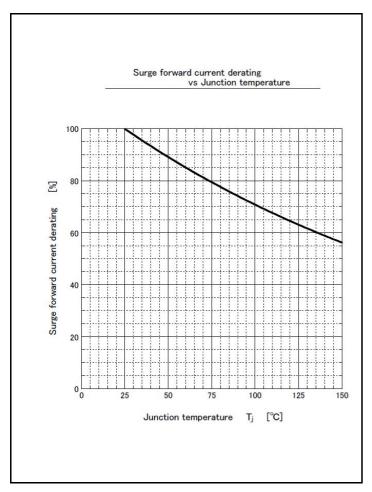


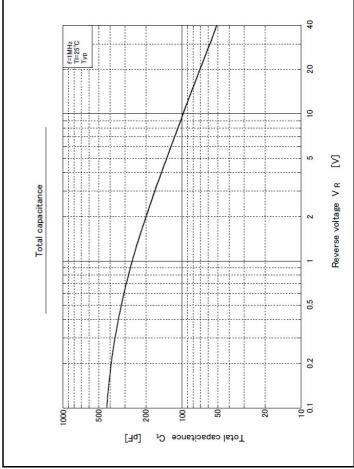


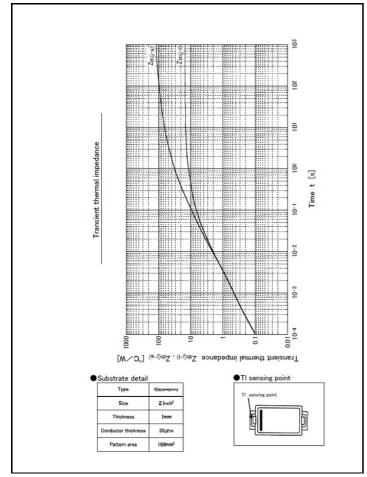


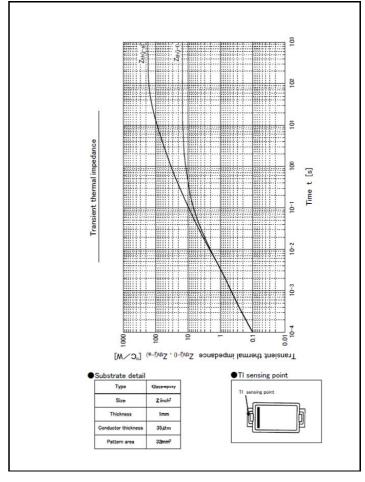








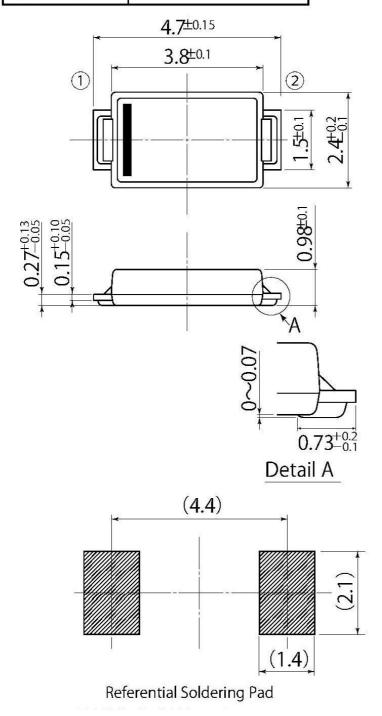


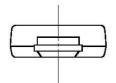


scale: 10/1

B5

JEDEC Code	()
JEITA Code	SC-110B
House Name	CE





[•] Optimize soldering pad to the board design and soldering condition.

Notes

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[Special applications]

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