

## D1FS4A

# **Schottky Barrier Diodes 40V, 1.5A**

### **Feature**

- Small SMD
- · High Recovery Speed
- Low V<sub>F</sub>
- · Based on AEC-Q101
- Pb free terminal
- RoHS:Yes

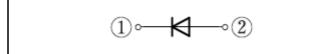
## **OUTLINE**

Package (House Name): 1F

Package (JEDEC Code): DO-214AC



## **Equivalent circuit**



## Absolute Maximum Ratings (unless otherwise specified : Tl=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		150	°C
Repetitive peak reverse voltage	$V_{RRM}$		40	V
Repetitive peak surge reverse voltage	V <sub>RRSM</sub>	Pulse width 0.5ms, duty=1/40	45	V
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, On alumina substrate, Ta=28°C	1.5	А
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=36°C	1.1	А
Surge forward current	I <sub>FSM</sub>	50Hz sine wave, Non-repetitive, 1cycle, Peak value, Tj=25°C	60	А
Repetitive peak surge reverse power	P <sub>RRSM</sub>	Pulse width 10μs, Tj=25°C	160	W

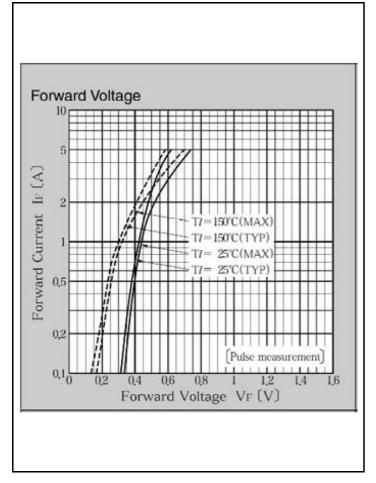
<sup>\*</sup> :See the original Specifications

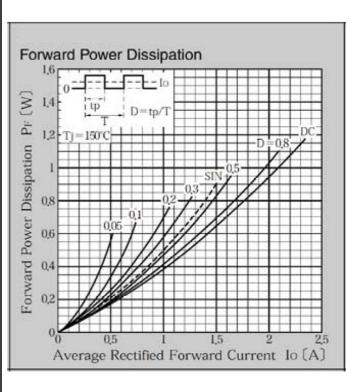
## **Electrical Characteristics** (unless otherwise specified : TI=25°C)

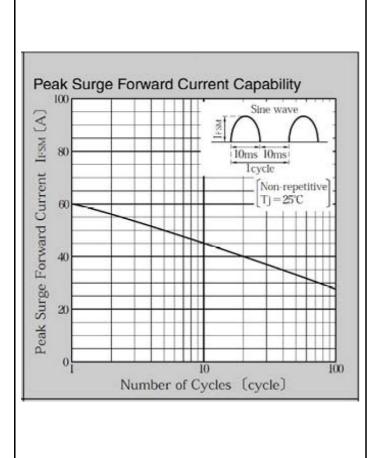
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Onne
Forward voltage	V <sub>F</sub>	IF=1.5A, Pulse measurement			0.48	V
Forward voltage	V <sub>F</sub>	IF=1.1A, Pulse measurement			0.45	V
Reverse current	I <sub>R</sub>	VR=40V, Pulse measurement			2	mA
Total capacitance	Ct	f=1MHz, VR=10V		95		pF
Thermal resistance	Rth(j-l)	Junction to lead			23	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On alumina substrate			108	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate			157	°C/W

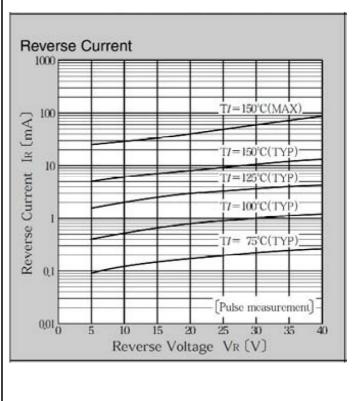
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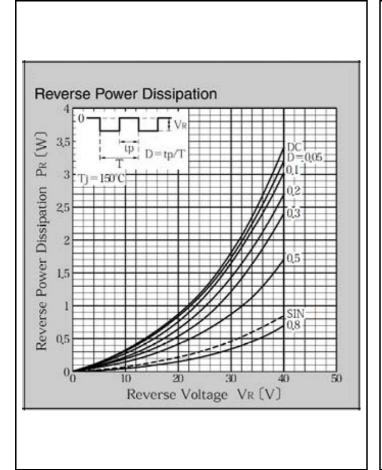
## **CHARACTERISTIC DIAGRAMS**

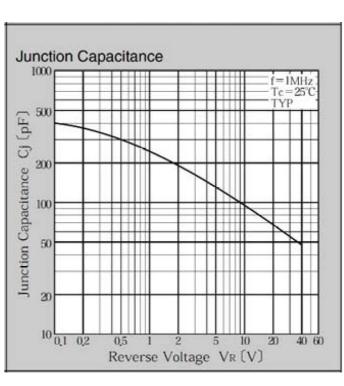


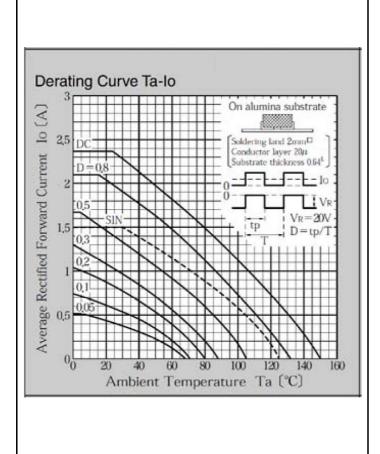


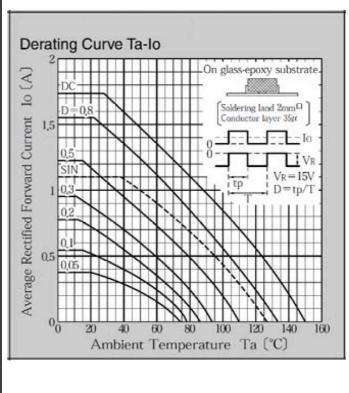


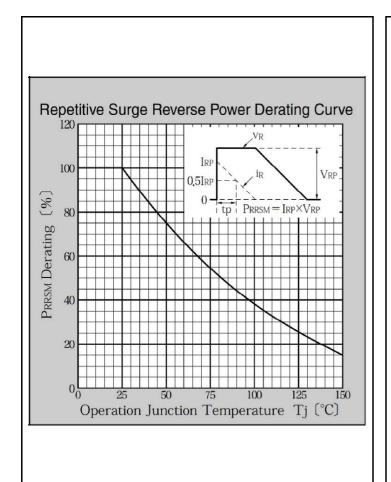


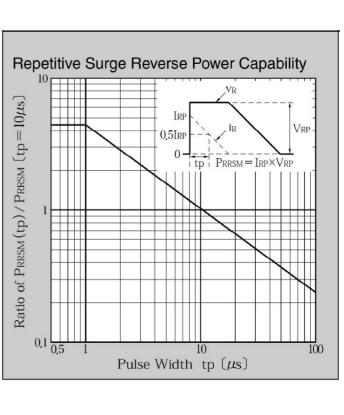








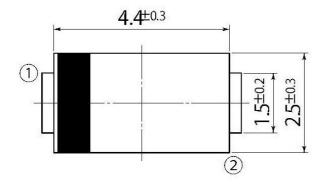


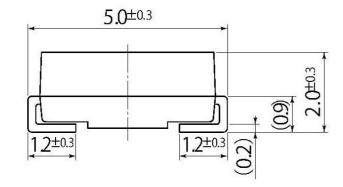


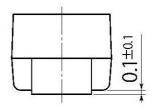
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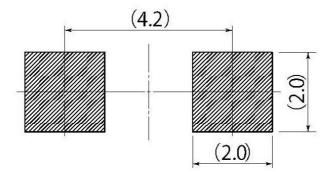
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JEDEC Code	DO-214AC	
JEITA Code	-	
House Name	1F	









Referential Soldering Pad

<sup>•</sup> Optimize soldering pad to the board design and soldering condition.

#### **Notes**

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