

UF540

Power MOSFET

27A, 100V N-CHANNEL
POWER MOSFET

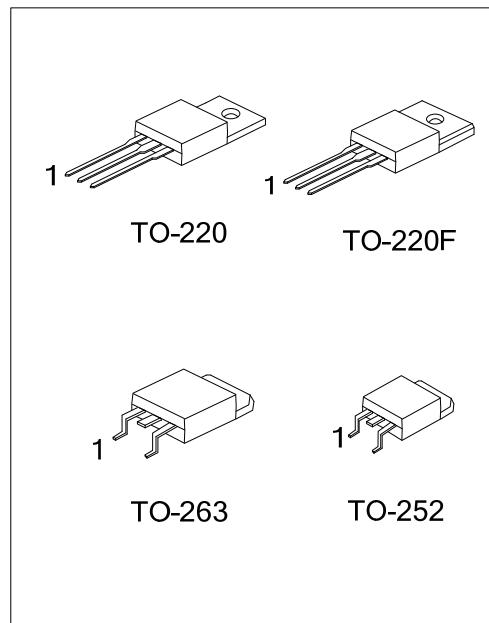
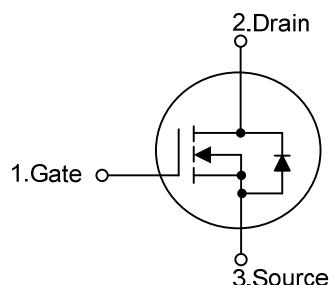
■ DESCRIPTION

The UTC **UF540** is a N-channel enhancement mode power MOSFET using UTC's advanced technology to provide the customers with a minimum on-state resistance and high switching speed.

■ FEATURES

- * $R_{DS(on)} \leq 36m\Omega$ @ $V_{GS}=10V$, $I_D=15A$
- * High Switching Speed

■ SYMBOL



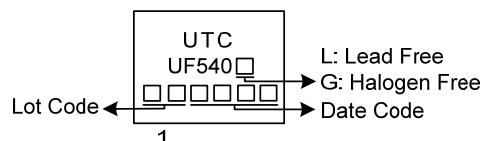
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UF540L-TA3-T	UF540G-TA3-T	TO-220	G	D	S	Tube
UF540L-TF3-T	UF540G-TF3-T	TO-220F	G	D	S	Tube
UF540L-TN3-R	UF540G-TN3-R	TO-252	G	D	S	Tape Reel
UF540L-TQ2-T	UF540G-TQ2-T	TO-263	G	D	S	Tube
UF540L-TQ2-R	UF540G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

UF540G-TA3-T 	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube, R: Tape Reel (2) TA3: TO-220,TF3: TO-220F, TN3: TO-252 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage (Note 2)		V _{DSS}	100	V
Gate-Source Voltage		V _{GSS}	±20	V
Drain Current	Continuous T _C =25°C	I _D	27	A
			17	A
	Pulsed	I _{DM}	108	A
Power Dissipation (T _C =25°C)		P _D	125	W
			40	W
			55	W
Junction Temperature		T _J	+150	°C
Storage Temperature		T _{STG}	-55 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. T_J = +25~+150°C

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS		UNIT
Junction to Case	TO-220	θ _{JC}	1.0		°C/W
	TO-263				
	TO-220F		3.125		°C/W
	TO-252				°C/W

■ ELECTRICAL CHARACTERISTICS (T_C=25°C, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =250μA, V _{GS} =0V	100			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA
Gate-Source Leakage Current	Forward	I _{GSS}	V _{GS} =+20V, V _{DS} =0V		100	nA
	Reverse		V _{GS} =-20V, V _{DS} =0V		-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250μA	2.0		4.0	V
Static Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =15A			36	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} =0V, V _{DS} =25V, f=1.0MHz		1620		pF
Output Capacitance	C _{OSS}			280		pF
Reverse Transfer Capacitance	C _{RSS}			48		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{DD} =80V, V _{GS} =10V, I _D =27A		42		nC
Gate to Source Charge	Q _{GS}			8		nC
Gate to Drain Charge	Q _{GD}			15		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DD} =50V, V _{GS} =10V, I _D =27A, R _{GEN} =25Ω (Fig.1, 2) (Note 2)		22		ns
Rise Time	t _R			32		ns
Turn-OFF Delay Time	t _{D(OFF)}			123		ns
Fall-Time	t _F			32		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I _S				27	A
Maximum Body-Diode Pulsed Current	I _{SM}				108	A
Drain-Source Diode Forward Voltage	V _{SD}	I _S =27A, V _{GS} =0V			1.4	V
Body Diode Reverse Recovery Time	t _{rr}	I _S =4.0A, dI _S /dt=25A/μs		300		ns

Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating ambient temperature.



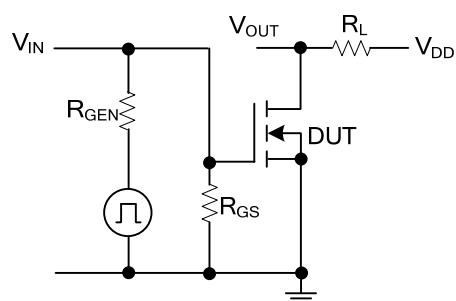
■ TEST CIRCUITS AND WAVEFORMS

Fig.1 Switching Test Circuit

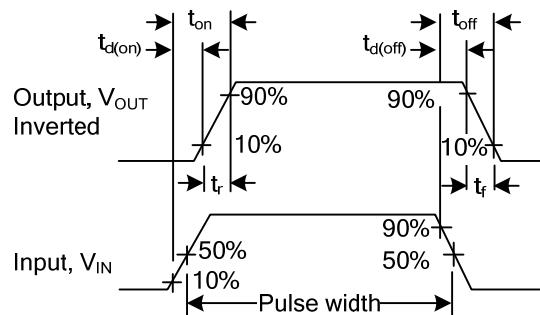
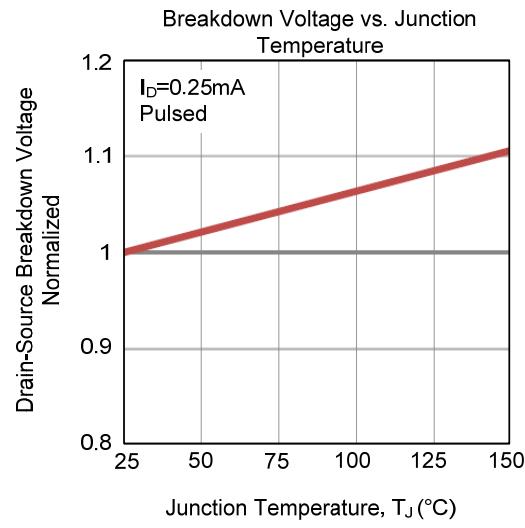
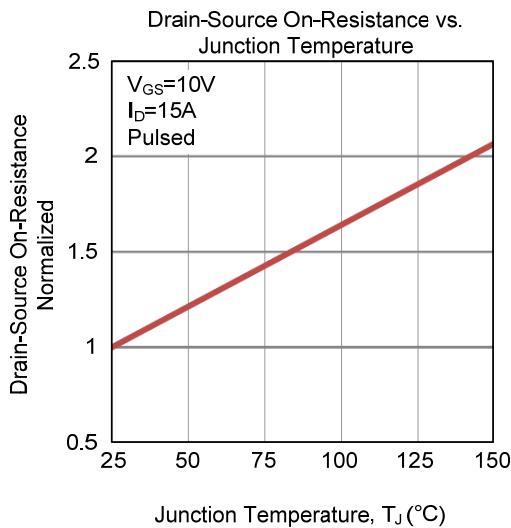
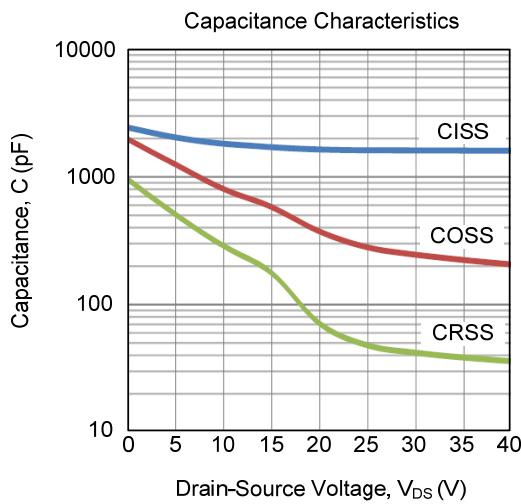
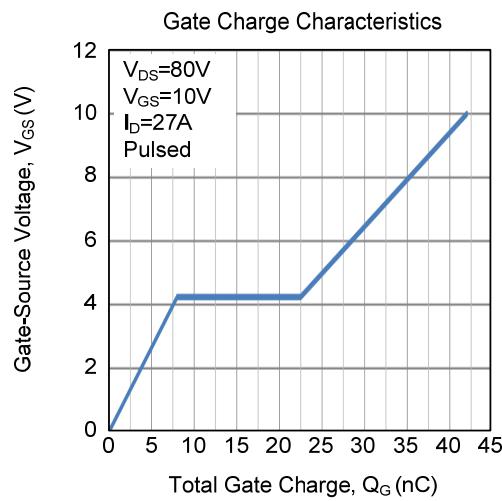
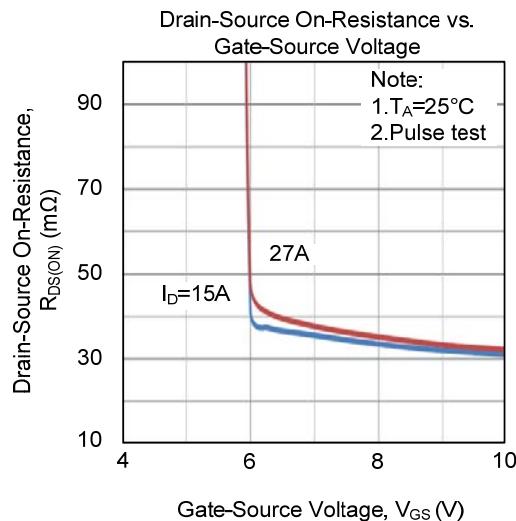
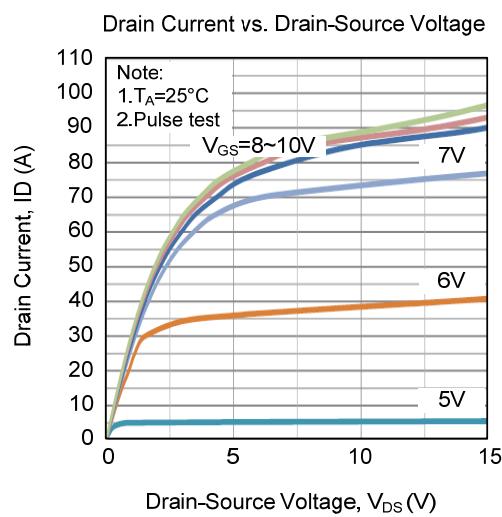
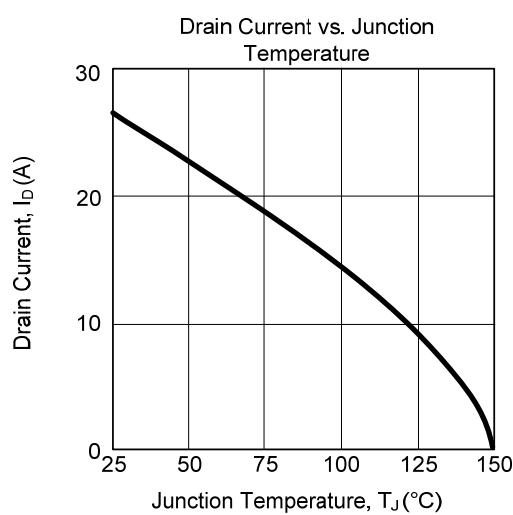
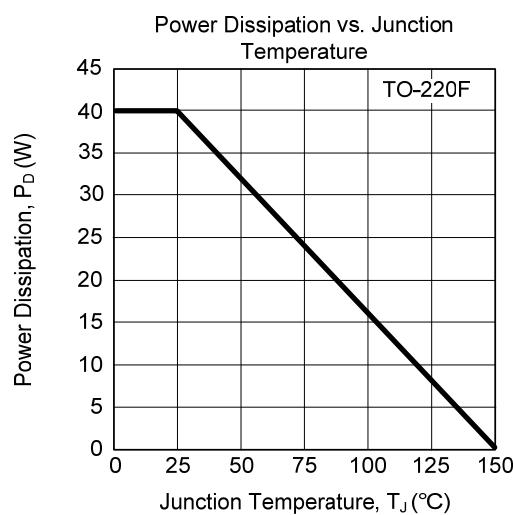
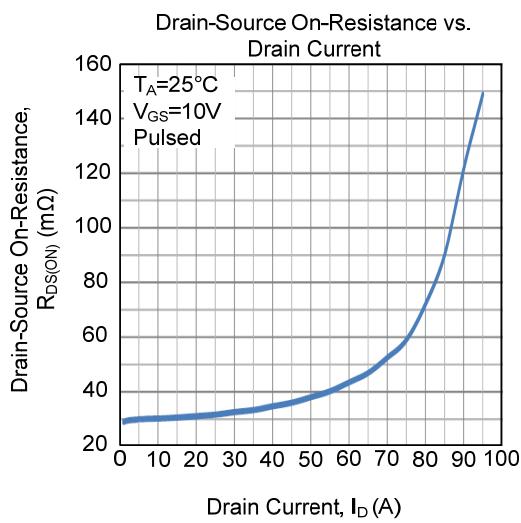
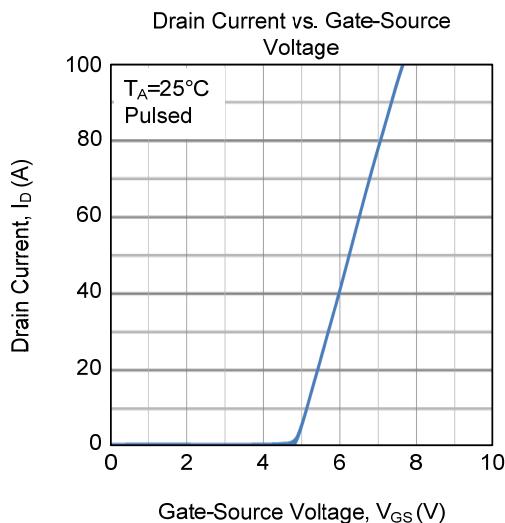
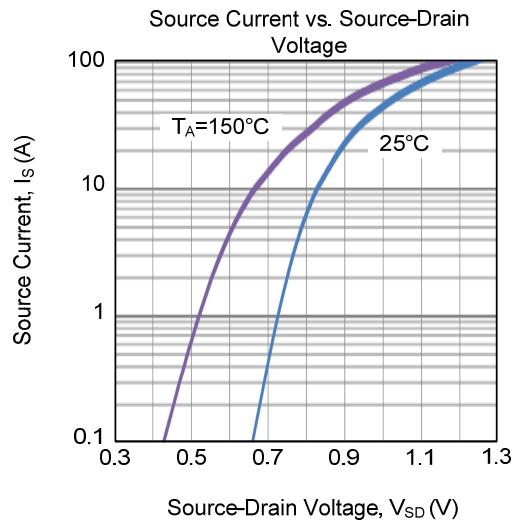
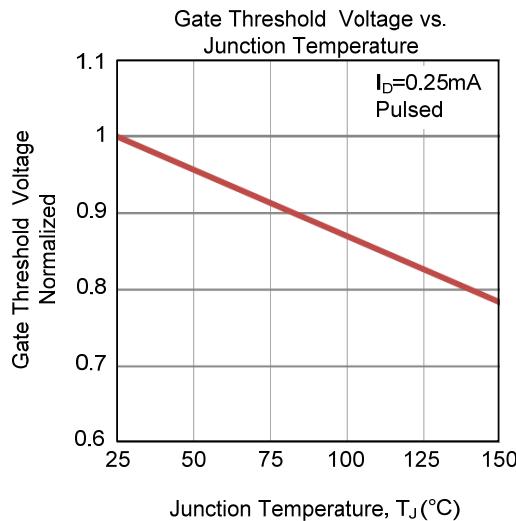


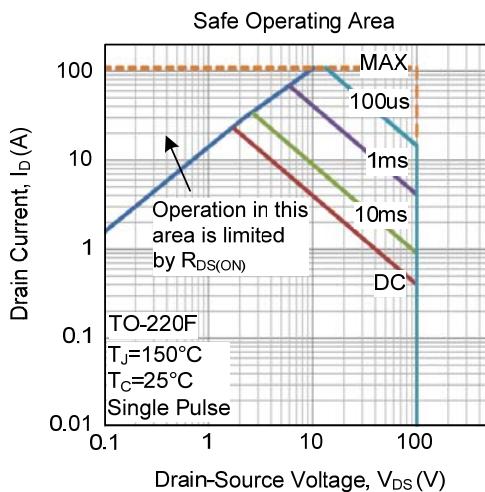
Fig.2 Switching Waveforms

■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)

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