

# **P5B52HP2**

# **Power MOSFETs** 525V, 5A, N-channel

#### **Feature**

- · N-channel
- ·SMD
- · High Voltage
- · Low Capacitance
- · High Avalanche Durability, High di/dt Durability
- · Pb free terminal
- · RoHS:Yes

### **OUTLINE**

Package (House Name): FB Package (JEDEC Code): TO-252AA

## **Equivalent circuit**



## Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperature	Tstg		-55 to 150	°C
Channel temperature	Tch		150	°C
Drain-source voltage	$V_{DSS}$		525	V
Gate-source voltage	V <sub>GSS</sub>		±30	V
Continuous drain current(DC)	I <sub>D</sub>		5	Α
Continuous drain current(Peak)	I <sub>DP</sub>	Pulse width 10µs, duty=1/100	20	Α
Continuous source current(DC)	Is		5	Α
Total power dissipation	PT		54	W
Repetitive avalanche current	I <sub>AR</sub>	Starting Tch=25°C Tch≦150°C	5	Α
Single avalanche energy	E <sub>AS</sub>	Starting Tch=25°C Tch≦150°C	45	mJ
Repetitive avalanche energy	E <sub>AR</sub>	Starting Tch=25°C Tch≦150°C	4.5	mJ
Drain-source diode di/dt strength	di/dt	Is=5A, Tc=25°C	350	A/µs

<sup>\* &#</sup>x27;See the original Specifications

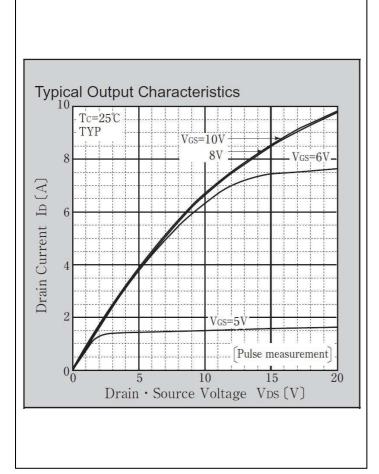
statement later.

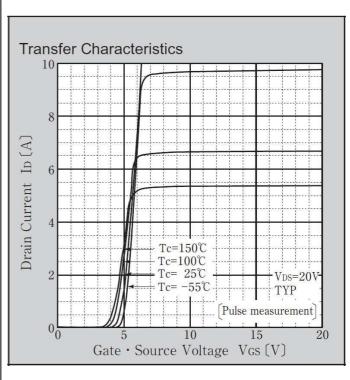
## **Electrical Characteristics** (unless otherwise specified : Tc=25°C)

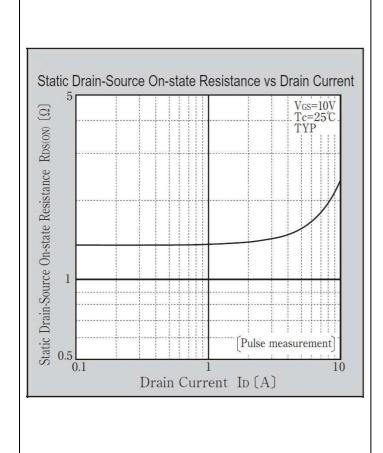
Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	Unit
Drain-Source breakdown voltage	V <sub>(BR)DSS</sub>	ID=1mA, VGS=0V	525			V
Zero gate voltage drain current	I <sub>DSS</sub>	VDS=525V, VGS=0V			100	μA
Gate-source leakage current	I <sub>GSS</sub>	VGS=±25V, VDS=0V			±10	μA
Forward transconductance	g <sub>fs</sub>	ID=2.5A, VDS=10V	2.5	5		S
Static drain-source on-state resistance	R <sub>DS(ON)</sub>	ID=2.5A, VGS=10V		1.4	1.7	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	3	3.75	4.5	V
Source-drain diode forward voltage	V <sub>SD</sub>	IS=2.5A, VGS=0V			1.5	V
Thermal resistance	Rth(j-c)	Junction to case			2.31	°C/W
Total gate charge	Qg	VDD=400V, VGS=10V, ID=5A		10.5		nC
Input capacitance	Ciss	VDS=50V, VGS=0V, f=1MHz		400		pF
Reverce transfer capacitnce	Crss	VDS=50V, VGS=0V, f=1MHz		4		pF
Output capacitance	Coss	VDS=50V, VGS=0V, f=1MHz		45		pF
Turn-on delay time	td(on)	ID=2.5A, RL=60Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		10.5		ns
Rise time	tr	ID=2.5A, RL=60Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		17		ns
Turn-off delay time	td(off)	ID=2.5A, RL=60Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		32		ns
Fall time	tf	ID=2.5A, RL=60Ω, VDD=150V, Rg=50Ω, VGS(+)=10V, VGS(-)=0V		22		ns

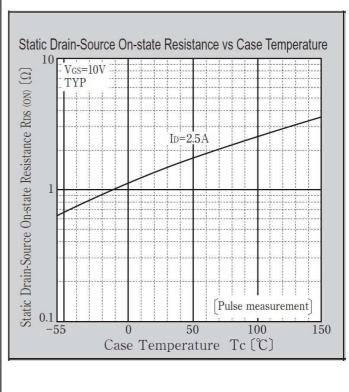
<sup>\* &#</sup>x27;See the original Specifications

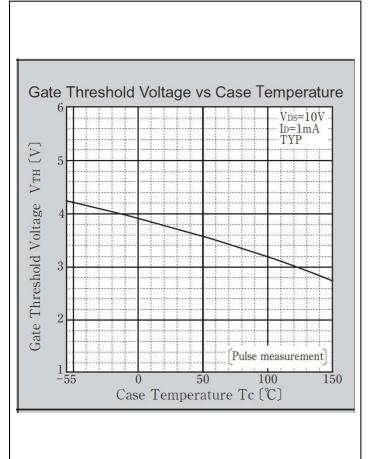
### **CHARACTERISTIC DIAGRAMS**

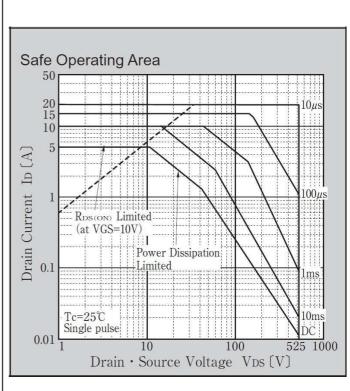


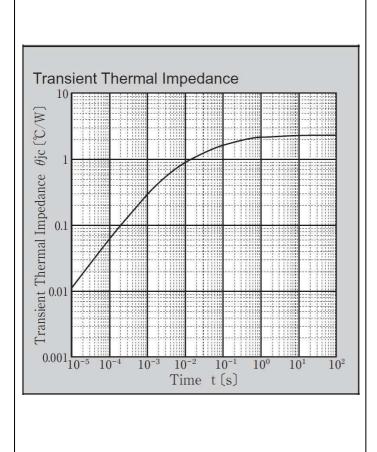


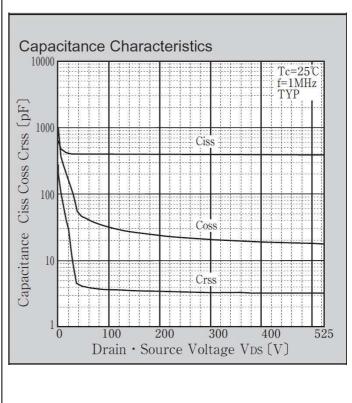


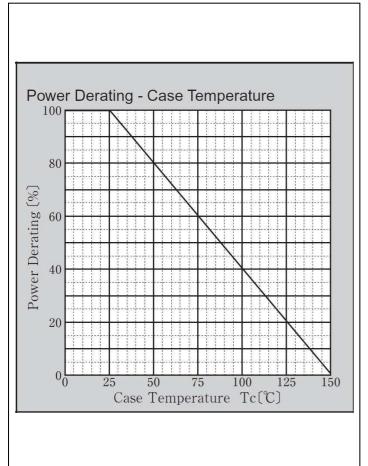


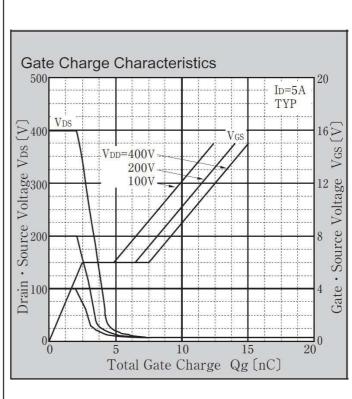


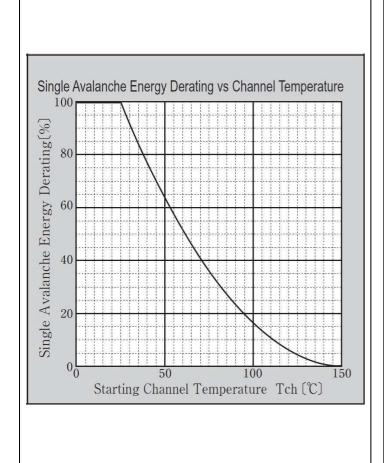


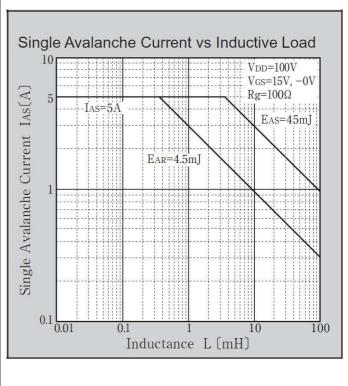








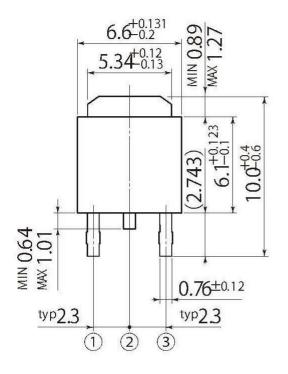


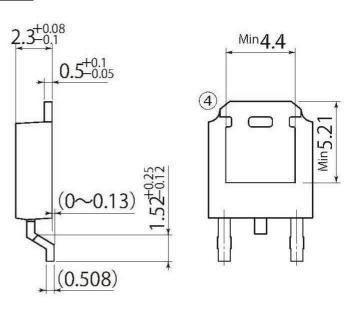


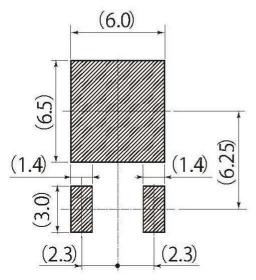
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G2

JEDEC Code	TO-252AA		
JEITA Code	-		
House Name	FB		







Referential Soldering Pad

 $<sup>\</sup>bullet$  Optimize soldering pad to the board design and soldering condition.

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