

High Voltage, Input Rectifier Diode, 40 A



PRIMARY CHARACTERISTICS				
I _{F(AV)} 40 A				
V_{R}	800 V to 1200 V			
V _F at I _F	1.1 V			
I _{FSM}	475 A			
T _J max.	150 °C			
Package	TO-247AC 2L			
Circuit configuration	Single			

FEATURES

- · Very low forward voltage drop
- 150 °C max. operating junction temperature
- · Glass passivated pellet chip junction
- Designed and qualified according to JEDEC®-JESD 47





APPLICATIONS

- Input rectification
- Vishay Semiconductors switches and output rectifiers which are available in identical package outlines

DESCRIPTION

High voltage rectifiers optimized for very low forward voltage drop with moderate leakage.

These devices are intended for use in main rectification (single or three phase bridge).

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Sinusoidal waveform	40	A		
V _{RRM}	Range	800/1200	V		
I _{FSM}		475	A		
V _F	40 A, T _J = 25 °C	1.1	V		
T _J		-40 to +150	°C		

VOLTAGE RATINGS					
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA		
VS-40EPS08-M3	800	900	1		
VS-40EPS12-M3	1200	1300	ı		

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current	I _{F(AV)}	$T_C = 105$ °C, 180° conduction half sine wave	40	
Maximum peak one cycle non-repetitive surge current		10 ms sine pulse, rated V _{RRM} applied	400	Α
		10 ms sine pulse, no voltage reapplied	475	
Maximum I ² t for fusing I ² t		10 ms sine pulse, rated V _{RRM} applied	800	A ² s
		10 ms sine pulse, no voltage reapplied	1131	A-S
Maximum I ² √t for fusing	I²√t	t t = 0.1 ms to 10 ms, no voltage reapplied 11 310 A^2		A²√s



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum farward valtage drap	V	20 A, T _J = 25 °C		1.0 V		
Maximum forward voltage drop	V_{FM}	40 A, T _J = 25 °C		1.1	V	
Forward slope resistance	r _t	T _{.1} = 150 °C		7.16	mΩ	
Threshold voltage	V _{F(TO)}			0.74	V	
Maximum vayaraa laakaaa ayyyant	1	T _J = 25 °C	V _B = Rated V _{RBM}	0.1	mA	
Maximum reverse leakage current	IRM	T _J = 150 °C	VR = nateu VRRM	1.0	IIIA	

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storrage temperature range	}	T _J , T _{Stg}		-40 to +150	°C
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.6	
Maximum thermal resistance, junction to ambient		R _{thJA}		40	°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, flat, smooth, and greased	0.2	
Approximate weight				6	g
Approximate weight				0.21	oz.
Mounting torque ————	minimum			6 (5)	kgf ⋅ cm
	maximum			12 (10)	(lbf ⋅ in)
Marking device			O	40EF	PS08
			Case style TO-247AC 2L	40EPS12	

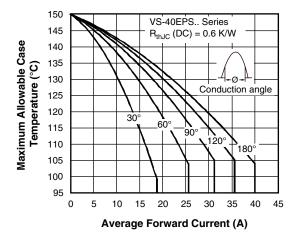


Fig. 1 - Current Rating Characteristics

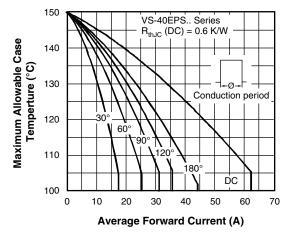


Fig. 2 - Current Rating Characteristics

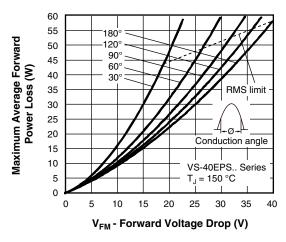


Fig. 3 - Forward Power Loss Characteristics

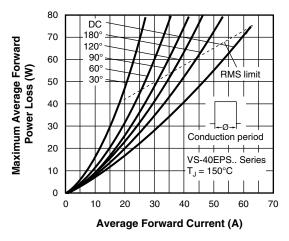


Fig. 4 - Forward Power Loss Characteristics

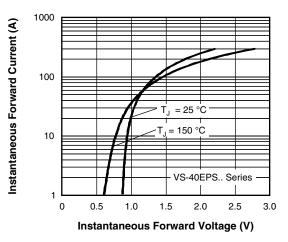


Fig. 5 - Forward Voltage Drop Chacteristics

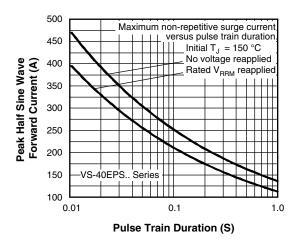


Fig. 6 - Maximum Non-Repetitive Surge Current

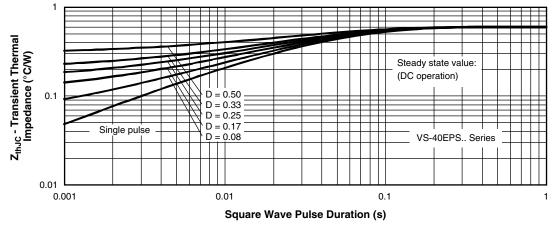
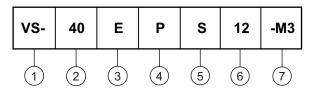


Fig. 7 - Thermal Impedance Z_{thJC} Characteristics



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

2 - Current rating (40 = 40 A)

Circuit configuration:

E = single diode

4 - Package:

P = TO-247AC 2L

5 - Type of silicon:

S = standard recovery rectifier

V 008 = 80

6 - Voltage rating

12 = 1200 V

Environmental digit:

-M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-40EPS08-M3	25	500	Antistatic plastic tubes		
VS-40EPS12-M3	25	500	Antistatic plastic tubes		

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96144</u>				
Part marking information	www.vishay.com/doc?95648			
SPICE model	www.vishay.com/doc?96047			



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