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### Vishay Semiconductors

# Hyperfast Rectifier, 30 A FRED Pt®



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	30 A			
$V_{R}$	600 V			
V <sub>F</sub> at I <sub>F</sub>	1.34 V			
t <sub>rr</sub> typ.	See Recovery table			
T <sub>J</sub> max.	175 °C			
Package	TO-247AC 2L			
Circuit configuration	Single			

#### **FEATURES**

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Single diode device
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

## DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	$V_{RRM}$		600	V	
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 116 °C	30	Δ.	
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25  ^{\circ}\text{C},  t_p = 10  \text{ms}$	300	A	
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	600	-	-	
Forward voltage V <sub>F</sub>	I <sub>F</sub> = 30 A	-	2.0	2.6	V	
	I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.34	1.75		
Davis and Indiana account		V <sub>R</sub> = V <sub>R</sub> rated	-	0.3	50	
Reverse leakage current I <sub>R</sub>		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	60	500	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V		33	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	3.5	-	nH



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	28	35	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	31	-	ns
		T <sub>J</sub> = 125 °C		-	77	-	
Peak recovery current I <sub>RRM</sub>	1	T <sub>J</sub> = 25 °C	$I_F = 30 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	3.5	-	А
	T <sub>J</sub> = 125 °C	$V_{\rm R} = 200 \text{ V}$	-	7.7	-	^	
Reverse recovery charge Q <sub>rr</sub>	0	T <sub>J</sub> = 25 °C		-	65	-	nC
	<b>∪</b> rr	T <sub>J</sub> = 125 °C		=	345	=	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	0.5	0.9	
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	40	°C/W
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.4	-	
Woight			-	6.0	-	g
Weight			-	0.22	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-247AC 2L		30EI	PH06	

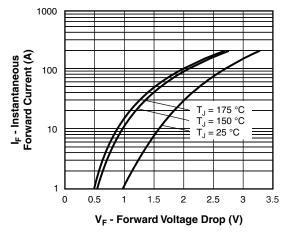


Fig. 1 - Typical Forward Voltage Drop Characteristics

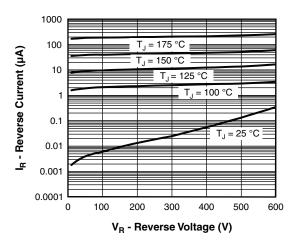


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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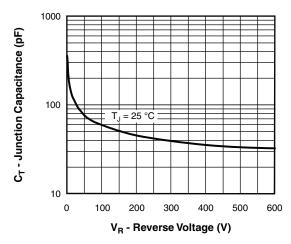


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

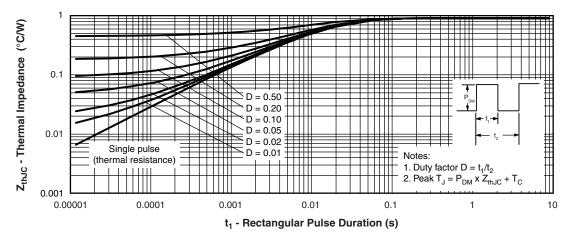


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

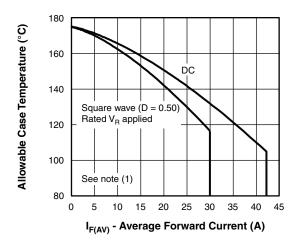


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

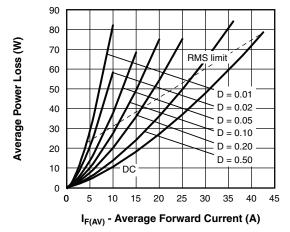


Fig. 6 - Forward Power Loss Characteristics

#### Note

Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>th,JC</sub>; Pd = forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = rated V<sub>R</sub>



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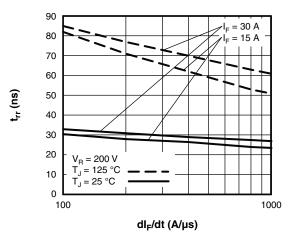


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

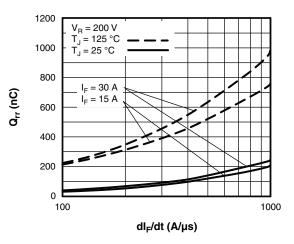
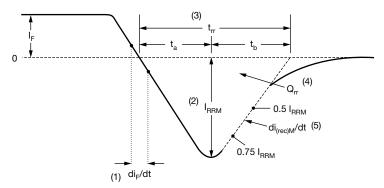


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RRM}$  and 0.50  $I_{\rm RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

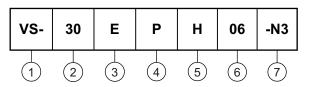
Fig. 9 - Reverse Recovery Waveform and Definitions



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#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Vishay Semiconductors product
- 2 Current rating (30 = 30 A)
- Gircuit configuration:

  E = single diode
- 4 Package:
  - P = TO-247AC modified
- 5 H = hyperfast recovery
- Voltage rating (06 = 600 V)
- 7 Environmental digit:
  - -N3 = halogen-free, RoHS-compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)				
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION	
VS-30EPH06-N3	25	500	Antistatic plastic tube	

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?96144</u>				
Part marking information	www.vishay.com/doc?95648			
SPICE model	www.vishay.com/doc?96573			



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