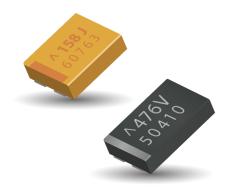
### **Highest CV/CC Conductive Polymer Chip Capacitors Undertab**





#### **FEATURES**

- Highest CV/cc in broad range of low profiles
- Conductive polymer electrode
- Benign failure mode under recommended use conditions
- Lower ESR
- Undertab terminations lavout:
  - High Volumetric Efficiency
  - High PCB assembly density
  - High capacitance in smaller dimensions
- 3x reflow 260°C compatible
- · 11 case sizes available

#### **APPLICATIONS**

- Consumer applications (e.g. mobiles, MP3 etc.)
- Bulk decoupling of SoC (System on chip)



















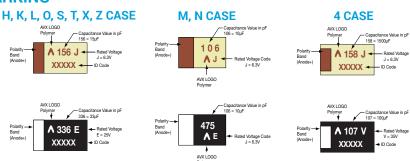


#### **CASE DIMENSIONS:**

#### millimeters (inches)

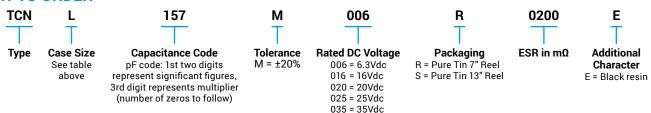
Code	EIA Code	EIA Metric	L±0.20 (0.008)	W+0.20 (0.008) -0.10 (0.004)	H max.	W <sub>p</sub> ±0.10 (0.004)	W <sub>N</sub> ±0.10 (0.004)	A <sub>P</sub> ±0.10 (0.004)	A <sub>N</sub> ±0.10 (0.004)	S Min.
М	0805	2012-09	2.05 (0.081)	1.30 (0.051)	0.90 (0.035)	1.00 (0.039)	1.00 (0.039)	0.85 (0.033)	0.85 (0.033)	0.40 (0.016)
N	0805	2012-10	2.05 (0.081)	1.30 (0.051)	1.00 (0.039)	1.00 (0.039)	1.00 (0.039)	0.85 (0.033)	0.85 (0.033)	0.40 (0.016)
0	1206	3216-06	3.20 (0.126)	1.60 (0.063)	0.60 (0.024)	1.30 (0.051)	1.30 (0.051)	1.15 (0.045)	1.15 (0.045)	0.90 (0.035)
K	1206	3216-10	3.20 (0.126)	1.60 (0.063)	1.00 (0.039)	1.30 (0.051)	1.30 (0.051)	1.15 (0.045)	1.15 (0.045)	0.90 (0.035)
S	1206	3216-12	3.20 (0.126)	1.60 (0.063)	1.20 (0.047)	1.30 (0.051)	1.30 (0.051)	1.15 (0.045)	1.15 (0.045)	0.90 (0.035)
L	1210	3528-10	3.50 (0.138)	2.80 (0.110)	1.00 (0.039)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Т	1210	3528-12	3.50 (0.138)	2.80 (0.110)	1.20 (0.047)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Н	1210	3528-15	3.50 (0.138)	2.80 (0.110)	1.50 (0.059)	2.50 (0.098)	2.10 (0.083)	1.15 (0.045)	1.35 (0.053)	1.00 (0.039)
Х	2917	7343-15	7.30 (0.287)	4.30 (0.169)	1.50 (0.059)	3.25 (0.128)	3.25 (0.128)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)
Z	2917	7343-15	7.30 ±0.30 (0.287 ±0.012)	4.30 ±0.30 (0.169 ±0.012)	1.50 (0.059)	2.40 (0.094)	2.40 (0.094)	1.30 ±0.30 (0.051 ±0.012)	1.30 ±0.30 (0.051 ±0.012)	4.40 (0.173)
4	2924	7361-20	7.30 (0.287)	6.10 (0.240)	2.00 (0.079)	4.75 (0.187)	4.75 (0.187)	2.00 (0.079)	3.20 (0.126)	2.10 (0.083)

#### **MARKING**



#### **HOW TO ORDER**

216



Part Numbers already changed to an "E" suffix will continue to be supplied with only black resin. Those Part Numbers currently produced with gold resin will eventually change to black before July, 2020.





### **Highest CV/CC Conductive Polymer Chip Capacitors Undertab**

#### TECHNICAL SPECIFICATIONS

Technical Data:	All technical data relate to an ambient temperature of +25°C									
Capacitance Range:	1.0 μF to 1500 μF									
Capacitance Tolerance:		±20%								
Leakage Current DCL:		0.1CV								
Rated Voltage DC (V <sub>B</sub> )	≤ +85°C:	4	6.3	10	16	20	25	35	50	
Category Voltage (V <sub>c</sub> )	≤ +105°C:	3.2	5	8	13	16	20	28	40	
Surge Voltage (V <sub>s</sub> )	≤ +85°C:	5.2	8	13	21	26	33	46	65	
Surge Voltage (V <sub>s</sub> )	≤ +105°C:	4	6	10	16	20	25	35	50	
Temperature Range:		-55°C to	+105°C							
Reliability:		1% per 1	000 hours	at 85°C.	V <sub>s</sub> with 0.	1Ω/V ser	ies imped	dance 60%	% confider	nce level

NOTE: Conductive Polymer Capacitors are designed to operate within the limits of the environmental conditions specified for each series. If operated continuously at their maximum temperature and / or humidity limit, or beyond these limits, capacitors may exhibit a parametric shift in capacitance and increases in ESR. These changes may occur earlier if the specified environmental conditions are exceeded. Similarly, their normal operational time period will be significantly extended if their general duty cycle includes operation below maximum temperature within humidity controlled environments. Careful attention should be paid to maximum temperature with associated high humidity environments as well as voltage derating, ripple current and current surges. Please reference the AVX Conductive Polymer Capacitor Guidelines for more information or contact factory for application assistance.

# CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance			Rated	Voltage DC to 8	5°C / 0.66DC to	105°C		
μF	Code	4V (G)	6.3V (J)	10V (A)	16V (C)	20V (D)	25V (E)	35V (V)	50V (T)
1.0	105								N(1500)
4.7	475						N(500)	L(300)/T(200)	
6.8	685				O(500)				
10	106			O(500)	O(500)		K(350)/S(350)	T(200)	
15	156		O(500)	O(500)					
22	226	O(500)	O(500)				T(200)		
33	336				L(200)/T(20)		T(250)		4(200)
47	476		M(500)		L(250) T(200)/T(150)		X(100)	X(150)/Z(150)	
68	686								
100	107		K(200,250) L(200)/S(250)				3(70)*/4(100)	3(200)*/4(100)	
150	157		L(200) S(250)/T(200)		X(100)		4(70)		
220	227		H(170)/T(200)		4(70)	4(100)	4(100)		
330	337				4(70)	4(100)			
470	477		X(50)		4(100)				
1000	108		X(200) 3(100)*/4(55)						
1500	158		4(55)						

Note for designers - for the highlighted ratings, higher voltage options are now available in the same case size and are recommended for new designs.

Released ratings, (ESR ratings in mOhms in parentheses)

\*Codes under developement - subject to change

**Engineering Samples - Please Contact AVX** 

Note: Voltage ratings are minimum values. AVX reserves the right to supply higher voltage ratings in the same case size, to the same reliability standards.







#### **RATINGS & PART NUMBER REFERENCE**

AVX		Capacitance	Rated	Maximum Operating	DCL Max.	DF Max.	ESR Max.	100kH	z RMS Curre	Product	MSL	
Part No.	Size	(μF)	Voltage (V)	Temperature (°C)	(μ <b>A</b> )	(%)	@ 100kHz (mΩ)	45°C	85°C	105°C	Category	IVISL
					4 Volt @ 85°							
TCN0226M004#0500E	0	22	4	105	8.8	10	500	400	300	200	3	3
					6.3 Volt @ 85							
TCN0156M006#0500E	0	15	6.3	105	9	10	500	400	300	200	3	3
TCN0226M006#0500E	0	22	6.3	105	13.2	10	500	400	300	200	3	3
TCNM476M006#0500E	М	47	6.3	105	28.2	10	500	400	300	200	3	3
TCNK107M006#0200E	K	100	6.3	105	60	10	200	700	500	300	3	5
TCNK107M006#0250E	K	100	6.3	105	60	10	250	600	400	300	3	5
TCNL107M006#0200E	L	100	6.3	105	60	10	200	700	500	300	3	5
TCNS107M006#0250E	S	100	6.3	105	60	10	250	600	400	300	3	3
TCNL157M006#0200E	L	150	6.3	105	90	10	200	700	500	300	3	5
TCNS157M006#0250E	S	150	6.3	85	90	10	250	600	400	-	5	3
TCNT157M006#0200E	T	150	6.3	105	90	10	200	700	500	300	3	4
TCNH227M006#0170E	H	220	6.3	105	132	10	170	800	600	400	3	4
TCNT227M006#0200E	T	220	6.3	85	132	10	200	700	500	-	5	4
TCNX477M006#0050E	X	470	6.3	85	282	10	50	1900	1300	-	5	5
TCNX108M006#0200E	X	1000	6.3	85	600	30	200	900	600	400	5	5
TCN3108M006#0100	3	1000	6.3	105	600	20	100	1200	840	480	3	5
TCN4108M006#0055E	4	1000	6.3	85	600	20	55	1860	1302	-	5	4
TCN4158M006#0055E	4	1500	6.3	85	900	20	55	1860	1302		5	4
					10 Volt @ 85							
TCN0106M010#0500E	0	10	10	105	10	10	500	400	300	200	3	3
TCN0156M010#0500E	0	15	10	105	15	10	500	400	300	200	3	3
		, ,			16 Volt @ 85						, , , , , , , , , , , , , , , , , , , ,	
TCNO685M016#0500E	0	6.8	16	105	10.9	10	500	400	300	200	3	3
TCN0106M016#0500E	0	10	16	105	16	10	500	400	300	200	3	3
TCNL336M016#0200E	L	33	16	85	52.8	6	200	700	500	_	5	5
TCNT336M016#0200E	T	33	16	105	52.8	6	200	700	500	300	3	4
TCNL476M016#0250E	L	47	16	85	75.2	6	250	600	400	-	5	5
TCNT476M016#0150E	T	47	16	105	75.2	6	150	800	600	400	3	4
TCNT476M016#0200	T	47	16	105	75.2	6	200	700	500	300	3	4
TCNX157M016#0100E	X	150	16	105	240	6	100	1300	900	600	3	4
TCN4227M016#0070E	4	220	16	105	352	20	70	1650	1155	660	2	4
TCN4337M016#0070E	4	330	16	105	528	20	70	1650	1155	660	3	4
TCN4477M016#0100E	4	470	16	85	752	20	100	1380	966	_	5	4
					20 Volt @ 85					,		
TCN4227M020#0100E	4	220	20	85	440	10	100	1380	966		5	4
TCN4337M020#0100E	4	330	20	105	660	20	100	1380	966	552	3	4
					25 Volt @ 85						, ,	
TCNN475M025#0500E	N	4.7	25	105	11.8	10	500	400	300	200	3	3
TCNK106M025#0350E	K	10	25	105	25	10	350	500	400	200	3	5
TCNS106M025#0350E	S	10	25	105	25	10	350	500	400	200	3	5
TCNT226M025#0200E	T	22	25	105	55	6	200	700	500	300	3	4
TCNT336M025#0250E	T	33	25	105	82.5	10	250	600	400	300	3	4
TCNX476M025#0100E	X	47	25	105	117.5	6	100	1300	900	600	2	5
TCN3107M025#0070	3	100	25	105	250	6	70	1440	1008	576	2	5
TCN4107M025#0100E	4	100	25	105	250	6	100	1380	966	552	2	4
TCN4157M025#0070E	4	150	25	105	375	6	70	1650	1155	660	2	4
TCN4227M025#0100E	4	220	25	105	550	10	100	1380	966	552	3	4
					35 Volt @ 85						,	
TCNL475M035#0300E	L	4.7	35	105	16.5	6	300	600	400	300	2	5
TCNT475M035#0200E	T	4.7	35	105	16.5	10	200	700	500	300	3	4
TCNT106M035#0200E	T	10	35	105	35	10	200	700	500	300	3	4
TCNX476M035#0150E	Х	47	35	105	165	10	150	1100	800	500	3	4
TCNZ476M035#0150E	Z	47	35	105	165	10	150	1100	800	500	3	4
TCN3107M035#0200	3	100	35	85	350	10	200	850	595	-	5	5
TCN4107M035#0100E	4	100	35	105	350	10	100	1380	966	552	2	3
					50 Volt @ 85							
TCNN105M050#1500E	N	1	50	105	5	10	1500	200	100	100	3	3
TON 1400CN 40E0 #0000E	4	33	50	85	165	12	200	970	679	-	5	3

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25°C. Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 2.2 volts. DCL is measured at rated voltage after 5 minutes.

ESR allowed to move up to 1.25 times catalog limit post mounting.

For typical weight and composition see page 276.

NOTE: AVX reserves the right to supply higher voltage ratings in the same case size to the same reliability standards.

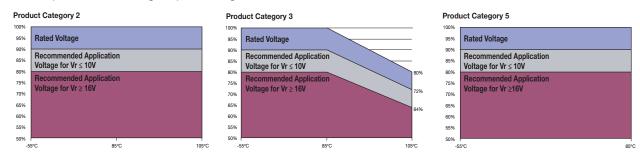






#### RECOMMENDED DERATING FACTOR

Voltage and temperature derating as percentage of Vr



### PRODUCT CATEGORY 2, 3 (TEMPERATURE RANGE -55°C TO +105°C)

TEST		Condition		Characteristics								
	Apply rated volta	age (Ur) at 85°C f	or 2000 hours	Visual examination	no visib	le damage						
	through a circuit	t impedance of ≤0	).1Ω/V (all ad voltage (Ur)	DCL	1.25 x iı	nitial limit						
Endurance	(CATEGORY 2)	And / or apply rate or 0.8x rated volta	ge (CATEGORY	ΔC/C	within ±20% of initial value							
	3) at 105°C for 2 impedance of ≤0	2000 hours through	DF	1.5 x ini	1.5 x initial limit							
	temperature for	0.1Ω/V. Always st 1-2 hours before	measuring.	ESR	2 x initia	2 x initial limit						
				Visual examination	no visib	le damage						
				$DCL(V_R \le 75V)$	1.25 x ii	nitial limit						
Storage Life		no voltage applied at room temperat		DCL (V <sub>R</sub> > 75V)	2 x initia	al limit						
Storage Life	hours before me		lure for 1-2	ΔC/C	within ±	:20% of initi	al value					
		g-		DF	1.5 x in	itial limit						
				ESR	2 x initia	al limit						
				Visual examination	no visil	ole damage	е					
		nd 95% relative hu pplied voltage. St		DCL	3 x initi	3 x initial limit						
Humidity		d humidity for 1-2		ΔC/C	within -	within +30/-20% of initial value						
	measuring.	aaa.ty .e		DF	1.5 x initial limit							
				ESR	2 x initial limit							
	Step 1	Temperature°C +20	Duration(min) 15	1	+20°C	-55°C	+20°C	+85°C	+105°C	+20°C		
Temperature	2	-55	15	DCL	IL*	n/a	IL*	10 x IL*	12.5 x IL*	IL*		
Stability	3 4	+20 +85	15 15	ΔC/C	n/a	+0/-20%	±5%	+20/-0%	+30/-0%	±5%		
	5 6	+105 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	2 x IL*	IL*		
	-			Visual examination	no visible damage							
		voltage (Ur) at 105 apply 1.3x 0.8x rate		DCL	initial limit							
Surge Voltage	105°C for CATEG	ORY 3 for 1000 cy	cles of duration 6	10/0	within +	within +10/-20% of initial value for Vr ≤ 10V						
voitage		ge, 5 min 30 sec dia		ΔC/C	within +	within +20/-30% of initial value for Vr ≥ 16V						
	a charge / discha	arge resistance of 1	00001	DF	1.25 x initial limit							
				Visual examination	no visible damage							
Mechanical				DCL	initial li	mit						
Shock	MIL-STD-202, M	ethod 213, Condi	tion C	ΔC/C	within:	±5% of initi	al value					
SHOCK				DF	initial li	mit						
				ESR	initial li	initial limit						
				Visual examination	no visil	ole damage	e					
				DCL	initial li	mit						
Vibration	MIL-STD-202, M	ethod 204, Condi	tion D	ΔC/C	within:	±5% of initi	al value					
				DF	initial li	mit						
				ESR	initial li	mit						

<sup>\*</sup>Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.





## **Highest CV/CC Conductive Polymer Chip Capacitors Undertab**

### PRODUCT CATEGORY 5 (TEMPERATURE RANGE -55°C TO +85°C)

TEST		Condition			Characteristics							
				Visual examination	no visible damage							
	Apply rated voltag	a (Ur) at 85°C for 20	000 hours through	DCL	1.25 x initial limit							
Endurance	a circuit impedance	e (Ur) at 85°C for 20 ce of ≤0.1Ω/V. Stabil	lize at room	ΔC/C	within ±20% of initial value							
	temperature for 1-	2 hours before mea	suring.	DF	1.5 x initial	1.5 x initial limit						
				ESR	2 x initial lir	mit						
				Visual examination	no visible d	lamage						
	Store at 85°C, no v	oltage applied, for 2	2000 hours.	DCL	1.25 x initia	al limit						
Storage Life		emperature for 1-2		ΔC/C	within ±209	% of initial va	lue					
-	measuring.			DF	1.5 x initia	l limit						
				ESR	2 x initial lir	mit						
				Visual examination	no visible	damage						
	Store at 65°C and	95% relative humidi	ity for 500 hours	DCL	5 x initial limit							
Humidity	with no applied vo	ltage. Stabilize at ro	oom temperature	ΔC/C	within +40/-20% of initial value							
•	and humidity for 1	-2 hours before mea	asuring.	DF	1.5 x initial limit							
				ESR	2 x initial limit							
	Step	Temperature°C	Duration(min)		+20°C	-55°C	+20°C	+85°C	+20°C			
Temperature	1 2	+20 -55	15 15	DCL	IL*	n/a	IL*	10 x IL*	IL*			
Stability	3	+20	15	ΔC/C	n/a	+0/-20%	±5%	+20/-0%	±5%			
•	<u>4</u> 5	+85 +20	15 15	DF	IL*	1.5 x IL*	IL*	1.5 x IL*	IL*			
	J	120	13	Visual examination	no visible damage							
	Ah. 1 O	(I I ) - t 0 5 0 0 f	1000lf	DCL	initial limit							
Surge		tage (Ur) at 85°C for sec charge, 5 min 30				within +10/-20% of initial value for Vr ≤ 10V						
Voltage		discharge resistance		ΔC/C	within +20/-30% of initial value for Vr ≥ 16V							
				DF	1.25 x initial limit							
				Visual examination	no visible damage							
				DCL	initial limit							
Mechanical	MIL-STD-202. Met	hod 213, Condition (	С	ΔC/C	within ±5%	of initial va	lue					
Shock	, , , ,	.,		DF	initial limit							
				ESR	initial limit							
				Visual examination	no visible	-						
				DCL	initial limit							
Vibration	MIL-STD-202. Met	hod 204, Condition I	D	ΔC/C		of initial va	alue					
				DF.	initial limit							
				ESR	initial limit							

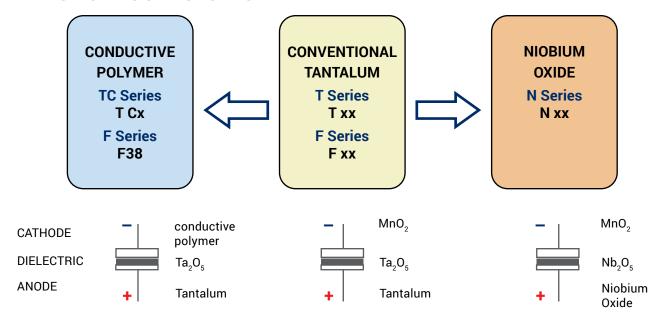
<sup>\*</sup>Initial Limit

Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.





#### SOLID ELECTROLYTIC CAPACITOR ROADMAP



#### **FIVE CAPACITOR CONSTRUCTION STYLES**



#### **SERIES LINE UP:** Conductive Polymer

