



DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) (max)	Package	I _{D (MAX)} T _A = +25°C
30V	190mΩ @ $V_{GS} = 10V$	SOT363	1A
307	$335 \text{m}\Omega$ @ $V_{GS} = 4.5 \text{V}$	301303	0.75A

Description

This MOSFET has been designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Power Management Functions
- Load Switch

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

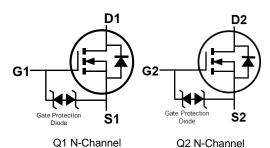
- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe.
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)

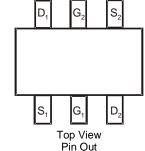




SOT363







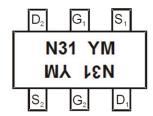
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3190LDW-7	SOT363	3000/Tape & Reel
DMN3190LDW-13	SOT363	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



N31 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: G = 2019) M = Month (ex: 9 = September)

Date Code Key

Year	201	1	~		2019	20	20	2021		2022	2	2023
Code	Υ		~		G	H	+	I		J		K
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	1000 900	mA
Continuous Diam Current (Note 6) VGS = 10V	t < 5s	$T_A = +25$ °C $T_A = +70$ °C	I _D	1300 1000	mA
Maximum Continuous Body Diode Forward Current	t (Note 5)	I _S	0.5	Α	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	I _{DM}	9.6	Α		

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	0.32	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	395	°C/W
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	0.4	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	320	°C/W	
Thermal Resistance, Junction to Case	R _{0JC}	143	C/VV	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$
Zero Gate Voltage Drain Current @T _C = +25°	C I _{DSS}	_	_	1	μA	$V_{DS} = 30V$, $V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	1.5	_	2.8	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance		_	122	190	mΩ	$V_{GS} = 10V, I_D = 1.3A$
Static Dialii-Source Off-Resistance	R _{DS(ON)}	_	181	335	11152	$V_{GS} = 4.5V, I_D = 290mA$
Diode Forward Voltage	V _{SD}	_	_	1.2	V	$V_{GS} = 0V, I_{S} = 250mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	87	_	pF	.,
Output Capacitance	Coss	_	17	_	pF	$V_{DS} = 20V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	12	_	pF	1 = 1.0WH12
Gate Resistance	Rg	_	69.8	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	0.9	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	2.0	_	nC	\\
Gate-Source Charge	Qgs	_	0.3	_	nC	$V_{DS} = 10V, I_{D} = 250mA$
Gate-Drain Charge	Q_{gd}	_	0.3	_	nC	
Turn-On Delay Time Turn-On Rise Time		_	4.5	_	ns	
		_	8.9	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time		_	30.3	_	ns	$R_G = 10\Omega, I_D = 100 \text{mA}$
Turn-Off Fall Time		_	15.6	_	ns	

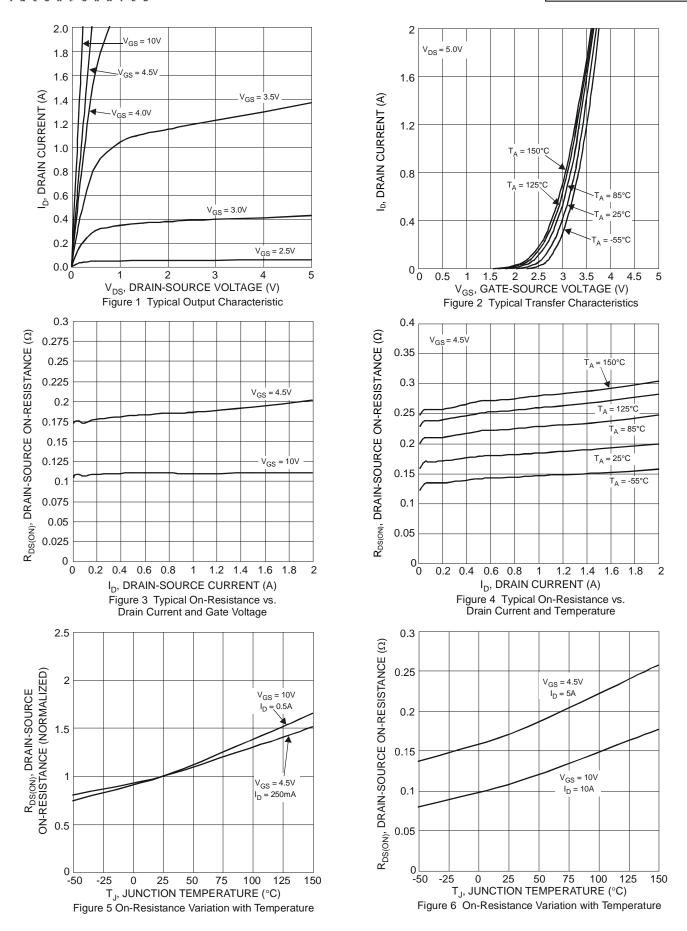
Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.

^{6.} Device mounted on 1" × 1" FR-4 PCB with high coverage 2oz. Copper, single sided.

^{7.} Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.







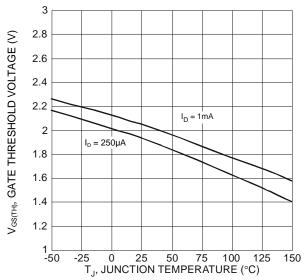
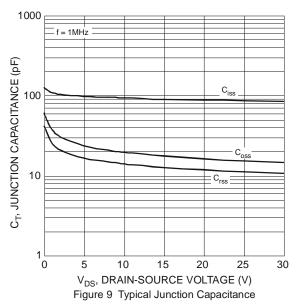
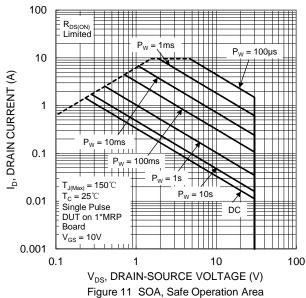
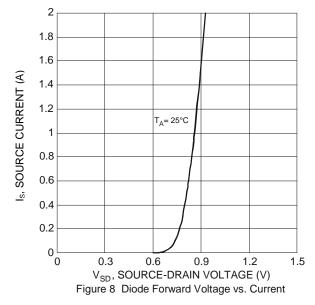
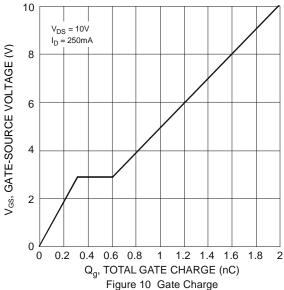


Figure 7 Gate Threshold Variation vs. Junction Temperature







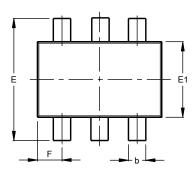


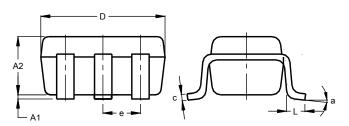


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



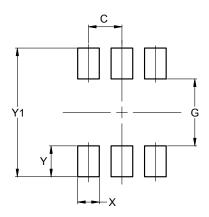


SOT363							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.10	0.30	0.25				
U	0.10	0.22	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C	.650 E	SC				
F	0.40	0.45	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value			
Dillielisions	(in mm)			
С	0.650			
G	1.300			
Х	0.420			
Y	0.600			
Y1	2.500			



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