# 2.5V Drive Nch MOS FET

# 2SK3019

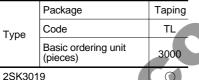
 Structure Silicon N-channel MOSFET

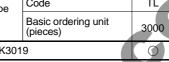
 Applications Interfacing, switching (30V, 100mA)

#### Features

- 1) Low on-resistance.
- 2) Fast switching speed.
- 3) Low voltage drive (2.5V) makes this device ideal for portable equipment.
- 4) Drive circuits can be simple.
- 5) Parallel use is easy.

#### Packaging specifications





#### ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	Vdss	30	V	
Gate-source voltage		Vgss	±20	V
	Continuous	lo	±100	mA
Drain current	Pulsed	Idp <sup>*1</sup>	±400	mA
Total power dissipatio	Pd*2	150	mW	
Channel temperature	Tch	150	°C	
Storage temperature		Tstg	-55 to +150	°C

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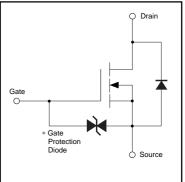
(2)Gate (3)Drain

(1)Source

EMT3

•Dimensions (Unit : mm)

Equivalent circuit



\*A protection diode is included between the date and the source terminals to protect the diode against static electricity when the product is in use. Use a protection circuit when the fixed voltages are exceeded.

\*1 Pw≤10 $\mu$ s, Duty cycle≤1%

\*2 With each pin mounted on the recommended lands.

#### Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth(ch-a)*	833	°C / W	

\* With each pin mounted on the recommended lands.

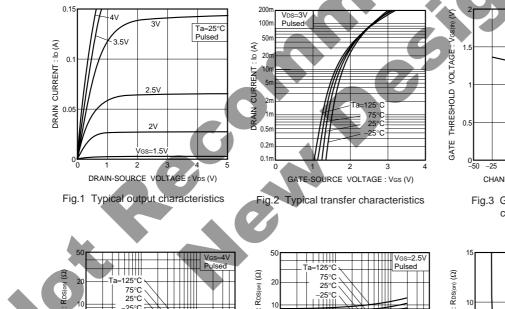


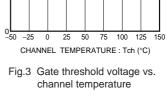
## Transistor

#### •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Gate-source leakage	lgss	-	-	±1	μΑ	Vgs=±20V, Vds=0V
Drain-source breakdown voltage	V(BR)DSS	30	_	-	V	ID=10μA, Vgs=0V
Zero gate voltage drain current	IDSS	_	_	1.0	μΑ	VDS=30V, VGS=0V
Gate threshold voltage	VGS(th)	0.8	_	1.5	V	Vds=3V, Id=100μA
Static drain-source on-state	RDS(on)	-	5	8	Ω	ID=10mA, Vgs=4V
resistance	RDS(on)	-	7	13	Ω	ID=1mA, Vgs=2.5V
Forward transfer admittance	Y <sub>fs</sub>	20	_	-	ms	ID=10mA, VDs=3V
Input capacitance	Ciss	_	13	-	pF	VDS=5V
Output capacitance	Coss	-	9	-	pF	Vgs=0V
Reverse transfer capacitance	Crss	-	4	-	pF	f=1MHz
Turn-on delay time	td(on)	-	15	-	ns	ID=10mA, VDD = 5V
Rise time	tr	-	35	-	ns	Vgs=5V
Turn-off delay time	td(off)	-	80	-	ns	RL=500Ω
Fall time	tr	_	80	-	ns	Rg=10Ω



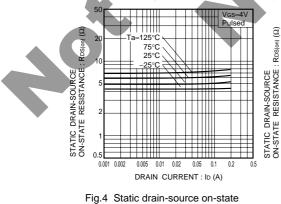




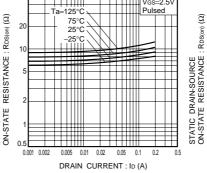
VDS=3V ID=0.1mA

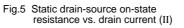
Ta=25°C

Pulsed



resistance vs. drain current (I)





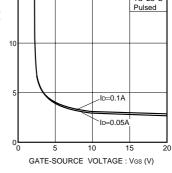
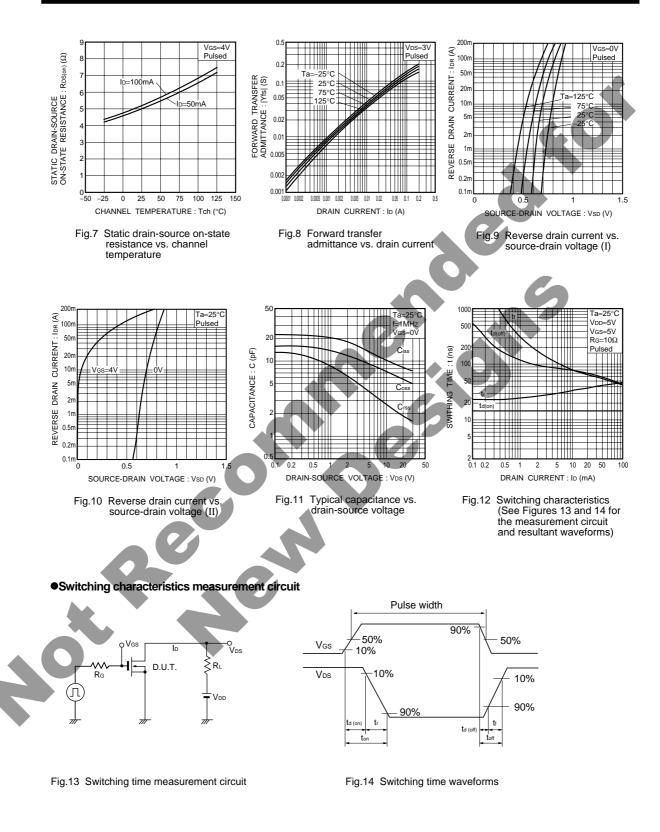


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

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Rev.C

### Transistor



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