



PJZ18NA50

500V N-Channel MOSFET

Voltage

500 V

Current

18 A

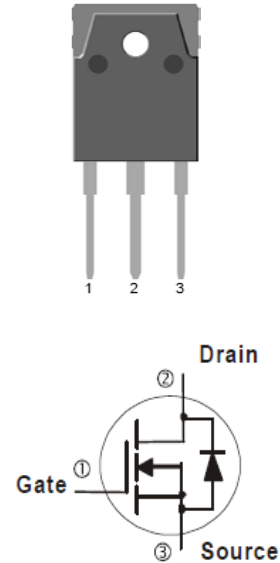
Features

- $R_{DS(ON)}, V_{GS}@10V, I_D@9A} < 0.35\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.

Mechanical Data

- Case: TO-3PL Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-3PL Approx. Weight : 0.182 ounces, 5.174grams

TO-3PL



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TO-3PL	UNITS
Drain-Source Voltage	V_{DS}	500	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current	I_D	18	A
Pulsed Drain Current	I_{DM}	72	A
Single Pulse Avalanche Energy ^(Note 1)	E_{AS}	1502	mJ
Power Dissipation	$T_C=25^\circ\text{C}$	240	W
	Derate above 25°C	1.92	W/ $^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal resistance			$^\circ\text{C}/\text{W}$
- Junction to Case	$R_{\theta JC}$	0.52	
- Junction to Ambient	$R_{\theta JA}$	50	

- Limited only By Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	3.1	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=9A$	-	0.27	0.35	Ω
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=500V, V_{GS}=0V$	-	0.02	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	-	± 10	± 100	nA
Diode Forward Voltage	V_{SD}	$I_S=18A, V_{GS}=0V$	-	0.85	1.4	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=400V, I_D=18A,$ $V_{GS}=10V$ (Note 2,3)	-	38	-	nC
Gate-Source Charge	Q_{gs}		-	13	-	
Gate-Drain Charge	Q_{gd}		-	12	-	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V,$ $f=1.0\text{MHz}$	-	2407	-	pF
Output Capacitance	C_{oss}		-	360	-	
Reverse Transfer Capacitance	C_{rss}		-	7.2	-	
Switching						
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=250V, I_D=18A,$ $R_G=25\Omega$ (Note 2,3)	-	60	-	ns
Turn-On Rise Time	t_r		-	132	-	
Turn-Off Delay Time	$t_{d(off)}$		-	116	-	
Turn-Off Fall Time	t_f		-	76	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	18	A
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	---	-	-	72	A
Reverse Recovery Time	t_{rr}	$V_{GS}=0V, I_S=18A$	-	583	-	ns
Reverse Recovery Charge	Q_{rr}	$di_f/dt=100A/\mu s$ (Note 2)	-	7.2	-	μC

NOTES :

1. $L=30\text{mH}, I_{AS}=9.65A, V_{DD}=50V, R_G=25\text{ohm}$, Starting $T_J=25^{\circ}\text{C}$
2. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
3. Essentially independent of operating temperature typical characteristics.



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TYPICAL CHARACTERISTIC CURVES

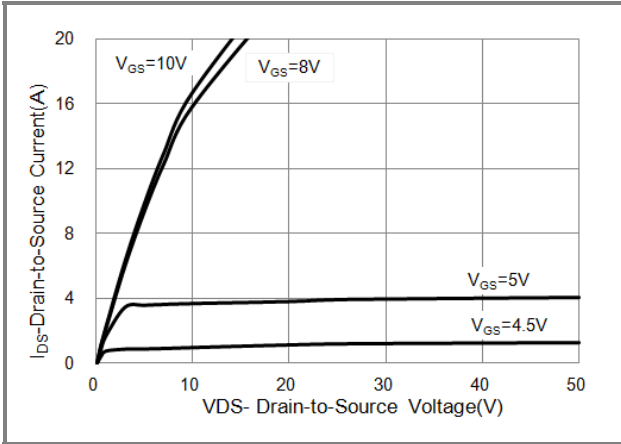


Fig.1 Output Characteristics

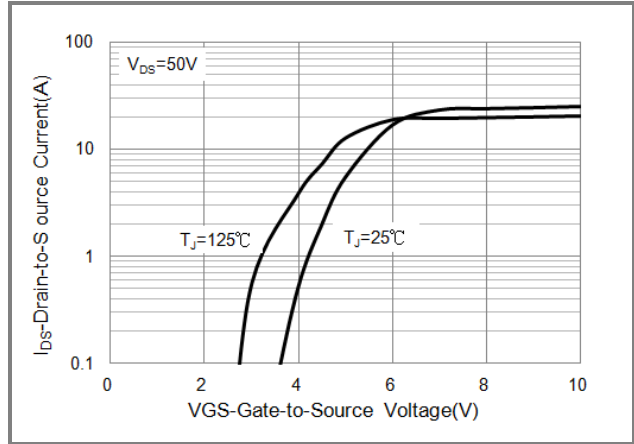


Fig.2 Transfer Characteristics

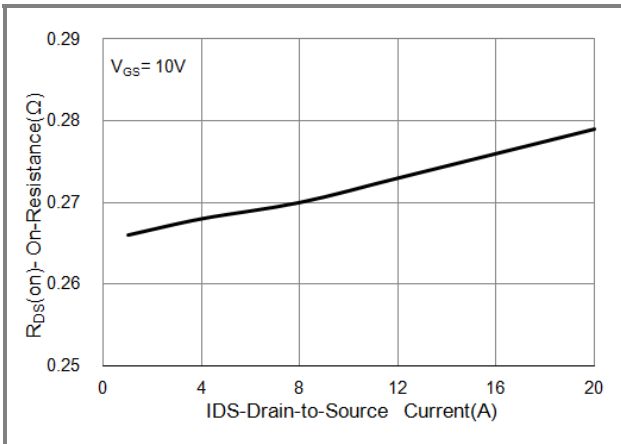


Fig.3 On-Resistance vs. Drain Current

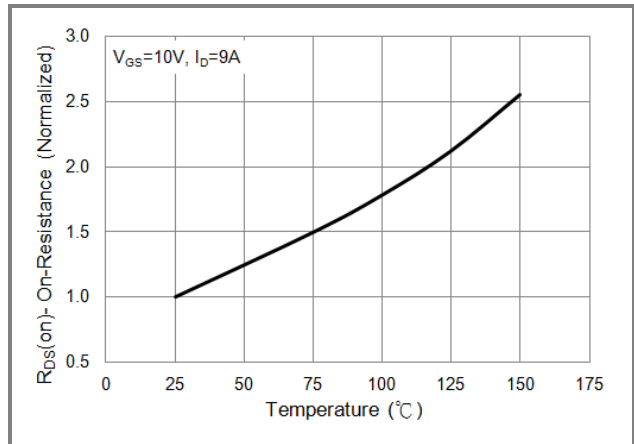


Fig.4 On-Resistance vs. Junction temperature

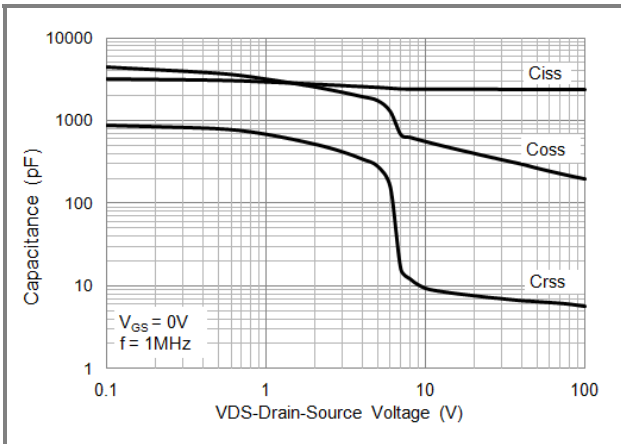


Fig.5 Capacitance vs. Drain-Source Voltage

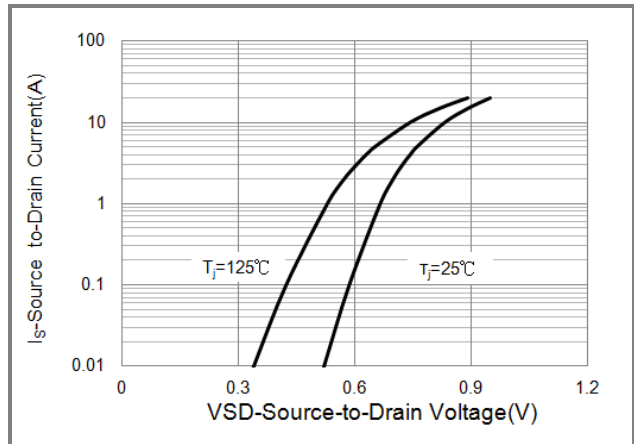


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

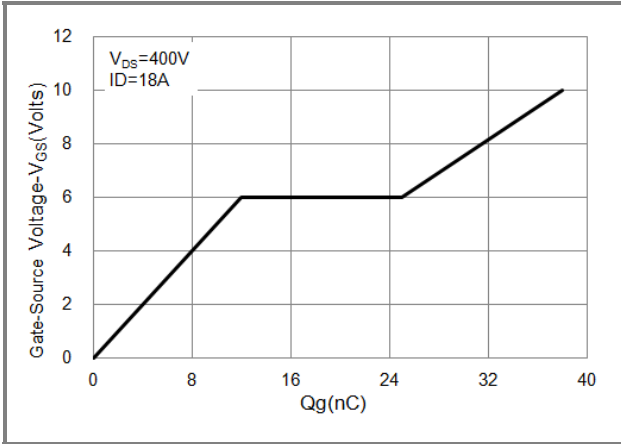


Fig.7 Gate-Charge Characteristics

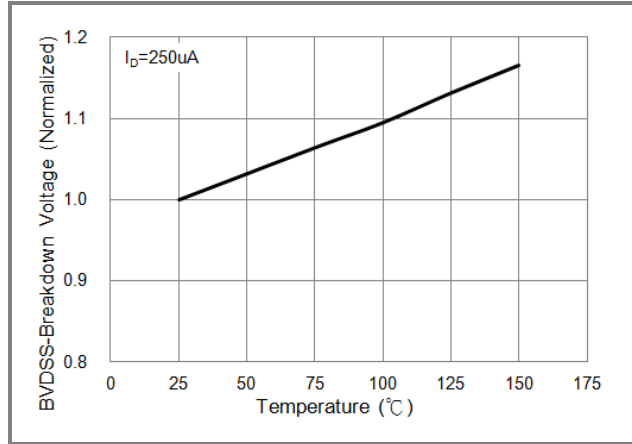


Fig.8 Breakdown Voltage Variation vs. Temperature

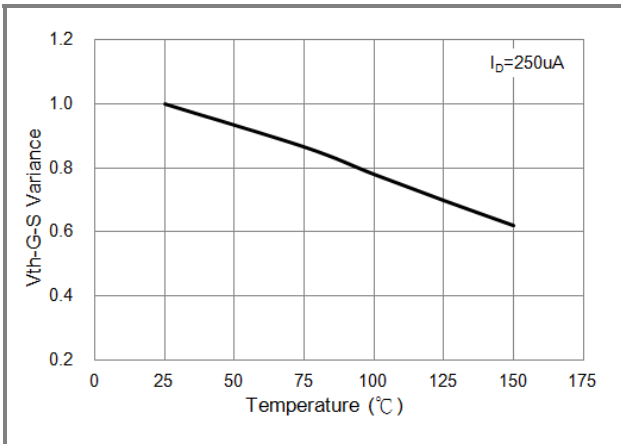


Fig.9 Threshold Voltage Variation with Temperature

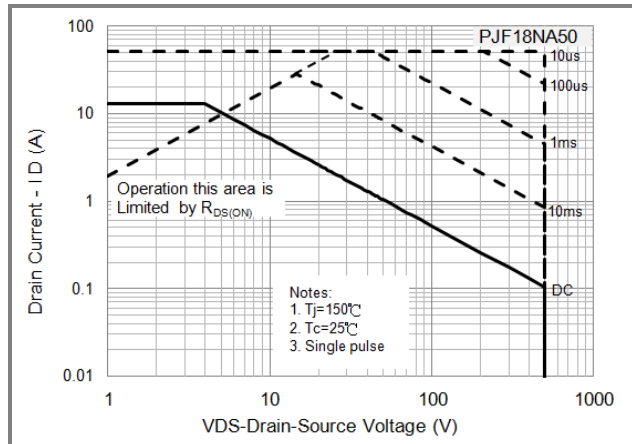


Fig.10 Maximum Safe Operating Area

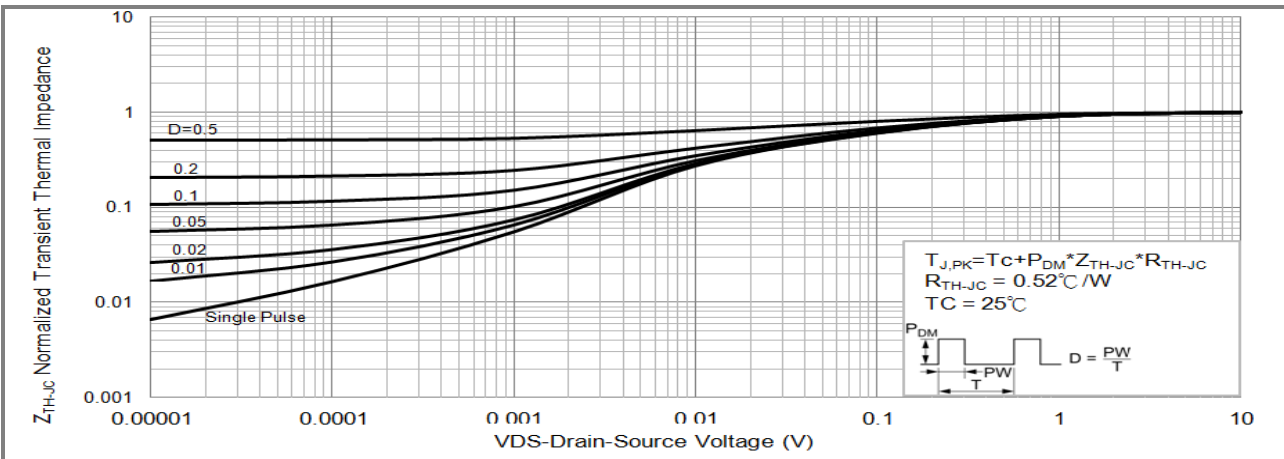
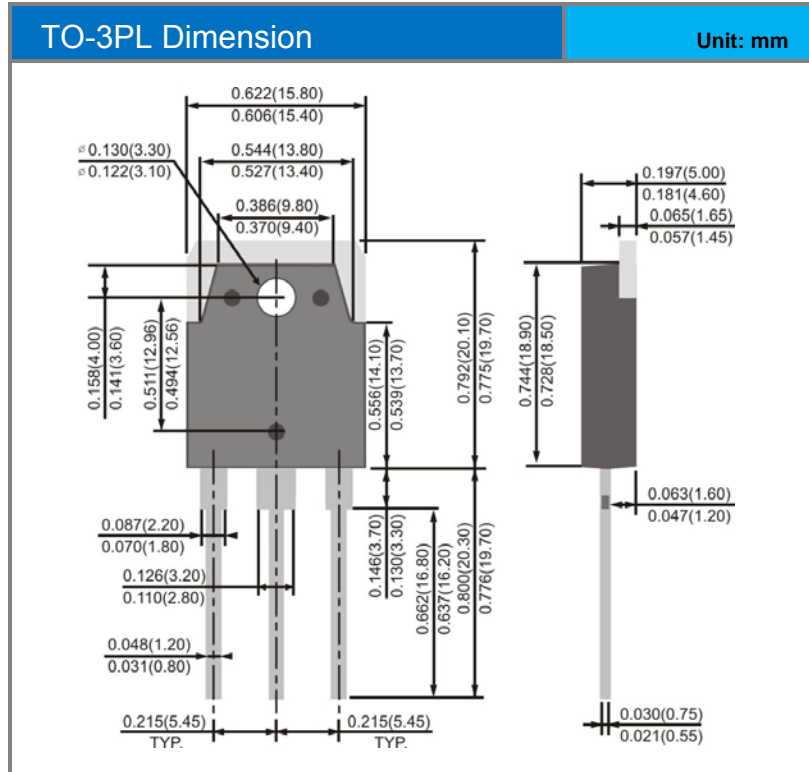


Fig.13 PJP18NA50 Normalized Transient Thermal Impedance vs. Pulse Width



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Packaging Information





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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJZ18NA50_T0_10001	TO-3PL	30pcs/tube	Z18NA50	RoHS



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