## 

Drain

Source

# PJW5P03

### 30V P-Channel Enhancement Mode MOSFET

Voltage

#### -30 V Current

#### Features

- $R_{DS(ON)}$ ,  $V_{GS}$ @-10V,  $I_D$ @-3A<50m $\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}$ @-4.5V,  $I_D$ @-2A<80m $\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- SOT-223 Approx. Weight : 0.043 ounces, 0.123grams

### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25<sup>°</sup>C unless otherwise noted)

-4.6 A

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	-30	V	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20		
Continuous Drain Current	T <sub>A</sub> =25°C		-4.6	A	
	T <sub>A</sub> =70°C	Ι <sub>D</sub>	-3.7		
Pulsed Drain Current <sup>(Note 1)</sup>		I <sub>DM</sub>	-20		
Power Dissipation	T <sub>A</sub> =25°C	_	1.6		
	T <sub>A</sub> =70°C	P <sub>D</sub>	1.0	W	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient <sup>(Note 5)</sup>		R <sub>eJA</sub>	78.1	°C/W	

SOT-223

Gate <sup>①</sup>

• Limited only By Maximum Junction Temperature

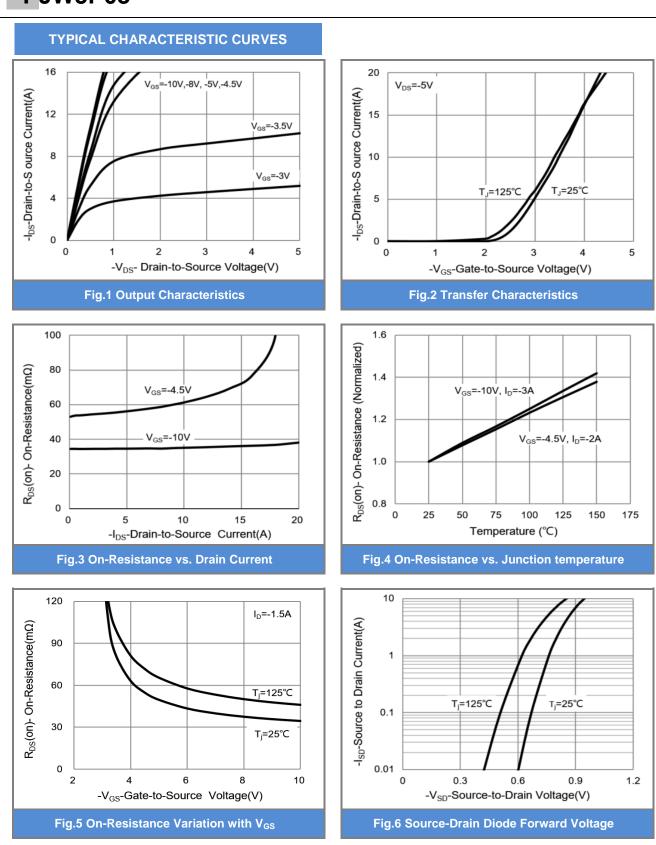


#### **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

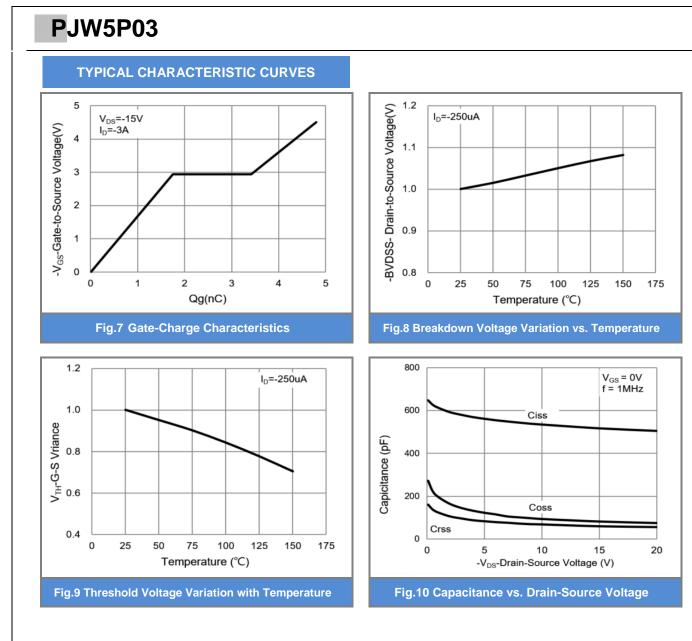
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=-250$ uA	-1	-1.6	-2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-3A	-	40	50	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2A	-	60	80	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg		-	4.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-3A, V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	1.7	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.7	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	516	-	pF
Output Capacitance	Coss		-	83	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DS</sub> =-15V, I <sub>D</sub> =-1A, V <sub>GEN</sub> =-10V, R <sub>G</sub> =6Ω	-	5.6	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	8.5	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	27	-	
Turn-Off Fall Time	t <sub>f</sub>	(()))	-	18	-	
Drain-Source Diode	·	<u> </u>				
Maximum Continuous Drain-Source			-	-	-4.6	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.76	-1	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. The maximum current rating is package limited.
- 4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}=150^{\circ}C$ . Ratings are based on low frequency and duty cycles to keep initial  $T_{J}=25^{\circ}C$ .
- 5.  $R_{\Theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing.







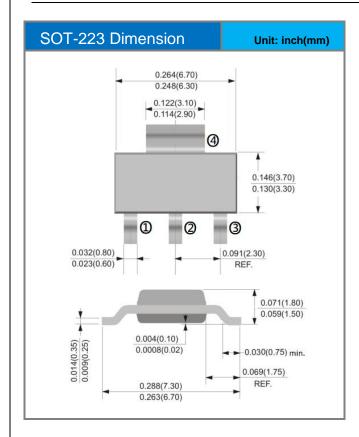


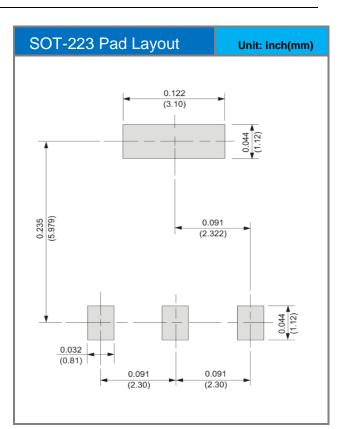






#### Packaging Information & Mounting Pad Layout









#### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJW5P03_R2_00001	SOT-223	2,500pcs / 13" reel	W5P03	Halogen free



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