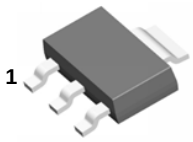
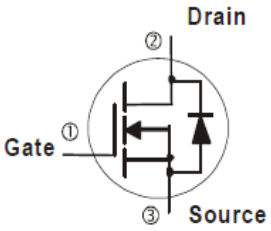
PJW4N10

100V N-Channel Enhancement Mode MOSFET

Voltage	100 V	Current	4 A

SOT-223





Features

- $R_{DS(ON)}$, V_{GS}@10V,I_D@2A<258mΩ
- $R_{DS(ON)}$, V_{GS}@6V,I_D@1A<268mΩ
- Low On-Resistance
- Low input capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123 grams
- Marking: W4N10

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	T_C=25°C	I_D	4	A
	T_C=100°C		2.5	
Pulsed Drain Current(Note 1)	T_C=25°C	I_{DM}	8	
Power Dissipation	T_C=25°C	P_D	8	W
	T_C=100°C		3.2	
Continuous Drain Current	T_A=25°C	I_D	2.5	A
	T_A=70°C		2	A
Power Dissipation	T_A=25°C	P_D	3.1	W
Power Dissipation	T_A=70°C		2	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal resistance(Note 4,5)	Junction to Case	$R_{\theta JC}$	15.6	°C/W
	Junction to Ambient	$R_{\theta JA}$	40.3	

- Limited only By Maximum Junction Temperature

PJW4N10

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V,I_D=250uA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	2.0	2.85	3.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=2A	-	210	258	mΩ
		V_{GS}=6V,I_D=1A	-	220	268	
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=100V,V_{GS}=0V	-	-	1	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V_{DS}=60V, I_D=4A, V_{GS}=10V (Note 2,3)	-	6.7	-	nC
Gate-Source Charge	Q_{gs}		-	2.1	-	
Gate-Drain Charge	Q_{gd}		-	1.1	-	
Input Capacitance	Ciss	V_{DS}=25V, V_{GS}=0V, f=1.0MHZ	-	378	-	pF
Output Capacitance	Coss		-	26	-	
Reverse Transfer Capacitance	Crss		-	20	-	
Turn-On Delay Time	$td_{(on)}$	V_{DS}=50V,RL=12.5Ω, V_{GS}=10V, R_G=6Ω (Note 2,3)	-	4.3	-	ns
Turn-On Rise Time	t_r		-	22	-	
Turn-Off Delay Time	$td_{(off)}$		-	9.7	-	
Turn-Off Fall Time	t_f		-	8.2	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	4	A
Diode Forward Voltage	V_{SD}	I_S=1A,V_{GS}=0V	-	0.78	1.2	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
4. The maximum current rating is package limited
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing

PJW4N10

TYPICAL CHARACTERISTIC CURVES

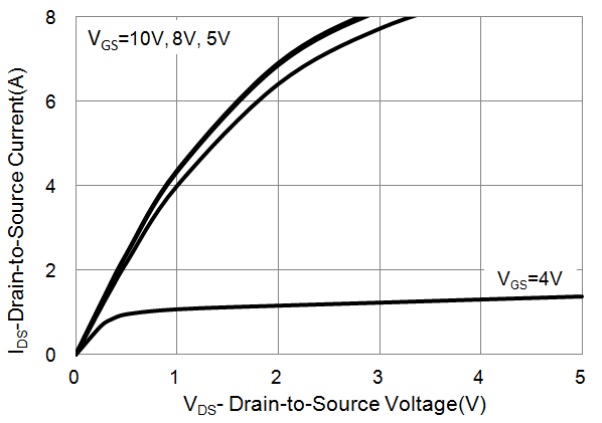


Fig.1 Output Characteristics

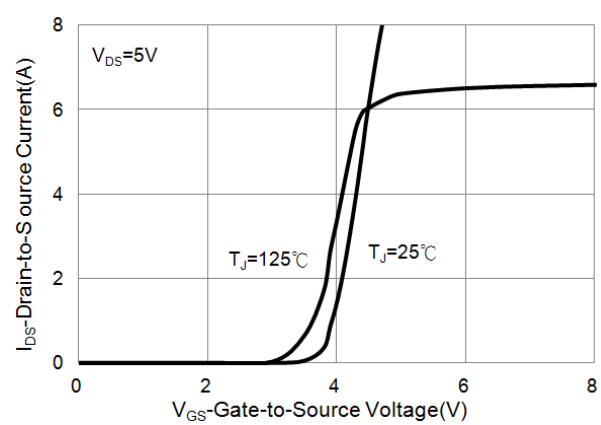


Fig.2 Transfer Characteristics

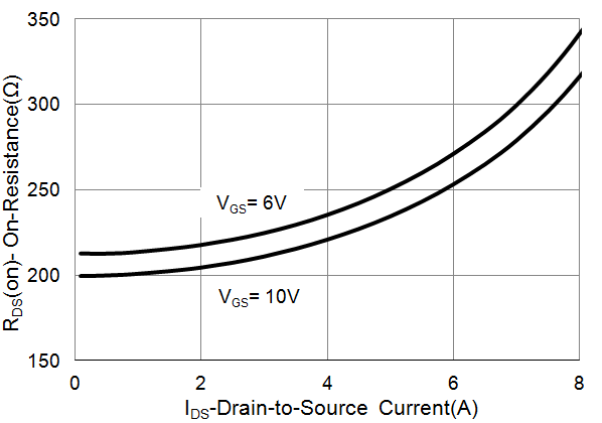


Fig.3 On-Resistance vs. Drain Current

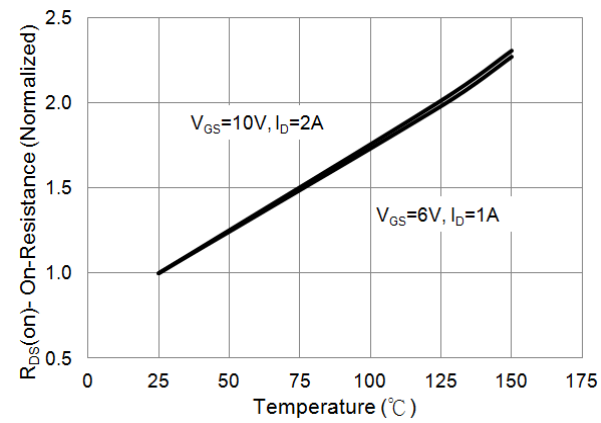


Fig.4 On-Resistance vs. Junction temperature

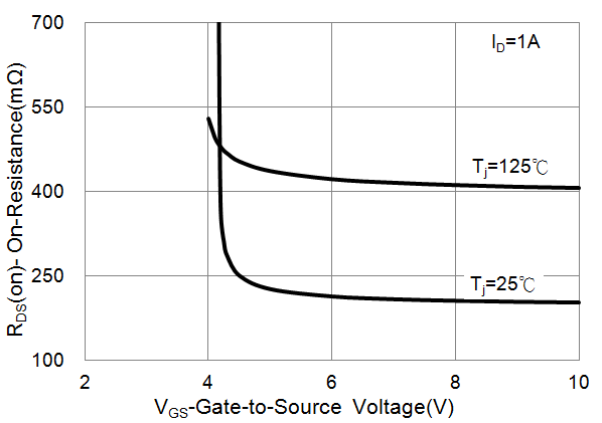


Fig.5 On-Resistance Variation with VGS.

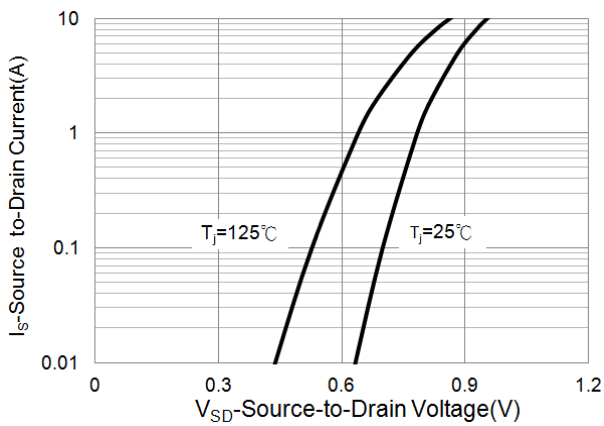


Fig.6 Source-Drain Diode Forward Voltage

PJW4N10

TYPICAL CHARACTERISTIC CURVES

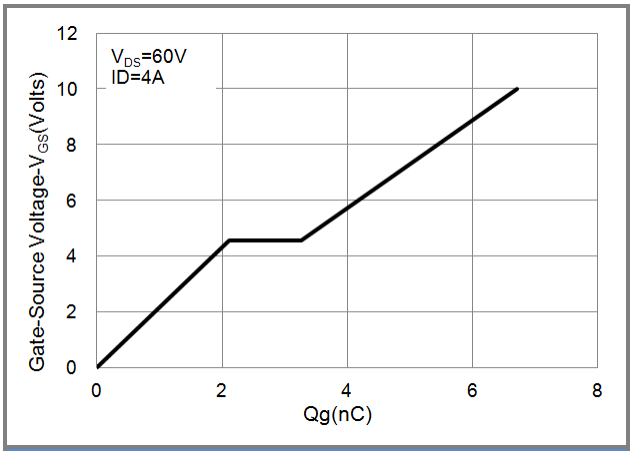


Fig.7 Gate-Charge Characteristics

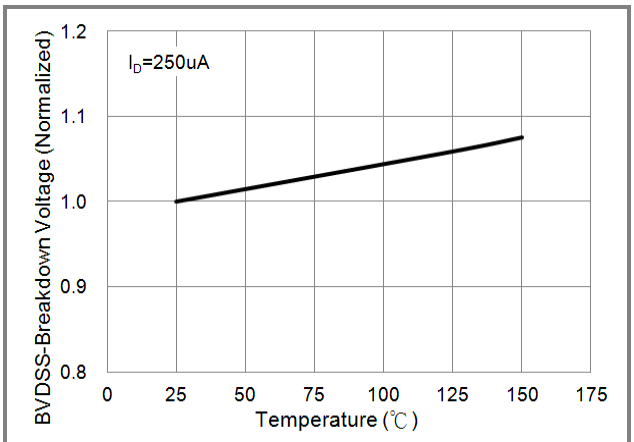


Fig.8 Breakdown Voltage Variation vs. Temperature

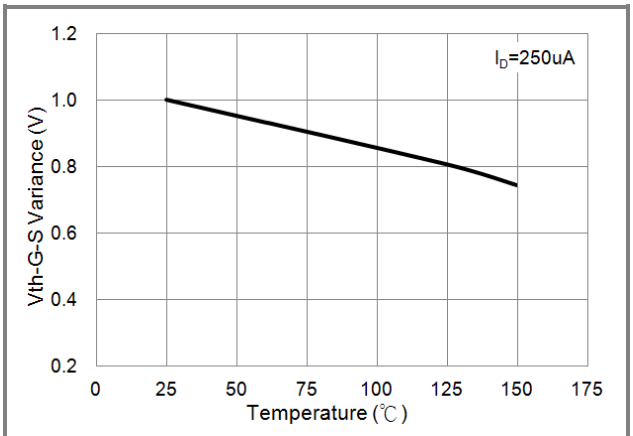


Fig.9 Threshold Voltage Variation with Temperature

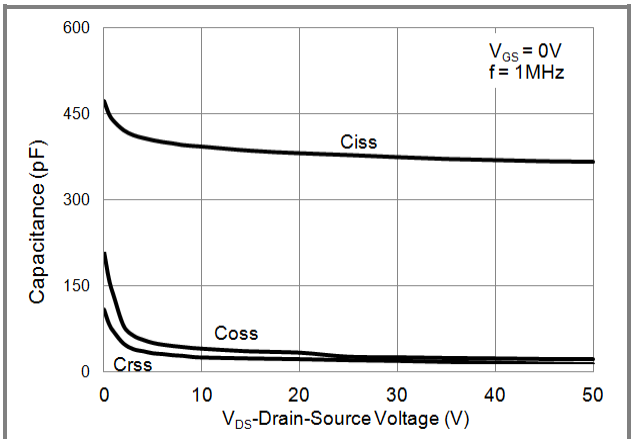


Fig.10 Capacitance vs. Drain-Source Voltage

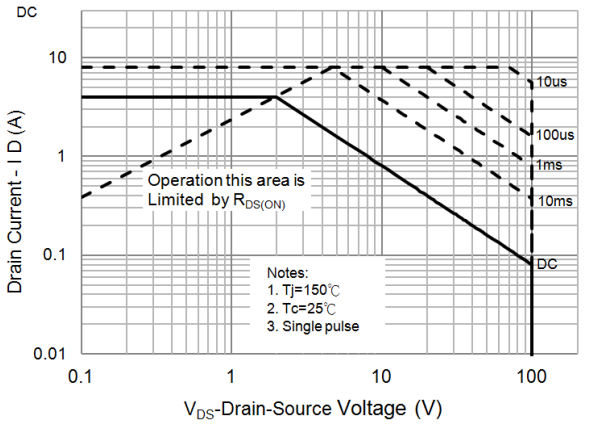


Fig.11 Maximum Safe Operating Area

PJW4N10

TYPICAL CHARACTERISTIC CURVES

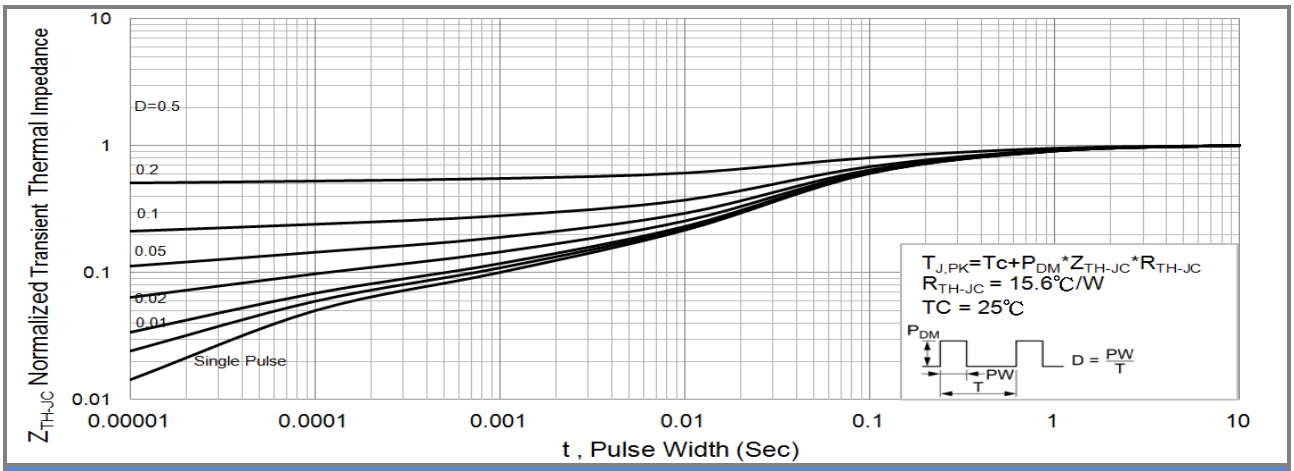
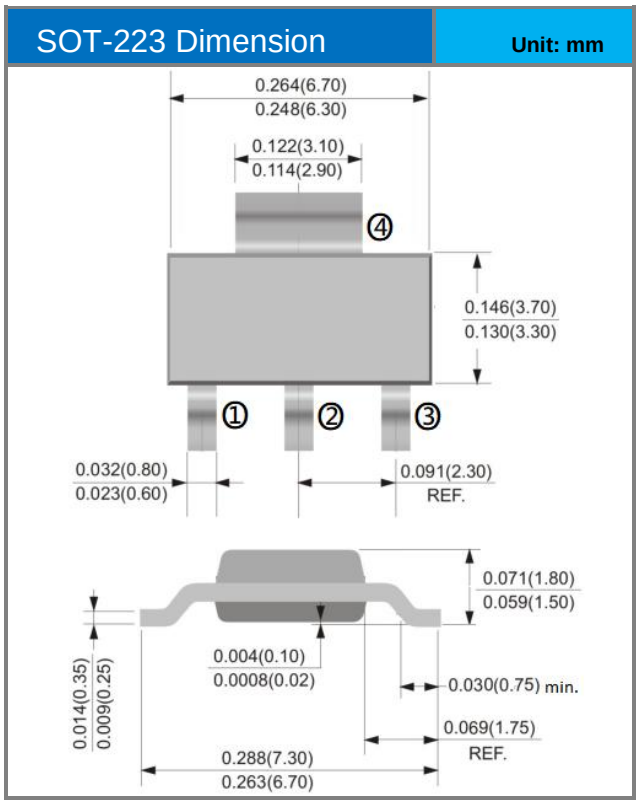


Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

PJW4N10

Packaging Information .

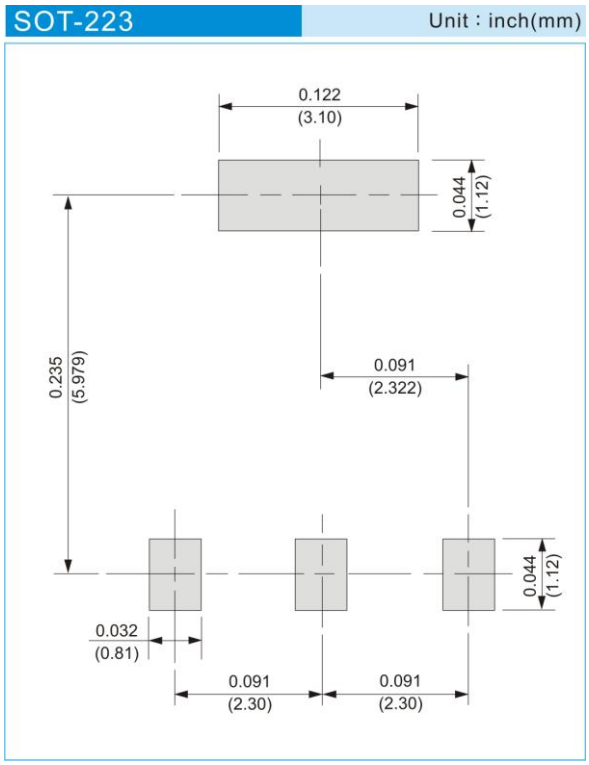


PJW4N10

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJW4N10_R2_00001	SOT-223	2,500pcs / 13” reel	W4N10	Halogen free

MOUNTING PAD LAYOUT



PJW4N10

Disclaimer

- Reproducing and modifying information of the document is prohibited without permission from Panjit International Inc..
- Panjit International Inc. reserves the rights to make changes of the content herein the document anytime without notification. Please refer to our website for the latest document.
- Panjit International Inc. disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- Panjit International Inc. does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the herein document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications. Panjit International Inc. makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown herein are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Panjit International Inc. for any damages resulting from such improper use or sale.
- Since Panjit uses lot number as the tracking base, please provide the lot number for tracking when complaining.