



PJV1703

20V P-Channel MOSFET

Voltage

-20 V

Current

-0.3 A

Features

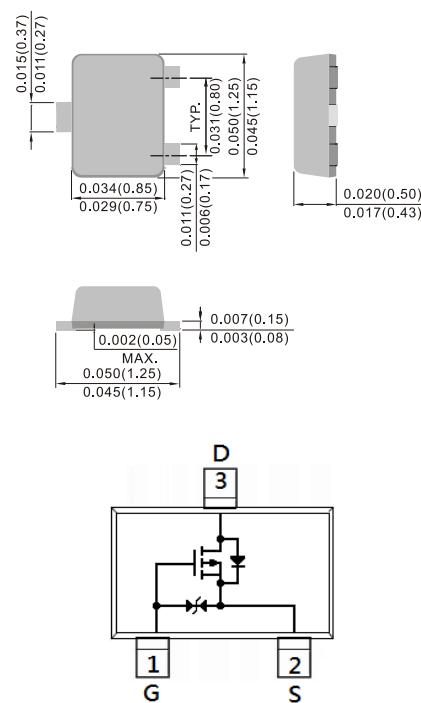
- Switching with Low R_{DS(ON)}
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: SOT-723 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00005 ounces, 0.0013 grams

SOT-723

Unit : inch(mm)



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current	I_D	-0.3	A
Pulsed Drain Current ^(Note 2)	I_{DM}	-0.8	
Power Dissipation	$T_a=25^\circ\text{C}$	150	mW
		1.2	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient ^(Note 3)	$R_{\theta JA}$	833	$^\circ\text{C}/\text{W}$



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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.5	-0.7	-1.1	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D= -0.3A$	-	0.6	1.0	Ω
		$V_{GS}=-2.5V, I_D= -0.2A$	-	0.9	1.5	
		$V_{GS}=-1.8V, I_D= -0.1A$	-	1.5	2.2	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	± 10	
Diode Forward Voltage	V_{SD}	$I_S=-0.1A, V_{GS}=0V$	-	-0.82	-1.2	V
Dynamic <small>(Note 5)</small>						
Total Gate Charge	Q_g	$V_{DS}=-10V, I_D=-0.2A,$ $V_{GS}=-4.5V$ <small>(Note 1,2)</small>	-	1.4	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	0.3	-	
Input Capacitance	C_{iss}	$V_{DS}=-16V, V_{GS}=0V,$ $f=1.0MHz$	-	53	-	pF
Output Capacitance	C_{oss}		-	16	-	
Reverse Transfer Capacitance	C_{rss}		-	13	-	
Turn-On Delay Time	$td_{(on)}$	$V_{DD}=-10V, I_D=-0.1A,$ $V_{GS}=-4.5V,$ $R_G=10\Omega$ <small>(Note 1,2)</small>	-	3.4	-	ns
Turn-On Rise Time	tr		-	3.6	-	
Turn-Off Delay Time	$td_{(off)}$		-	9.6	-	
Turn-Off Fall Time	tf		-	7.3	-	

NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

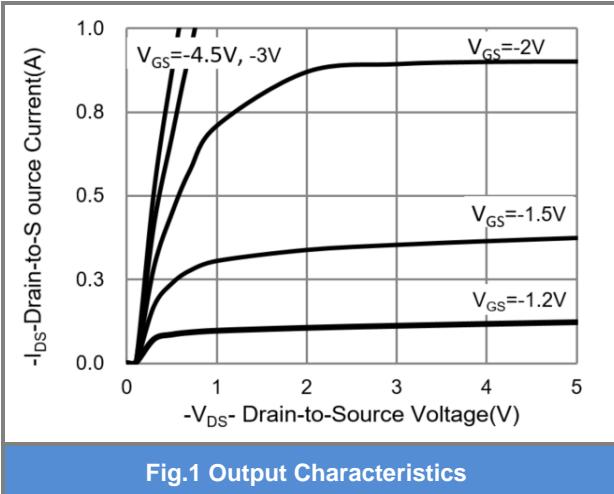


Fig.1 Output Characteristics

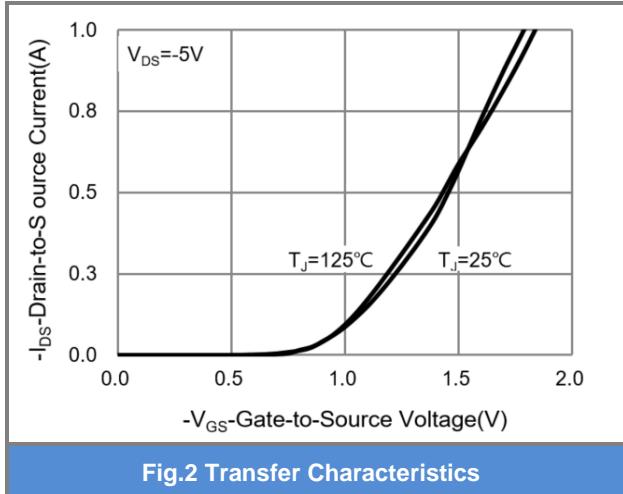


Fig.2 Transfer Characteristics

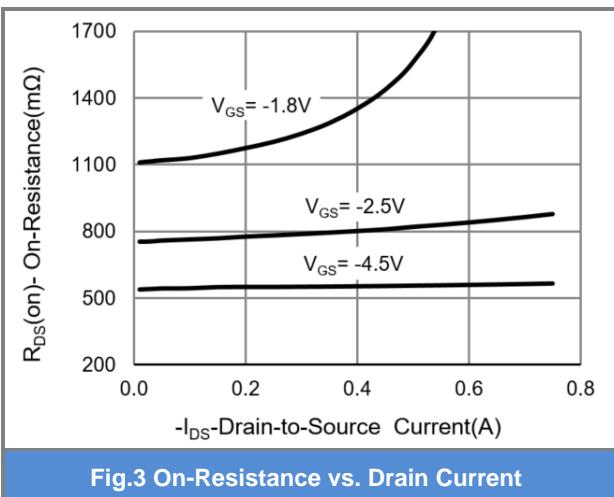


Fig.3 On-Resistance vs. Drain Current

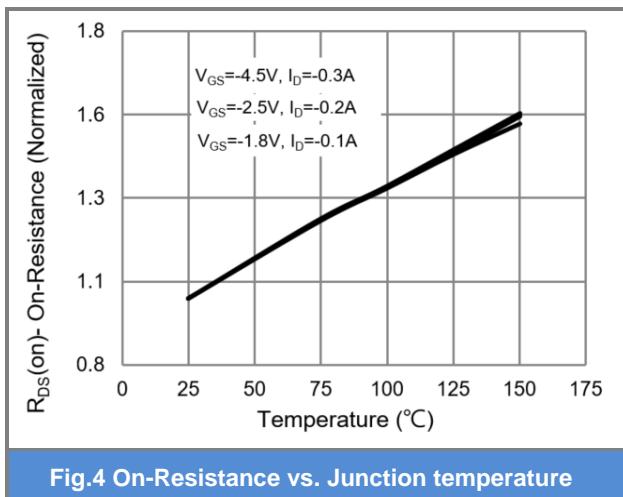


Fig.4 On-Resistance vs. Junction temperature

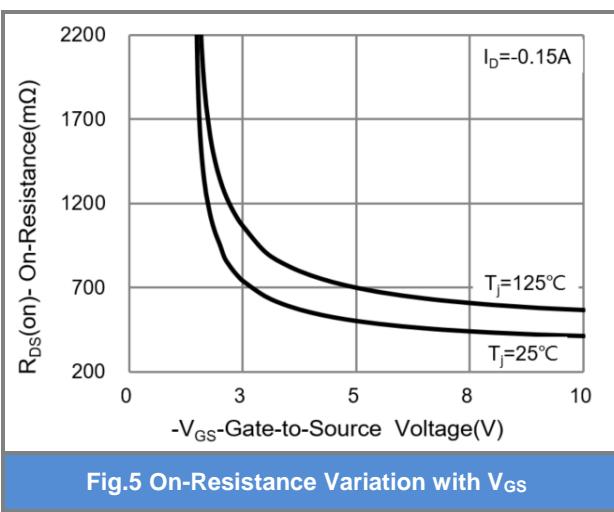


Fig.5 On-Resistance Variation with V_{GS}

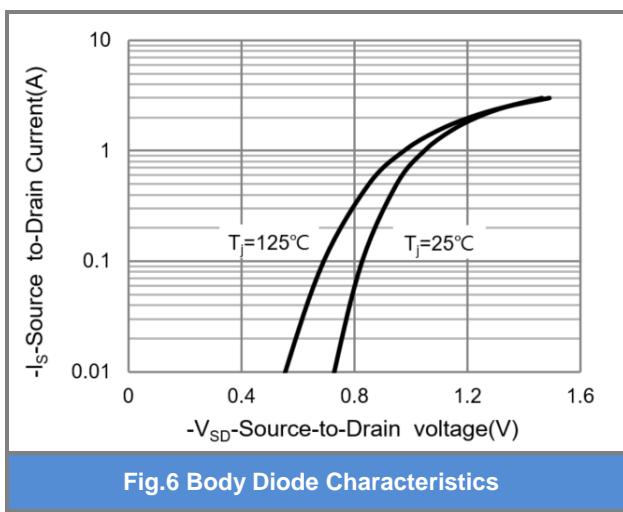


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

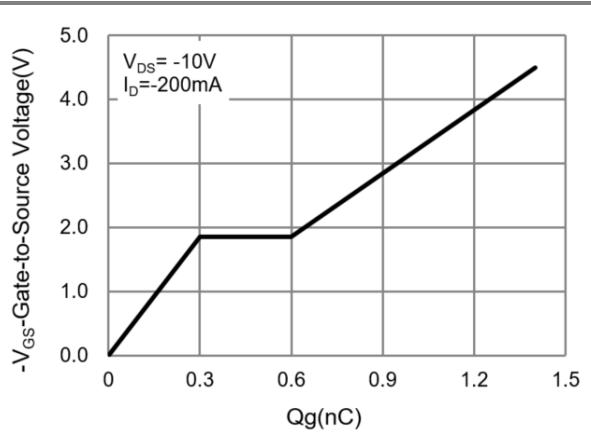


Fig.7 Gate-Charge Characteristics

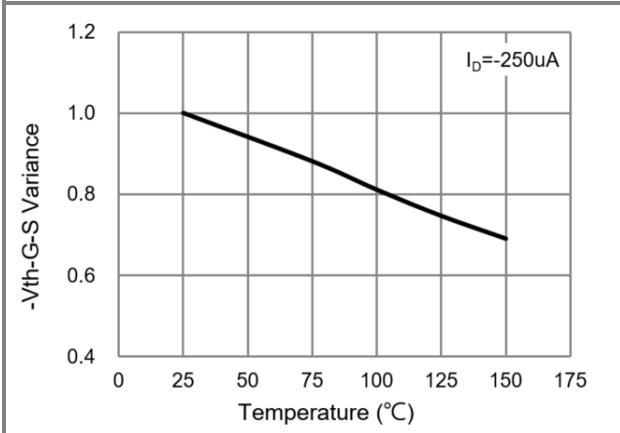


Fig.8 Threshold Voltage Variation with Temperature

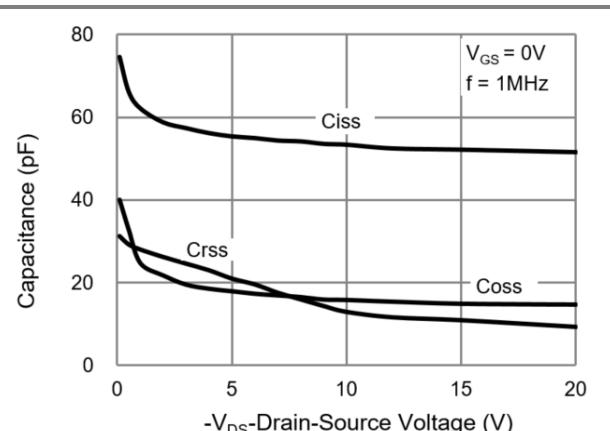


Fig.9 Capacitance vs. Drain-Source Voltage

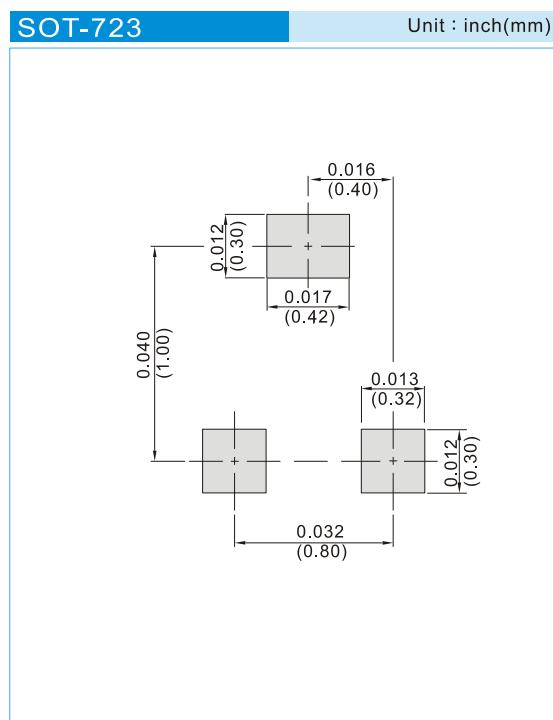


PJV1703

Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJV1703_R1_00001	SOT-723	8K pcs / 7" reel	P3	Halogen free

Mounting Pad Layout





PJV1703

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