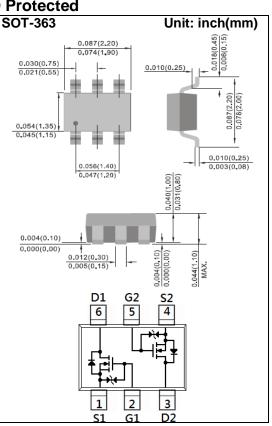
ΡΛΝ	ĴΪΤ
	SEMI CONDUCTOR

20V N-Channel Enhancement Mode MOSFET – ESD Protected

Voltage 20 V Current 0.5A Features • RDS(ON) , VGS@4.5V, ID@0.5A<0.4Ω RDS(ON), VGS@2.5V, ID@0.3A<0.7Ω RDS(ON), VGS@1.8V, ID@0.1A<1.2Ω(typ.) • Advanced Trench Process Technology • Specially Designed for Switch Load, PWM Application, etc. ESD Protected • Lead free in compliance with EU RoHS 2011/65/EU directive. Green molding compound as per IEC61249 Std.(Halogen Free) **Mechanical Data** • Case : SOT-363 Package • Terminals : Solderable per MIL-STD-750, Method 2026 Approx. Weight : 0.0002 ounces, 0.006 grams • Marking : T02



Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	0.5	А
Pulsed Drain Current (Note 4)		I _{DM}	2.0	А
Power Dissipation	T _a =25°C	P _D	350	mW
	Derate above 25°C		2.8	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R_{\thetaJA}	357	°C/W



Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						-
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.4	0.63	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =0.5A	-	0.32	0.4	Ω
		V _{GS} =2.5V,I _D =0.3A	-	0.6	0.7	
		V _{GS} =1.8V,I _D =0.1A	-	1.2	-	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =16V, V _{GS} =0V	-	0.02	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 10V, V _{DS} =0V	-	<u>+</u> 2	<u>+</u> 10	uA
Dynamic (Note 5)						
Total Gate Charge	Q_{g}		-	0.9	-	nC
Gate-Source Charge	Q_{gs}	V_{DS} =10V, I _D =0.5A, V_{GS} =4.5V ^(Note 1,2)	-	0.3	-	
Gate-Drain Charge	Q_gd		-	0.1	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	39	-	pF
Output Capacitance	Coss		-	18	-	
Reverse Transfer Capacitance	Crss		-	9	-	
Turn-On Delay Time	td _(on)		-	3	-	
Turn-On Rise Time	tr	V_{DD} =10V, I _D =0.5A, V_{GS} =4.5V, R_{G} =6 $\Omega^{(Note 1,2)}$	-	22	-	ns
Turn-Off Delay Time	td _(off)		-	7	-	
Turn-Off Fall Time	tf	R _G =012	-	19	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I _S		_	-	0.4	Α
Diode Forward Current	18				0.4	~
Diode Forward Voltage	V_{SD}	I _S =0.5A, V _{GS} =0V	-	0.91	1.3	V

NOTES :

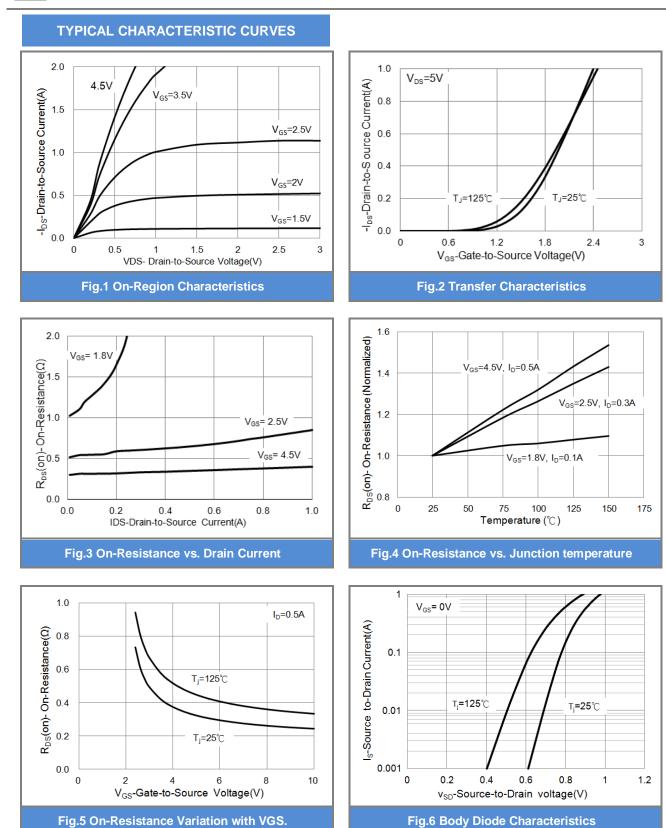
1. Pulse width</br>

2. Essentially independent of operating temperature typical characteristics.

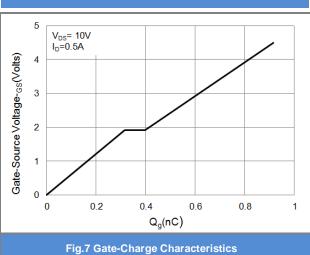
3. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper

- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing









TYPICAL CHARACTERISTIC CURVES

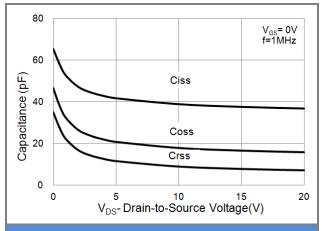
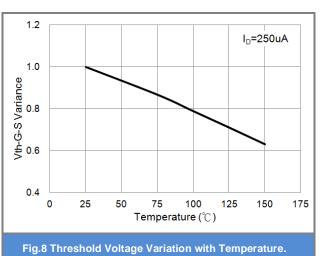


Fig.9 Threshold Voltage Variation with Temperature.



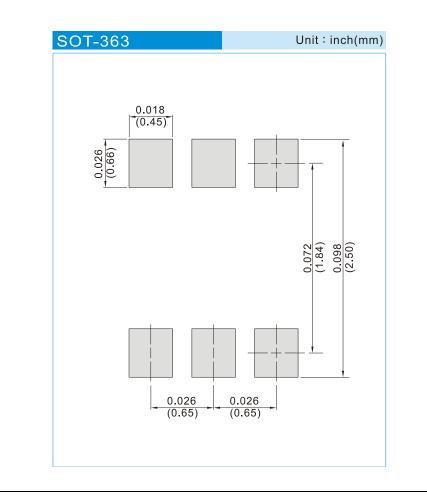




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJT7802_R1_00001	SOT-363	3K pcs / 7" reel	T02	Halogen free
PJT7802_R2_00001	SOT-363	10K pcs / 13" reel	T02	Halogen free

MOUNTING PAD LAYOUT





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