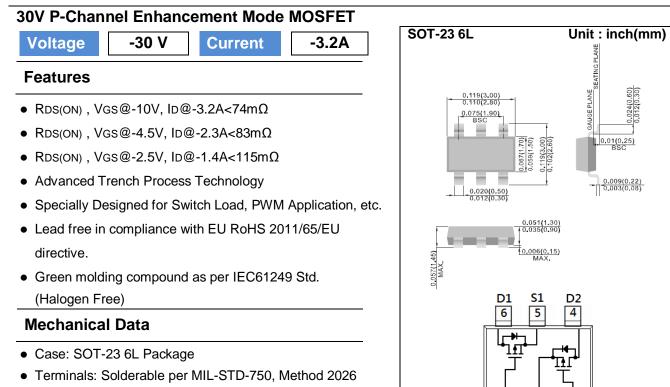
PAN	JIT
	SEMI CONDUCTOR





• Approx. Weight: 0.0005 ounces, 0.014 grams

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 12	V
Continuous Drain Current		I _D	-3.2	А
Pulsed Drain Current		I _{DM}	-13	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		$R_{ extsf{ heta}JA}$	100	°C/W

G1

\$7

GC



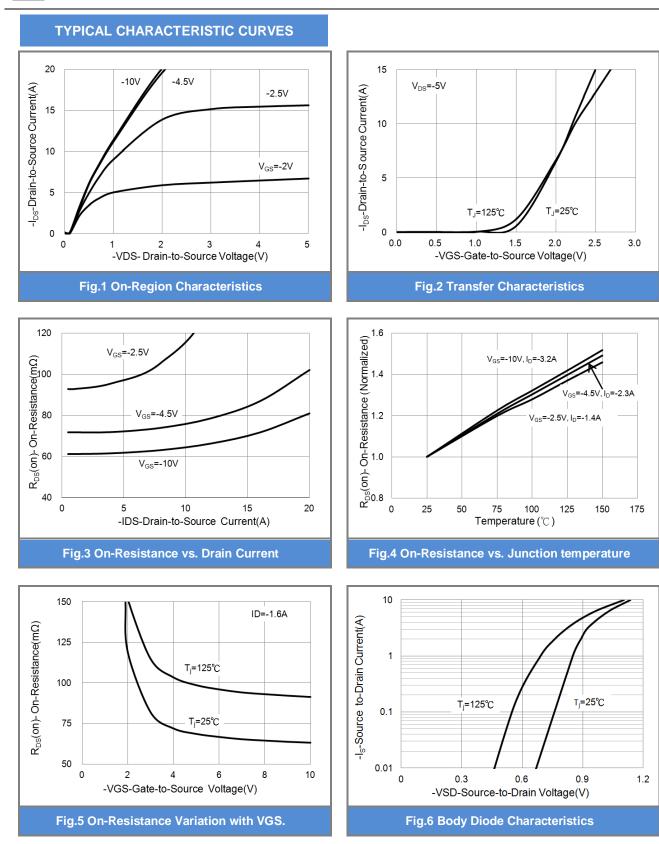
Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static	[1	Г	1	[1
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I_{D} =-250uA	-30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-0.5	-0.96	-1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3.2A	-	60	74	mΩ
		V _{GS} =-4.5V, I _D =-2.3A	-	67	83	
		V _{GS} =-2.5V, I _D =-1.4A	-	84	115	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-0.01	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 12V, V _{DS} =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg	V _{DS} =-15V, I _D =-3.2A, V _{GS} =-10V ^(Note 1,2)	-	15	-	nC
Gate-Source Charge	Q_gs		-	1.3	-	
Gate-Drain Charge	Q_gd		-	2	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V,	-	633	-	pF
Output Capacitance	Coss		-	50	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	35	-	
Switching						
Turn-On Delay Time	td _(on)		-	3	-	ns
Turn-On Rise Time	tr	V _{DD} =-15V, I _D =-3.2A, V _{GS} =-10V,	-	43	-	
Turn-Off Delay Time	td _(off)		-	223	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	100	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	1				-1.5	^
Diode Forward Current	I _S		-	-	-1.5	A
Diode Forward Voltage	V_{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.77	-1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited





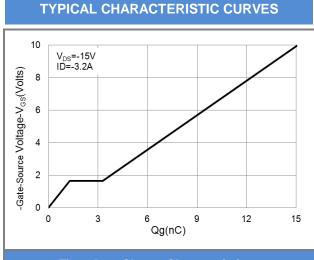


Fig.7 Gate-Charge Characteristics

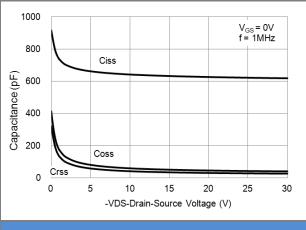
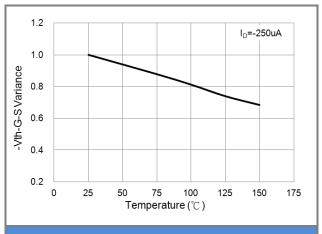


Fig.9 Capacitance vs. Drain-Source Voltage.





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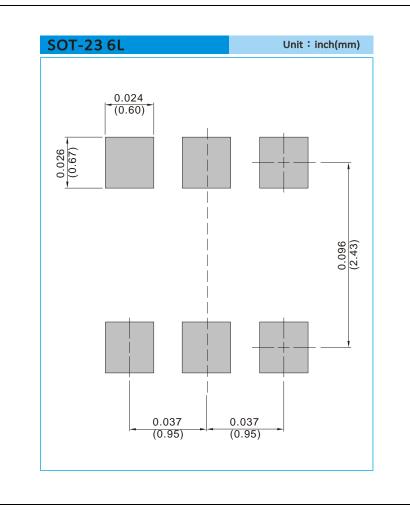




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJS6801_S1_00001	SOT-23 6L	3K pcs / 7" reel	ST1	Halogen free
PJS6801_S2_00001	SOT-23 6L	10K pcs / 13" reel	ST1	Halogen free

MOUNTING PAD LAYOUT







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