



30V P-Channel Enhancement Mode MOSFET

Voltage

-30 V

Current

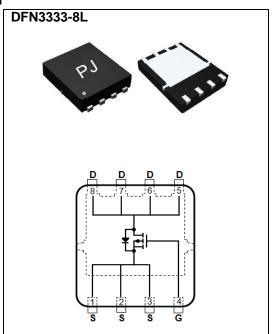
-5 A

Features

- R_{DS(ON)}, V_{GS}@-10V, I_D@-3A<50mΩ
- $R_{DS(ON)}$, $V_{GS}@-4.5V$, $I_{D}@-2A<80m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: DFN3333-8L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.001 ounces, 0.03 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	-30		
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _A =25°C	l _D	-5.0	А	
	T _A =70°C		-4.0		
Pulsed Drain Current (Note 1)		I _{DM}	-20		
Power Dissipation	T _A =25°C	_	2.0	W	
Power Dissipation	T _A =70°C	Pb	1.3		
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance Junction to Ambient (Note 5)		$R_{\theta JA}$	62.5	°C/W	

• Limited only By Maximum Junction Temperature





Electrical Characteristics (T_A=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250uA$	-1	-1.6	-2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V_{GS} =-10V, I_D =-3A	-	40	50	mΩ
		V_{GS} =-4.5V, I_{D} =-2A	-	60	80	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-30V, V_{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Q_g	V _{DS} =-15V, I _D =-3A, V _{GS} =-4.5V ^(Note 1,2)	-	4.8	-	nC
Gate-Source Charge	Q_gs		-	1.7	-	
Gate-Drain Charge	Q_gd		-	1.7	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V,	-	516	-	pF
Output Capacitance	Coss		-	83	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	61	-	
Turn-On Delay Time	td _(on)	\/ 45\/ 40	-	5.6	-	
Turn-On Rise Time	t _r	V_{DS} =-15V, I_{D} =-1A, V_{GEN} =-10V, R_{G} =6 Ω (Note 1,2)	-	8.5	-	ns
Turn-Off Delay Time	td _(off)		-	27	-	
Turn-Off Fall Time	t _f		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	ı		-	-	-5	А
Diode Forward Current	I _S					
Diode Forward Voltage	V_{SD}	I _S =-1A, V _{GS} =0V	-	-0.76	-1	V

NOTES:

- 1. Pulse width<300us, Duty cycle<2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}$ =150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited
- 5. R_{OJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper
- 6. Guaranteed by design, not subject to production testing.





TYPICAL CHARACTERISTIC CURVES

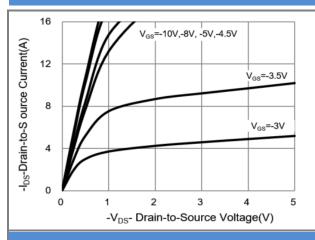


Fig.1 On-Region Characteristics

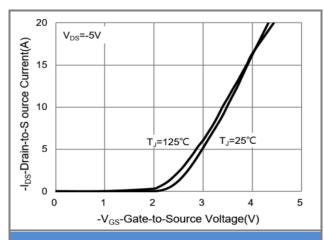


Fig.2 Transfer Characteristics

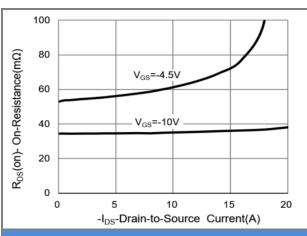


Fig.3 On-Resistance vs. Drain Current

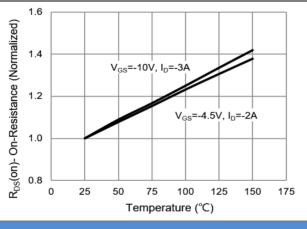


Fig.4 On-Resistance vs. Junction temperature

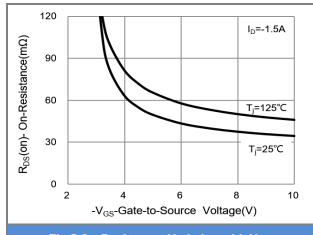


Fig.5 On-Resistance Variation with V_{GS}

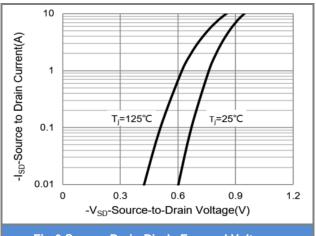


Fig.6 Source-Drain Diode Forward Voltage





TYPICAL CHARACTERISTIC CURVES

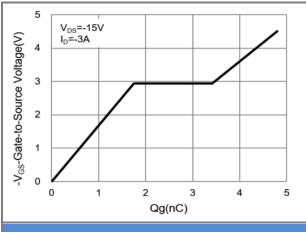


Fig.7 Gate-Charge Characteristics

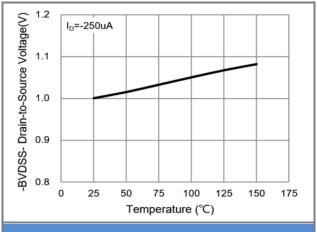


Fig.8 Breakdown Voltage Variation vs. Temperature

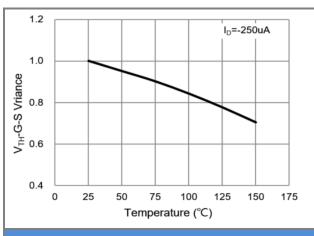


Fig.9 Threshold Voltage Variation with Temperature

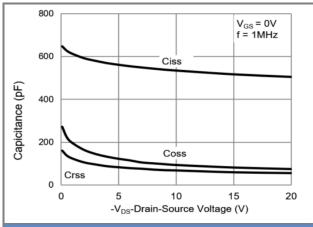


Fig.10 Capacitance vs. Drain-Source Voltage

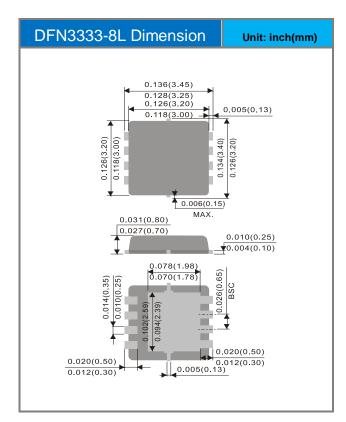


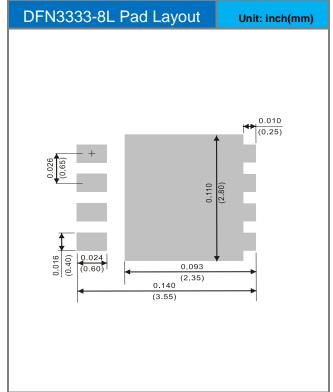


Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJQ4413P_R2_00001	DFN3333-8L	5K pcs / 13" reel	4413	Halogen free

Packaging Information & Mounting Pad Layout









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