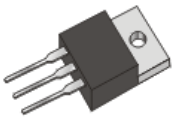
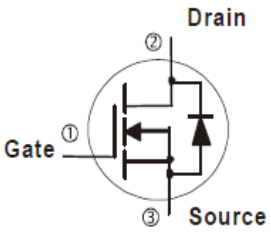
PJP70N10L

100V N-Channel Enhancement Mode MOSFET

Voltage	100 V	Current	70 A



TO-220AB



Features

- $R_{DS(ON)}$, V_{GS}@10V,I_D@20A<12mΩ
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std. (Halogen Free)

Mechanical Data

- Case : TO-220AB Package
- Terminals : Solderable per MIL-STD-750, Method 2026

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±25	V
Continuous Drain Current	T_C=25°C	I_D	70	A
	T_C=100°C		50	
Pulsed Drain Current (Note 1)	T_C=25°C	I_{DM}	280	
Power Dissipation	T_C=25°C	P_D	166	W
	T_C=100°C		83	
Single Pulse Avalanche Energy (Note 5)		E_{AS}	225	mJ
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~175	°C
Typical Thermal Resistance - Junction to Case - Junction to Ambient		 $R_{\theta JC}$ $R_{\theta JA}$	 0.9 62.5	°C/W

- Limited only By Maximum Junction Temperature

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V,I_D=250uA	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS},I_D=250uA	2	2.94	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=20A	-	9.4	12	mΩ
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=80V,V_{GS}=0V	-	-	1.0	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±25V,V_{DS}=0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	V_{DS}=50V, I_D=30A, V_{GS}=10V (Note 1,2)	-	69	-	nC
Gate-Source Charge	Q_{gs}		-	9.8	-	
Gate-Drain Charge	Q_{gd}		-	27	-	
Input Capacitance	Ciss	V_{DS}=25V, V_{GS}=0V, f=1.0MHZ	-	3061	-	pF
Output Capacitance	Coss		-	366	-	
Reverse Transfer Capacitance	Crss		-	187	-	
Turn-On Delay Time	$td_{(on)}$	V_{DD}=50V, I_D=30A, V_{GS}=10V, R_G=6.8Ω (Note 1,2)	-	25	-	ns
Turn-On Rise Time	t_r		-	66	-	
Turn-Off Delay Time	$td_{(off)}$		-	76	-	
Turn-Off Fall Time	t_f		-	46	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	70	A
Diode Forward Voltage	V_{SD}	I_S=20A,V_{GS}=0V	-	0.84	1.3	V
Reverse Recovery Time	trr	V_{GS}=0V, I_S=30A dI$_F$/ dt=100A/us (Note 2)	-	46	-	nS
Reverse Recovery Charge	Qrr		-	88	-	nC

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
4. The maximum current rating is package limited.
5. The test condition is L=0.5mH, I_{AS}=30A, V_{DD}=25V, V_{GS}=10V
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

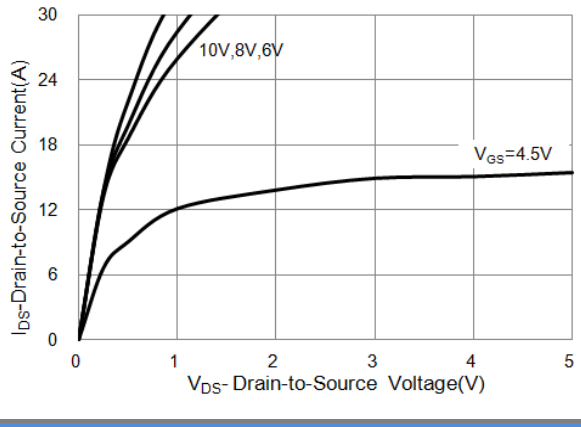

Fig.1 Output Characteristics

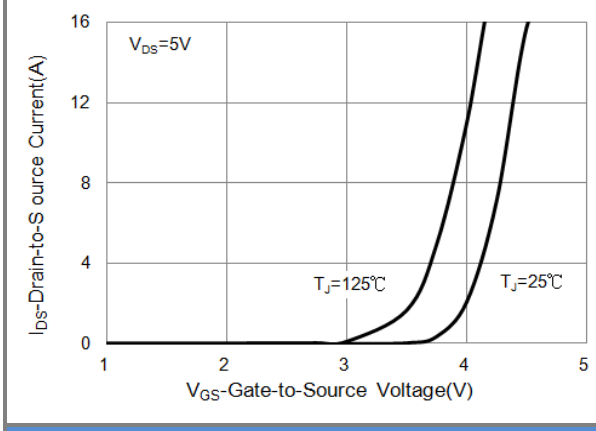

Fig.2 Transfer Characteristics

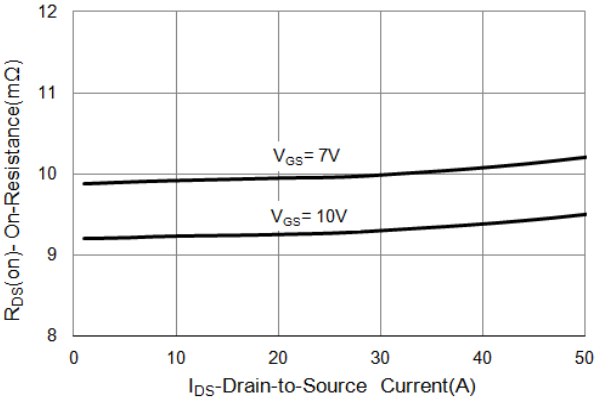

Fig.3 On-Resistance vs. Drain Current

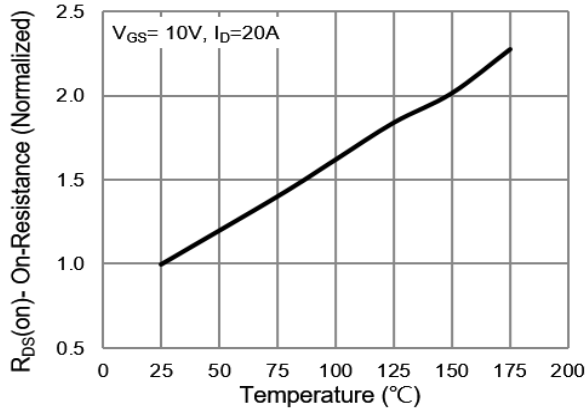

Fig.4 On-Resistsnce vs. Junction temperature

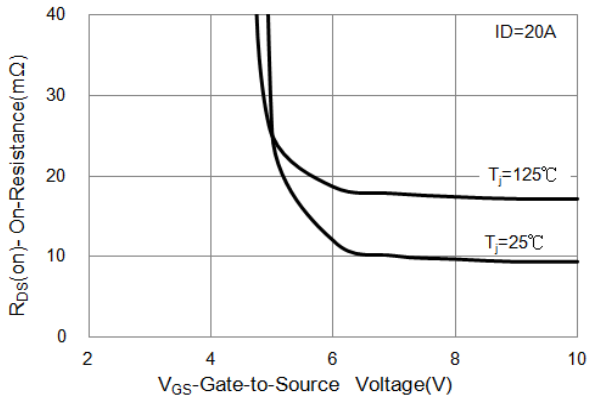

Fig.5 On-Resistance Variation with VGS.

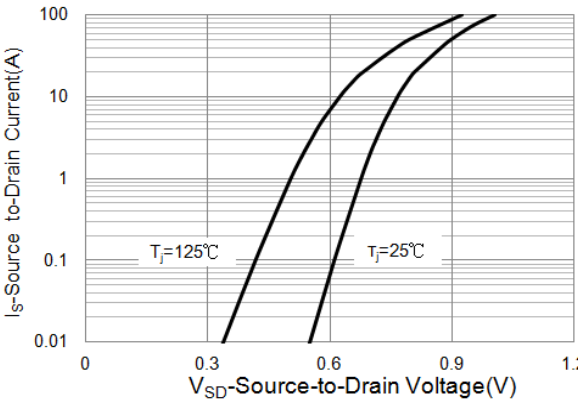

Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

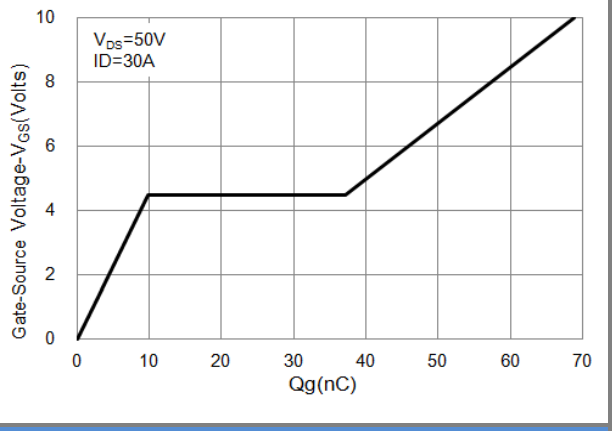

Fig.7 Gate-Charge Characteristics

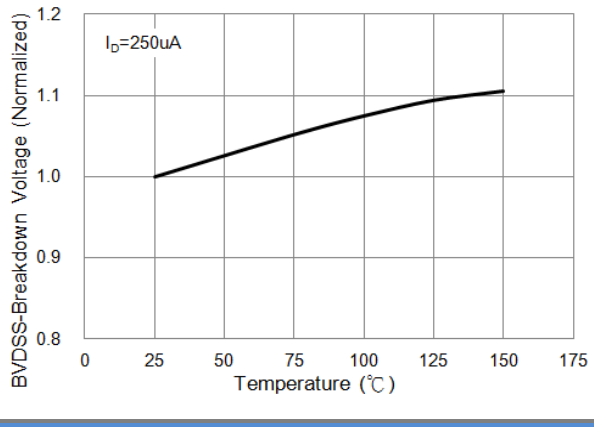

Fig.8 Breakdown Voltage Variation vs. Temperature

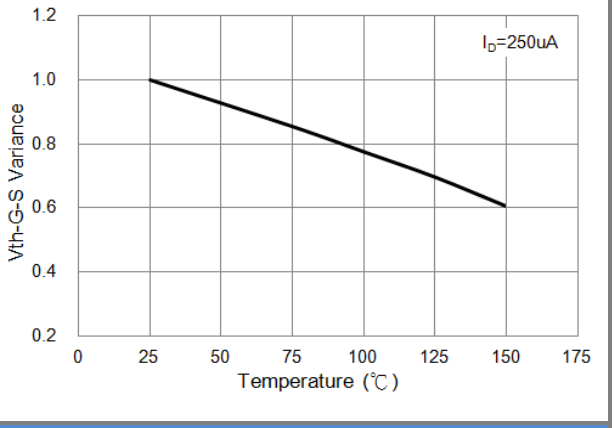

Fig.9 Threshold Voltage Variation with Temperature

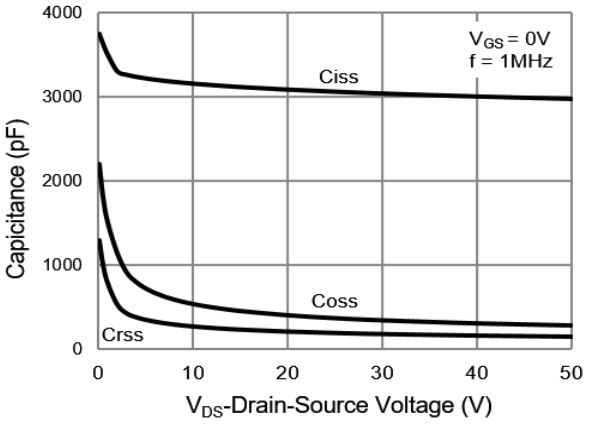

Fig.10 Capacitance vs. Drain-Source Voltage

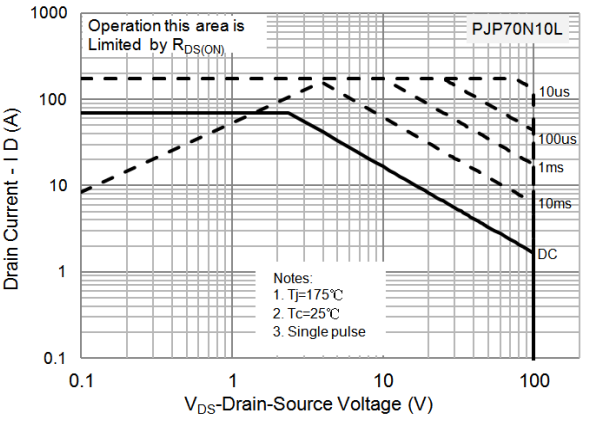

Fig.11 Maximum Safe Operating Area

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TYPICAL CHARACTERISTIC CURVES

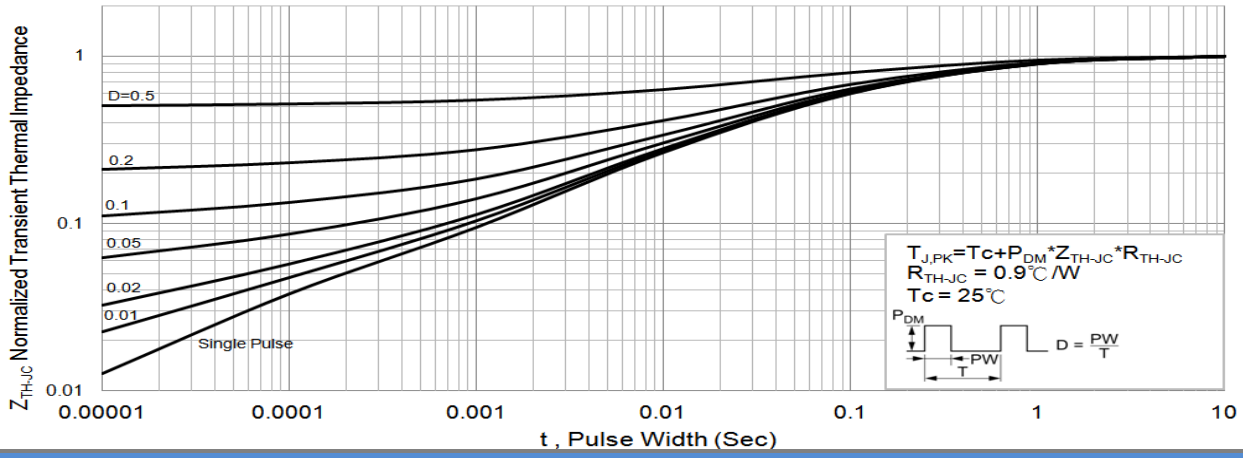

Fig.12 Normalized Transient Thermal Impedance vs. Pulse Width

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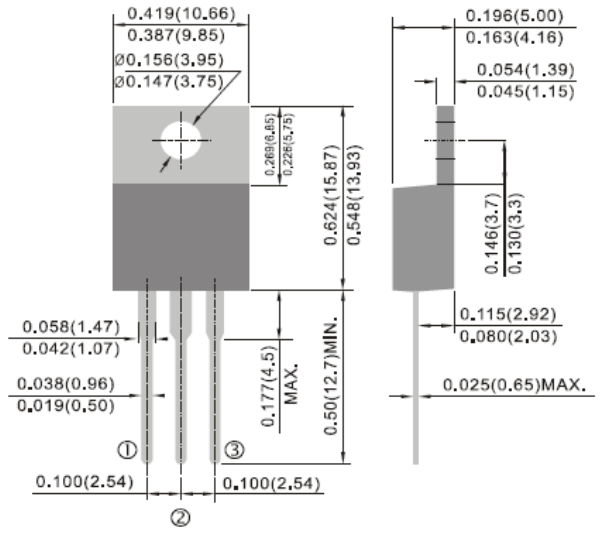
Packaging Information .

TO-220AB Dimension | Unit: inch(mm)



PJP70N10L

PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJP70N10L_T0_00001	TO-220AB	50pcs / Tube	P70N10L	Halogen free

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