



# PJL9835A

## 60V Dual P-Channel Enhancement Mode MOSFET

Voltage      -60 V      Current      -5.5 A

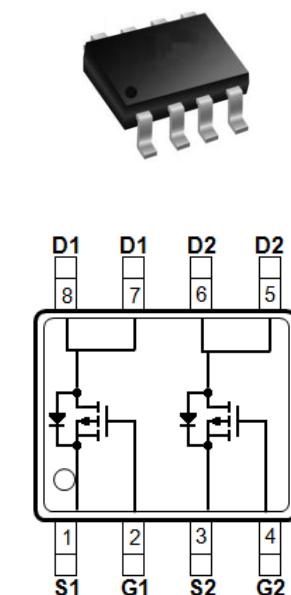
### Features

- $R_{DS(ON)}$ ,  $V_{GS} @ -10V, I_D @ -5.5A < 48m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS} @ -4.5V, I_D @ -3.0A < 65m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Green molding compound as per IEC61249 Std.  
(Halogen Free)

### Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Marking: L9835A

SOP-8



### Maximum Ratings and Thermal Characteristics ( $T_A = 25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <small><math>T_A = 25^\circ C</math></small>	$I_D$	-5.5	A
		-4.4	
Pulsed Drain Current <small>(Note 1)</small>	$I_{DM}$	-22	A
Power Dissipation <small><math>T_A = 25^\circ C</math></small>	$P_D$	2.5	W
		1.6	
Single Pulse Avalanche Energy <small>(Note 5)</small>	$E_{AS}$	24	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Typical Thermal resistance - Junction to Ambient, $t \leq 10s$ <small>(Note 6)</small>	$R_{\theta JA}$	50	°C/W



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## Electrical Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.7	-2.5	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-5.5A$	-	40	48	$m\Omega$
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-3.0A$	-	55	65	$m\Omega$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-60V, V_{GS}=0V$	-	-	-1.0	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 100$	$nA$
<b>Dynamic</b> (Note 7)						
Total Gate Charge	$Q_g$	$V_{DS}=-30V, I_D=-5.5A,$ $V_{GS}=-10V$ (Note 3)	-	22	-	nC
Gate-Source Charge	$Q_{gs}$		-	4.1	-	
Gate-Drain Charge	$Q_{gd}$		-	5.2	-	
Input Capacitance	$C_{iss}$	$V_{DS}=-30V, V_{GS}=0V,$ $f=1.0MHz$	-	1256	-	pF
Output Capacitance	$C_{oss}$		-	87	-	
Reverse Transfer Capacitance	$C_{rss}$		-	59	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-30V, I_D=-1A,$ $V_{GS}=-10V, R_G=6\Omega$ (Note 3)	-	13	-	ns
Turn-On Rise Time	$t_r$		-	42	-	
Turn-Off Delay Time	$t_{d(off)}$		-	65	-	
Turn-Off Fall Time	$t_f$		-	16	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	-	-	-5.5	A
Diode Forward Voltage	$V_{SD}$	$I_s=-1.0A, V_{GS}=0V$	-	-0.72	-1.0	V

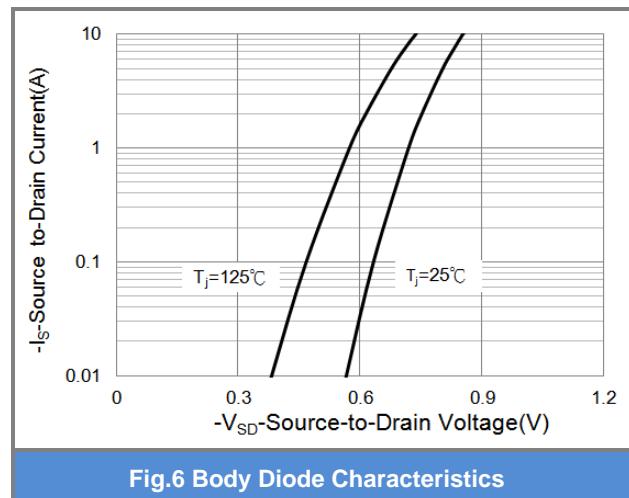
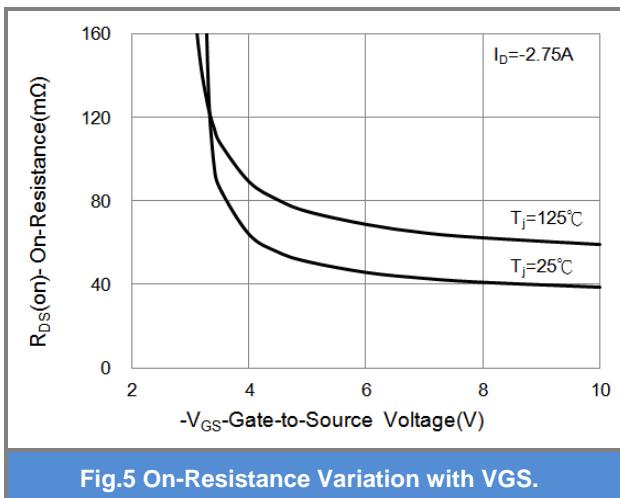
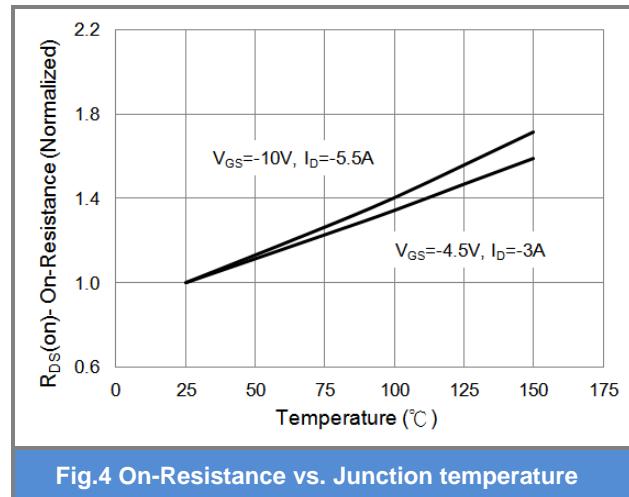
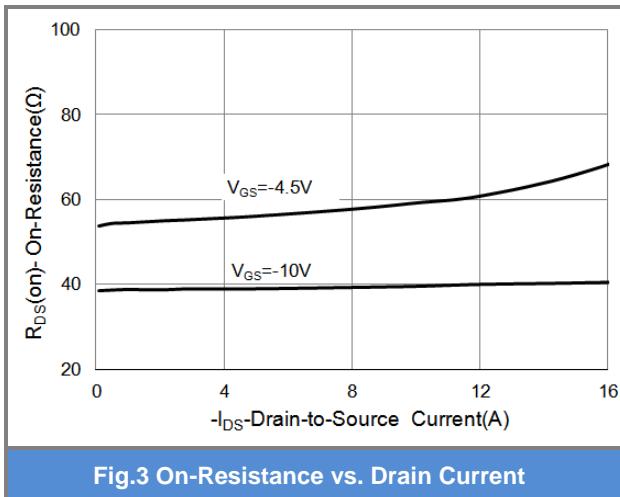
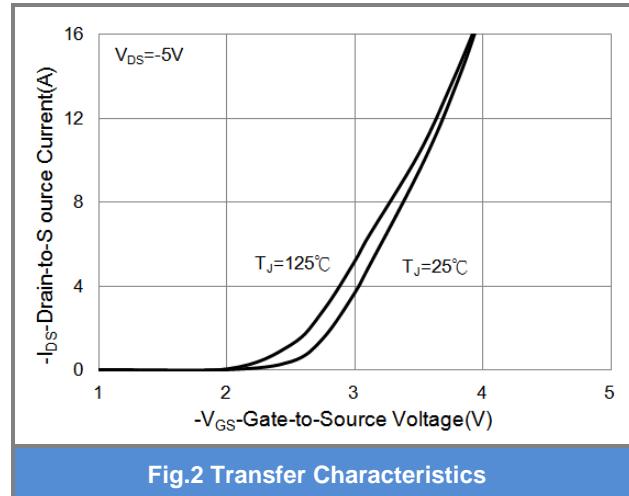
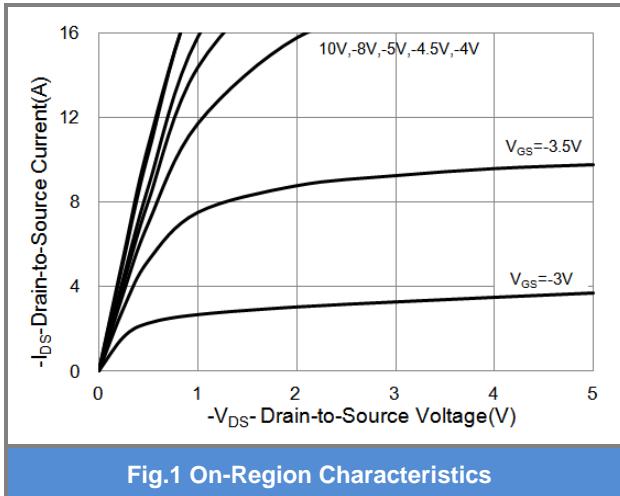
### NOTES :

1. Pulse width<300us, Duty cycle<2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature  $T_J(MAX)=150^\circ C$ . Ratings are based on low frequency and duty cycles to keep initial  $T_J = 25^\circ C$ .
5. The test condition is  $L=0.1mH, I_{AS}=-22A, V_{DD}=-25V, V_{GS}=-10V$
6.  $R_{QJA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
7. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES





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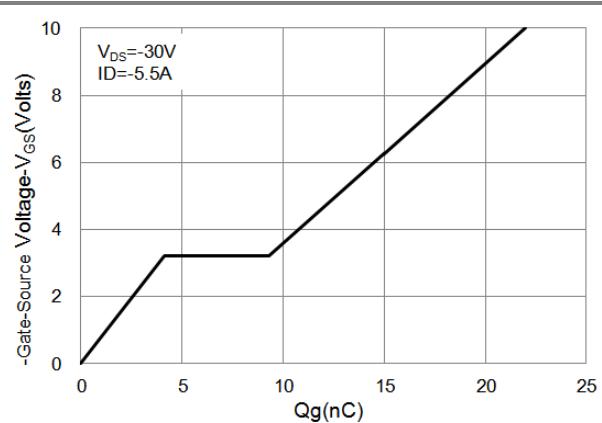


Fig.7 Gate-Charge Characteristics

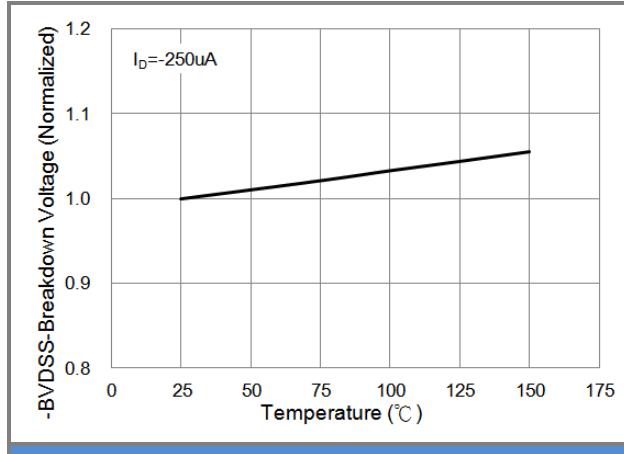


Fig.8 Breakdown Voltage Variation vs. Temperature

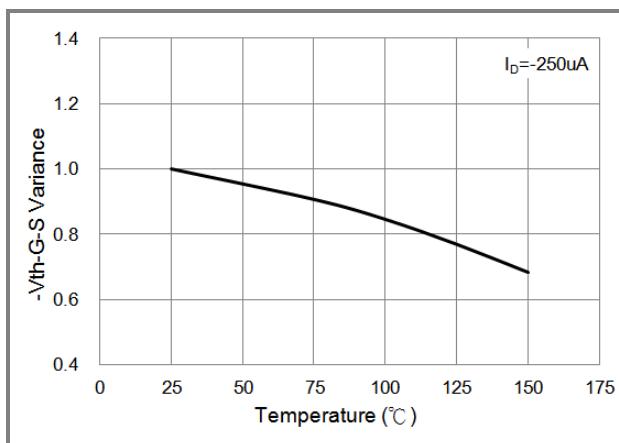


Fig.9 Threshold Voltage Variation with Temperature.

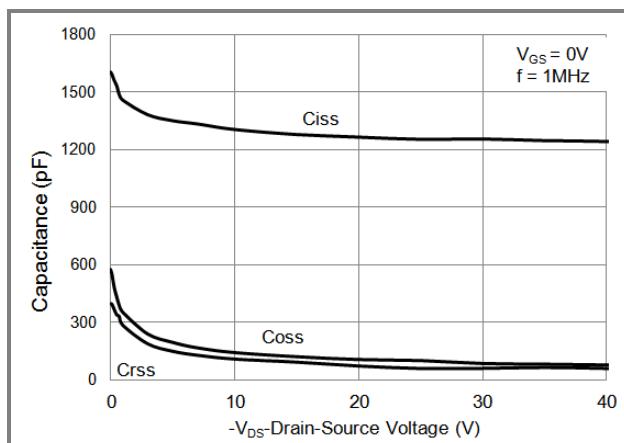


Fig.10 Capacitance vs. Drain-Source Voltage.

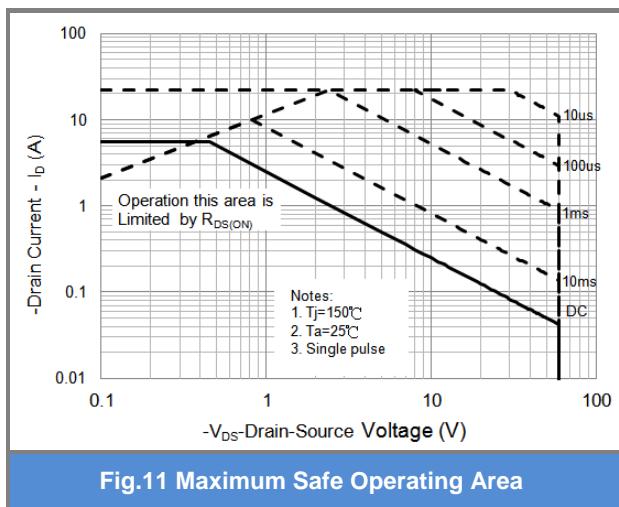
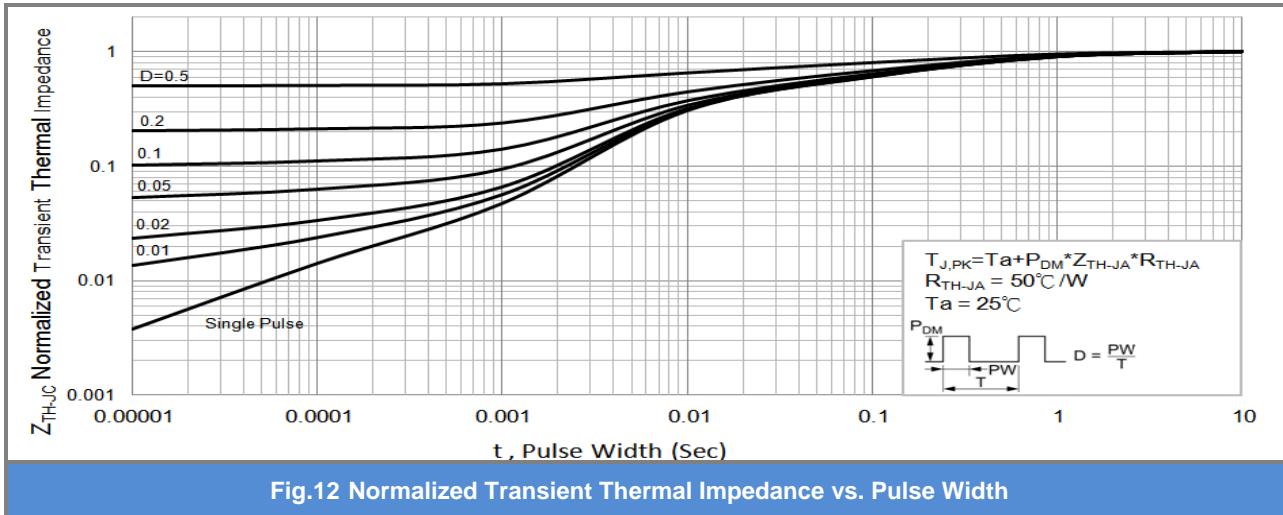


Fig.11 Maximum Safe Operating Area



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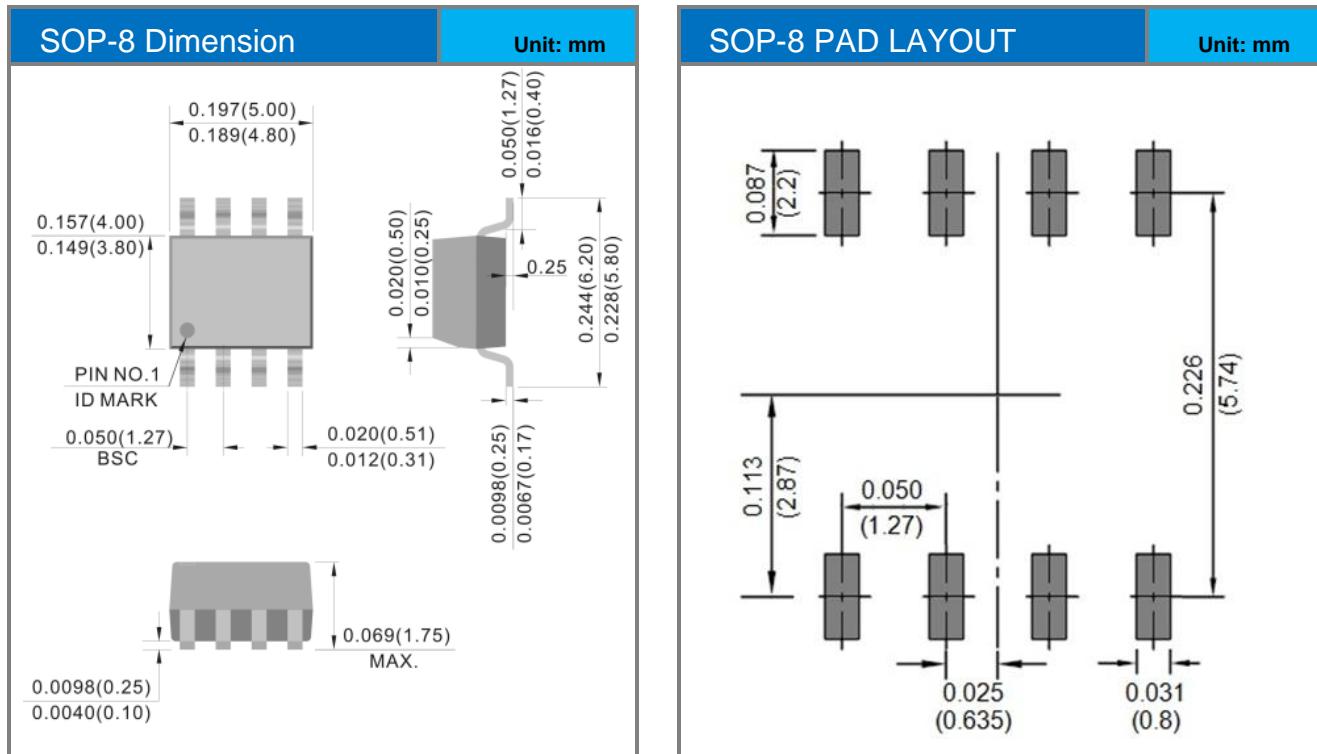


# PJL9835A

## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJL9835A_R2_00001	SOP-8	2.5K pcs / 13" reel	L9835A	Halogen free

## Packaging Information & Mounting Pad Layout





## PJL9835A

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