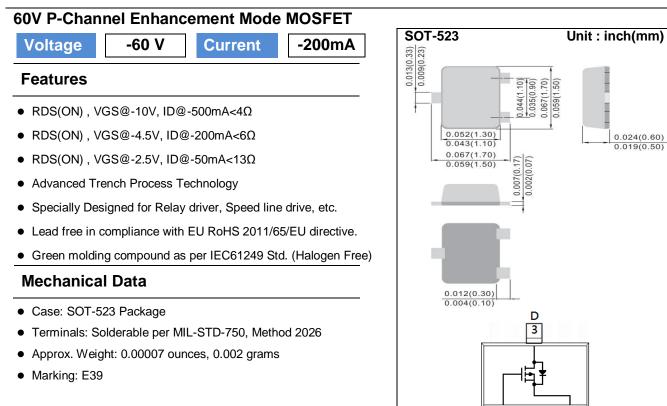
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	SEMI CONDUCTOR



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Voltage		V _{GS}	<u>+</u> 20	V
Continuous Drain Current		I _D	-200	mA
Pulsed Drain Current		I _{DM}	-800	mA
Power Dissipation	T _A =25°C		300	mW
	Derate above 25°C	P _D	2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{θJA}	417	°C/W

1 G



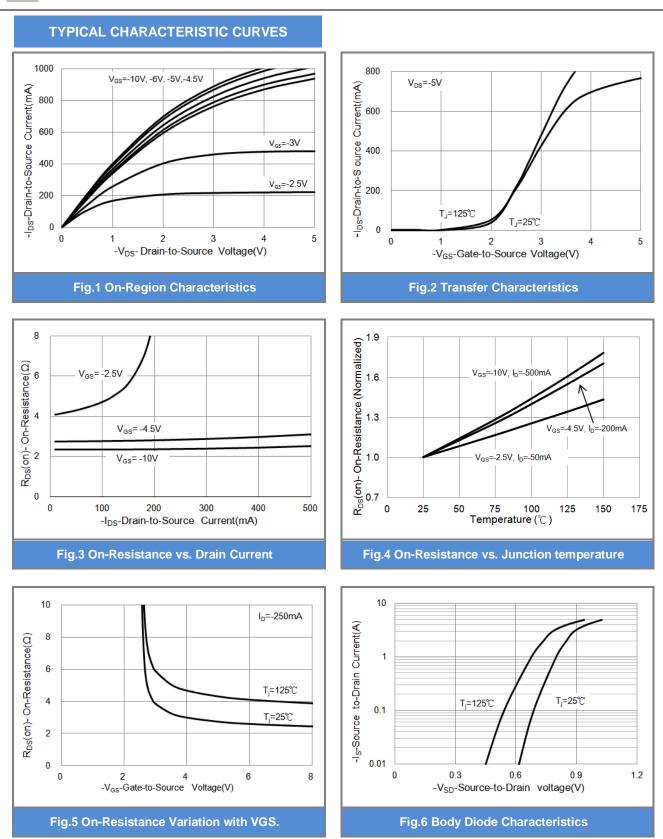
Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

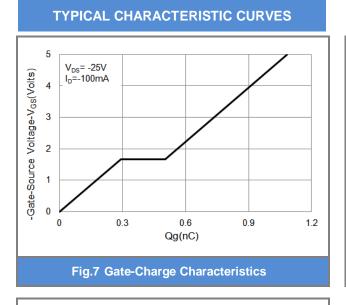
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V,I_{D}=-250uA$	-60	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250$ uA	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V,I _D =-500mA	-	2.4	4	Ω
		V _{GS} =-4.5V,I _D =-200mA	-	2.65	6	
		V _{GS} =-2.5V,I _D =-50mA	-	4.5	13	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V,V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V,V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 4)						
Total Gate Charge	Q_{g}	V _{DS} =-25V, I _D =-100mA, V _{GS} =-4.5V	-	1.1	-	nC
Gate-Source Charge	Q_gs		-	0.3	-	
Gate-Drain Charge	Q_gd		-	0.2	-	
Input Capacitance	Ciss	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	51	-	pF
Output Capacitance	Coss		-	15	-	
Reverse Transfer Capacitance	Crss		-	2.2	-	
Turn-On Delay Time	td _(on)		-	4.8	-	
Turn-On Rise Time	tr	V _{DD} =-25V, I _D =-100mA, V _{GS} =-10V,	-	19	-	
Turn-Off Delay Time	td _(off)		-	52	-	ns
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S		-	-	-200	mA
Diode Forward Voltage	V_{SD}	I _S =-500mA, V _{GS} =0V	-	-0.95	-1.3	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{®JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing







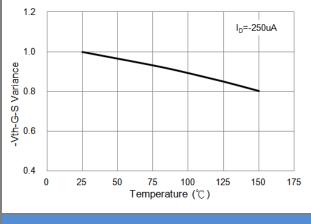
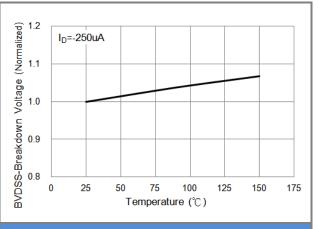


Fig.9 Threshold Voltage Variation with Temperature.





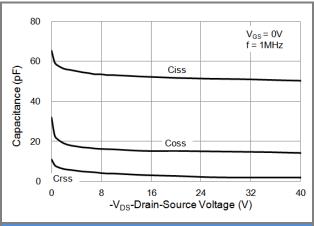


Fig.10 Capacitance vs. Drain-Source Voltage.

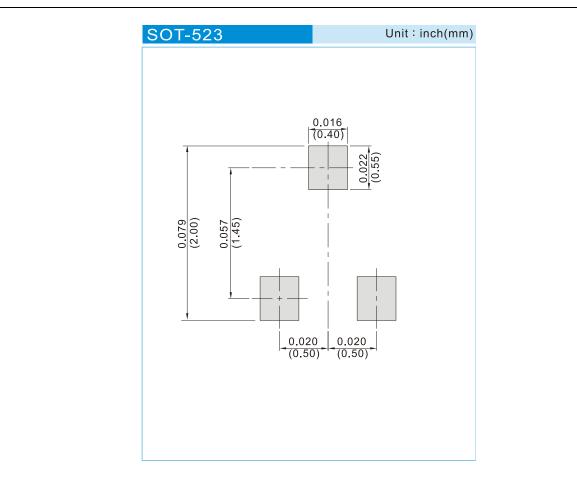




PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJE8439_R1_00001	SOT-523	4K pcs / 7" reel	E39	Halogen free

MOUNTING PAD LAYOUT







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