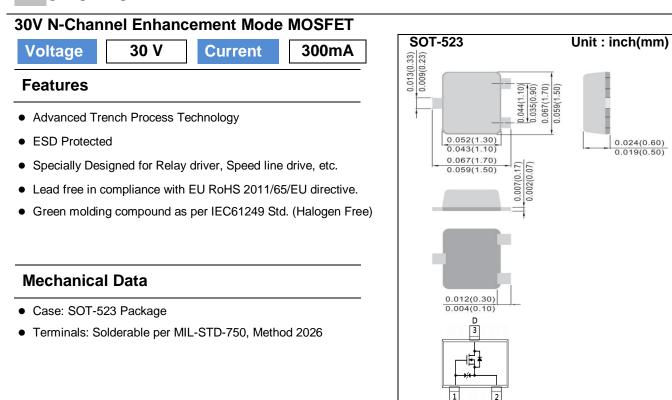
H	JII
	SEMI
	CONDUCTOR





Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	<u>+</u> 10	V
Continuous Drain Current		I _D	300	mA
Pulsed Drain Current		I _{DM}	600	mA
Power Dissipation	T _A =25°C	P _D	300	mW
	Derate above 25°C		2.4	mW/°C
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		R _{eja}	417	°C/W





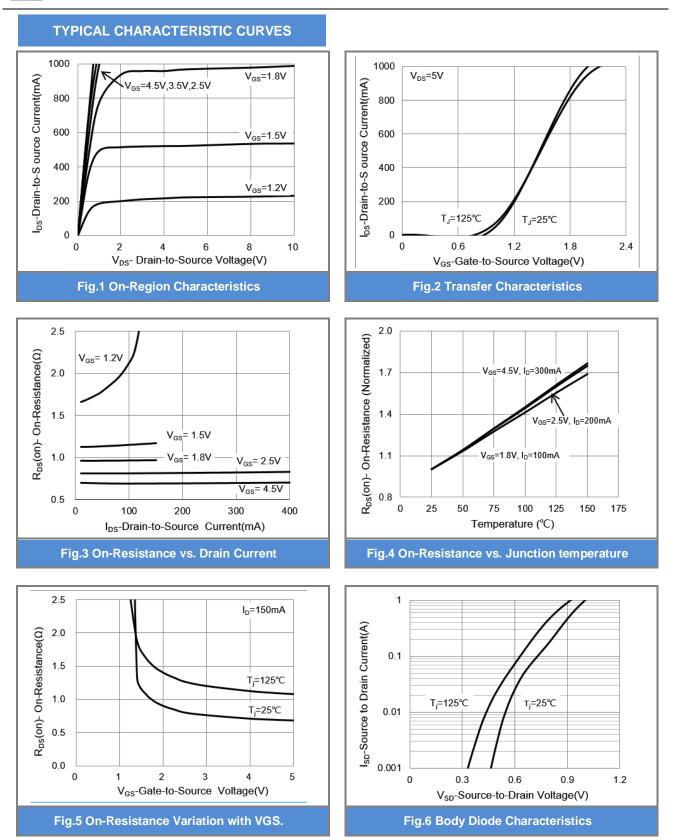
Electrical Characteristics (T_A=25°C unless otherwise noted)

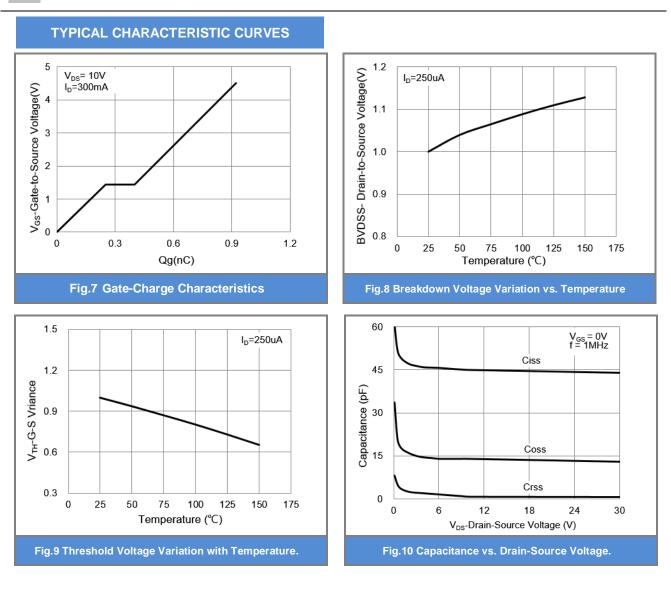
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V,I _D =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.4	0.75	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V,I _D =300mA	-	0.7	1.2	Ω
		V_{GS} =2.5V,I _D =200mA	-	0.8	1.6	
		V _{GS} =1.8V,I _D =100mA	-	0.9	2.0	
		V _{GS} =1.5V,I _D =50mA	-	1.1	3.0	
		V_{GS} =1.2V,I _D =20mA	-	1.5	4.0	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V,V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V,V _{DS} =0V	-	-	<u>+</u> 10	uA
Dynamic (Note 4)						
Total Gate Charge	Qg	V _{DS} =10V, I _D =300mA, V _{GS} =4.5V	-	0.9	-	nC
Gate-Source Charge	Q_gs		-	0.3	-	
Gate-Drain Charge	Q_gd		-	0.2	-	
Input Capacitance	Ciss		-	45	-	
Output Capacitance	Coss	$V_{DS}=10V, V_{GS}=0V,$	-	14	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	0.8	-	
Turn-On Delay Time	td _(on)		-	8.3	-	
Turn-On Rise Time	tr	V_{DD} =10V, I _D =300mA, V _{GS} =4V, R _G =10Ω ^(Note 1,2)	-	5.7	-	ns
Turn-Off Delay Time	td _(off)		-	35	-	
Turn-Off Fall Time	tf	$R_G = 10\Omega$	-	12	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S		-	-	300	mA
Diode Forward Voltage	V_{SD}	I _S =300mA, V _{GS} =0V	-	0.9	1.3	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing.







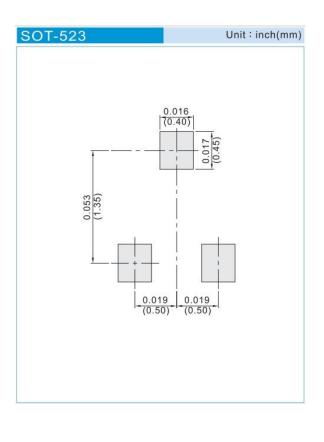




PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJE8428_R1_00001	SOT-523	4K pcs / 7" reel	E28	Halogen free

MOUNTING PAD LAYOUT







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