ΡΛΝ	ĴΪΤ
	SEMI CONDUCTOR

PJD85N03

30V N-Channel Enhancement Mode MOSFET

Voltage

30 V Current

Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@20A<3.8m\Omega$
- R_{DS(ON)}, V_{GS}@4.5V, I_D@15A<5.5mΩ
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

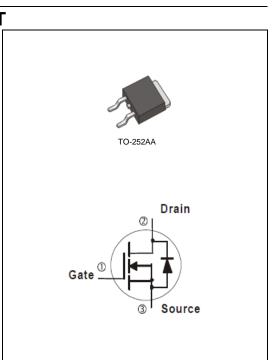
- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams

Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

85 A

PARAMET	ER	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	30	- v	
Gate-Source Voltage		V_{GS}	<u>+</u> 20	V	
Continuous Drain Current	T _C =25°C		85		
	T _c =100°C	I _D	54	А	
Pulsed Drain Current (Note 1)	T _C =25°C	I _{DM}	340		
Power Dissipation	T _C =25°C	PD	58	W	
	T _C =100°C		23		
Continuous Drain Current	T _A =25°C	I _D	16	^	
	T _A =70°C		13	A	
Power Dissipation	T _A =25°C	D	2.0	14/	
Power Dissipation	T _A =70°C	PD	1.3	W	
Single Pulse Avalanche Energy	(Note 6)	E _{AS}	100	mJ	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Typical Thermal Resistance (Note 4,5)	Junction to Case	$R_{\theta JC}$	2.16	°C/W	
	Junction to Ambient	$R_{ extsf{ heta}JA}$	62.5		

• Limited only By Maximum Junction Temperature





Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

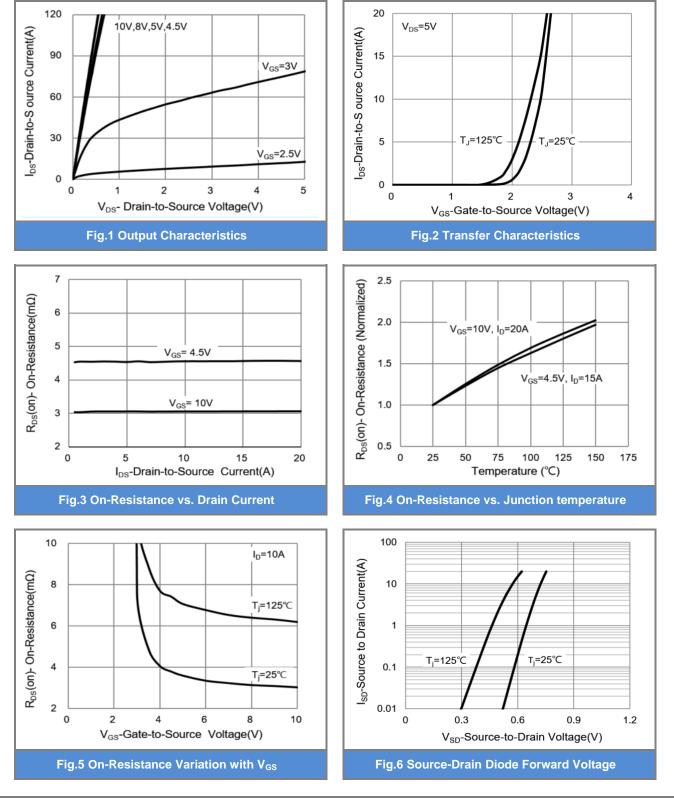
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	N
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	1	1.6	2.5	V
Drain-Source On-State Resistance		V _{GS} =10V, I _D =20A	-	3	3.8	mΩ
	R _{DS(on)}	V _{GS} =4.5V, I _D =15A	-	4.5	5.5	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 7)						
Total Gate Charge	Qg	V _{DS} =15V, I _D =24A, V _{GS} =4.5V ^(Note 2,3)	-	23	-	nC
Gate-Source Charge	Q _{gs}		-	8	-	
Gate-Drain Charge	Q _{gd}		-	9	-	
Input Capacitance	Ciss	V _{DS} =25V, V _{GS} =0V, f=1.0MHZ	-	2436	-	pF
Output Capacitance	Coss		-	306	-	
Reverse Transfer Capacitance	Crss		-	196	-	
Turn-On Delay Time	td _(on)	V _{DS} =15V, I _D =15A, V _{GS} =10V, R _G =1Ω (Note 2.3)	-	32	-	
Turn-On Rise Time	tr		-	169	-	
Turn-Off Delay Time	td _(off)		-	232	-	ns
Turn-Off Fall Time	t _f	(-	170	-	
Drain-Source Diode						·
Maximum Continuous Drain-Source				-	85	А
Diode Forward Current	I _S		-			
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.66	1	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J =25°C.
- 4. The maximum current rating is package limited.
- 5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 6. The test condition is L=0.1mH, $I_{AS}{=}45A,\,V_{DD}{=}25V,\,V_{GS}{=}10V$
- 7. Guaranteed by design, not subject to production testing.

November 18,2016-REV.00



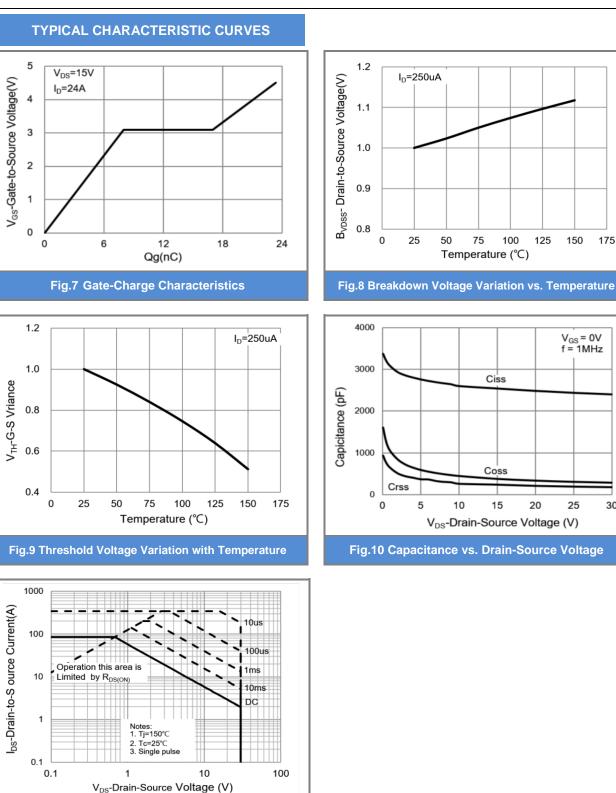


PJD85N03

TYPICAL CHARACTERISTIC CURVES



Fig.11 Maximum Safe Operating Area



PJD85N03



125

150

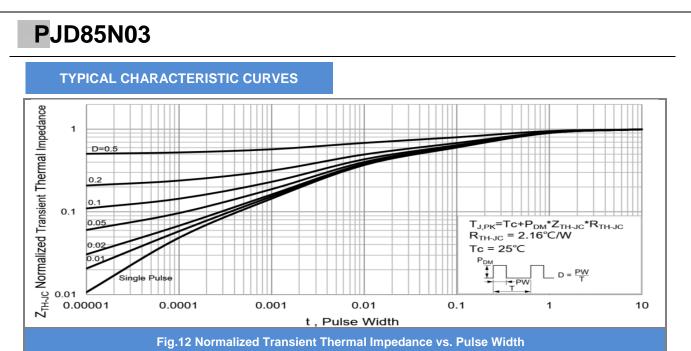
V_{GS} = 0V f = 1MHz

25

30

175

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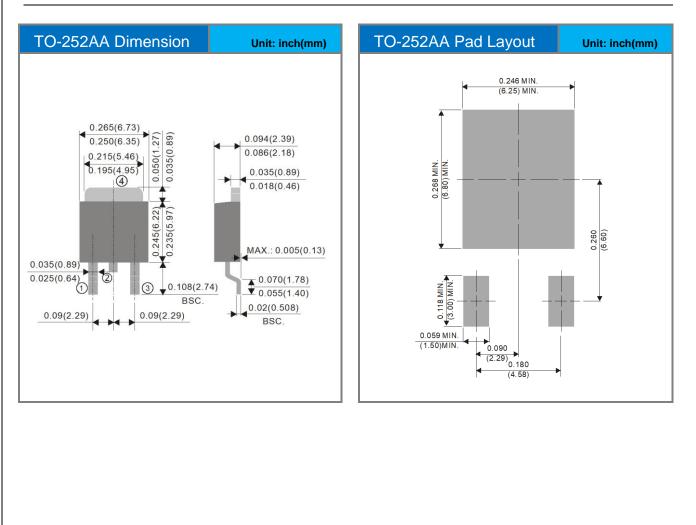


PJD85N03

Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJD85N03_L2_00001	TO-252AA	3,000pcs / 13" reel	D85N03	Halogen free

Packaging Information & Mounting Pad Layout





PJD85N03

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