



# PJA3457

## 20V P-Channel Enhancement Mode MOSFET

**Voltage**

**-20 V**

**Current**

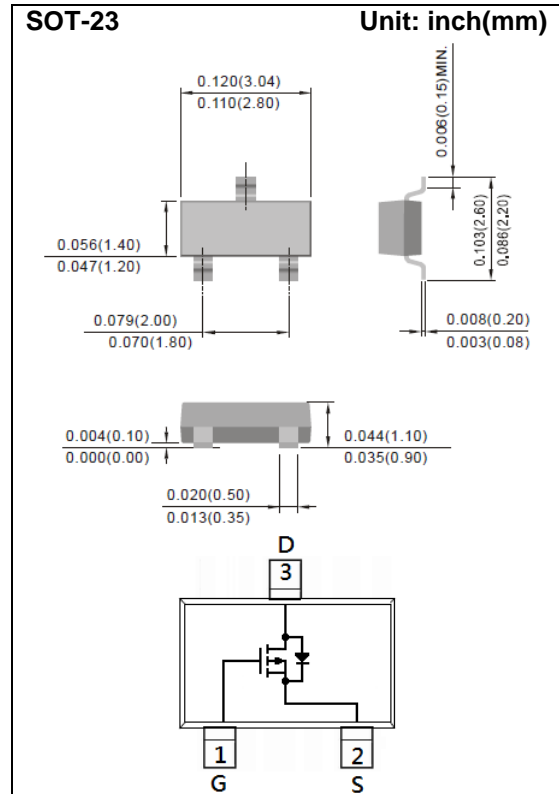
**-5.2 A**

### Features

- RDS(ON) , V<sub>GS</sub>@-4.5V, I<sub>D</sub>@-4.0A<33mΩ
- RDS(ON) , V<sub>GS</sub>@-2.5V, I<sub>D</sub>@-3.0A<40mΩ
- RDS(ON) , V<sub>GS</sub>@-1.8V, I<sub>D</sub>@-2.0A<52mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc.
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive).
- Green molding compound as per IEC61249 Std.. (Halogen Free)

### Mechanical Data

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A57



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage		V <sub>GS</sub>	±10	V
Continuous Drain Current		I <sub>D</sub>	-5.2	A
Pulsed Drain Current		I <sub>DM</sub>	-20.8	A
Power Dissipation	T <sub>a</sub> =25°C	P <sub>D</sub>	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance		R <sub>θJA</sub>	100	°C/W
- Junction to Ambient <sup>(Note 3)</sup>				



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## Electrical Characteristics ( $T_A=25^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.3	-0.5	-1	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=-4.5V, I_D=-4.0A$	-	27	33	mΩ
		$V_{GS}=-2.5V, I_D=-3.0A$	-	33	40	
		$V_{GS}=-1.8V, I_D=-2.0A$	-	41	52	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-20V, V_{GS}=0V$	-	-	-1	μA
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 10V, V_{DS}=0V$	-	-	±100	nA
<b>Dynamic</b> (Note 5)						
Total Gate Charge	$Q_g$	$V_{DS}=-10V, I_D=-1.0A,$ $V_{GS}=-4.5V$ (Note 1,2)	-	14	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.5	-	
Gate-Drain Charge	$Q_{gd}$		-	2.9	-	
Input Capacitance	$C_{iss}$	$V_{DS}=-15V, V_{GS}=0V,$ $f=1.0\text{MHZ}$	-	1237	-	pF
Output Capacitance	$C_{oss}$		-	155	-	
Reverse Transfer Capacitance	$C_{rss}$		-	133	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-10V, I_D=-1.0A,$ $V_{GS}=-4.5V,$ $R_G=25\Omega$ (Note 1,2)	-	8.1	-	ns
Turn-On Rise Time	$t_r$		-	32	-	
Turn-Off Delay Time	$t_{d(off)}$		-	207	-	
Turn-Off Fall Time	$t_f$		-	114	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$	---	-	-	-5.2	A
Diode Forward Voltage	$V_{SD}$	$I_S=-1.0A, V_{GS}=0V$	-	-0.75	-1.2	V

**NOTES :**

1. Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3.  $R_{\theta JA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
4. The maximum current rating is package limited
5. Guaranteed by design, not subject to production testing



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## TYPICAL CHARACTERISTIC CURVES

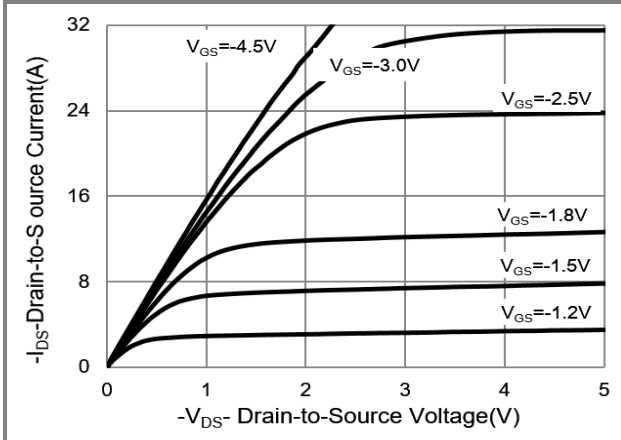


Fig.1 On-Region Characteristics

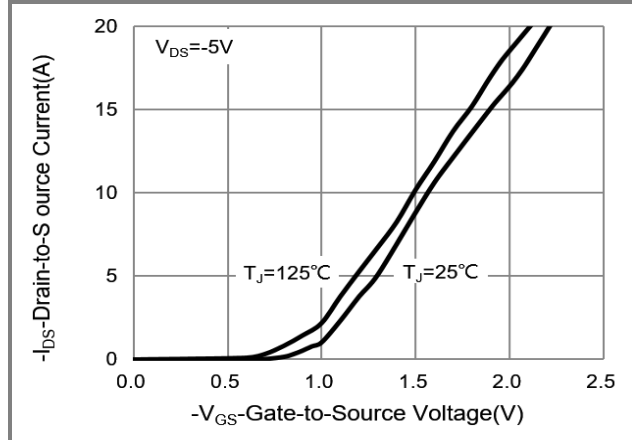


Fig.2 Transfer Characteristics

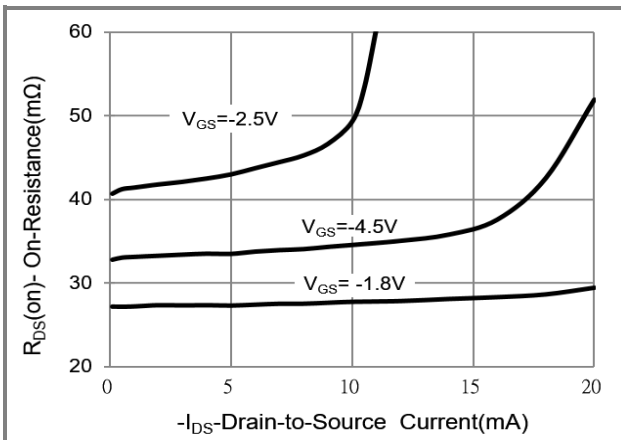


Fig.3 On-Resistance vs. Drain Current

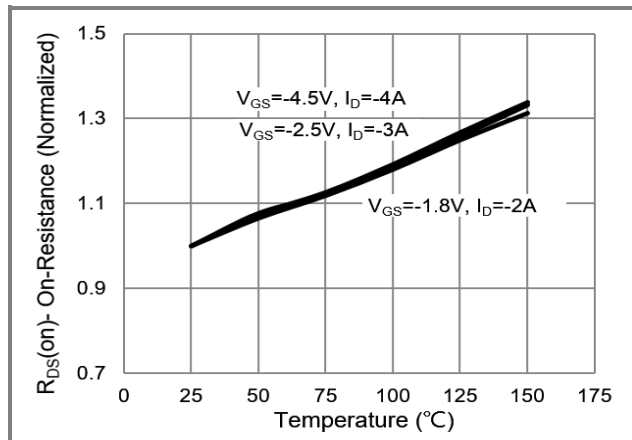


Fig.4 On-Resistance vs. Junction temperature

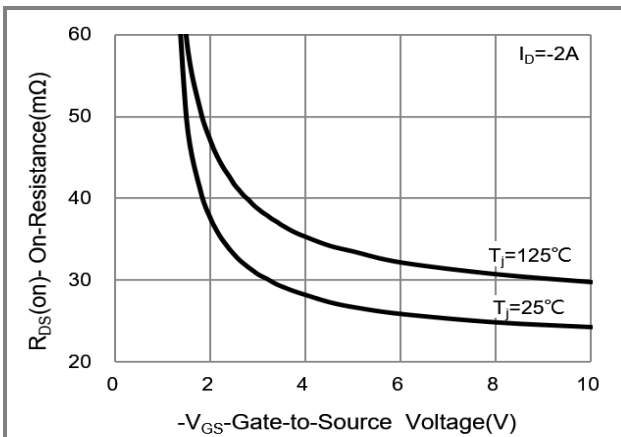


Fig.5 On-Resistance Variation with VGS.

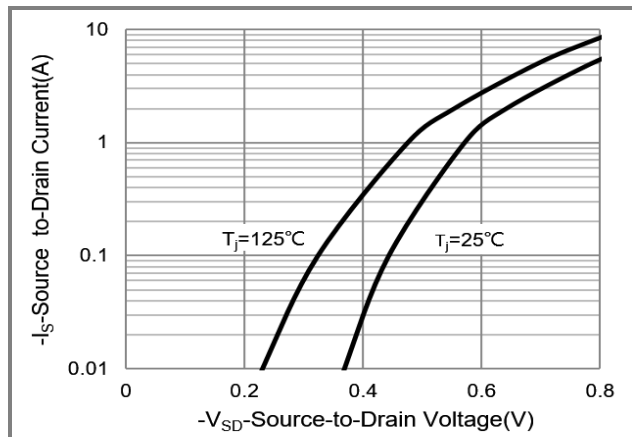


Fig.6 Body Diode Characteristics



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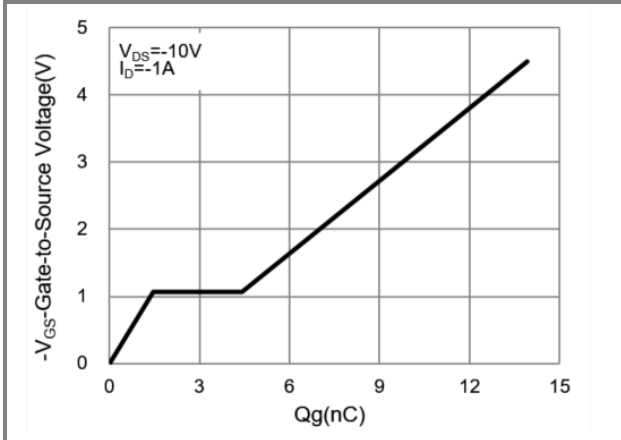


Fig.7 Gate-Charge Characteristics

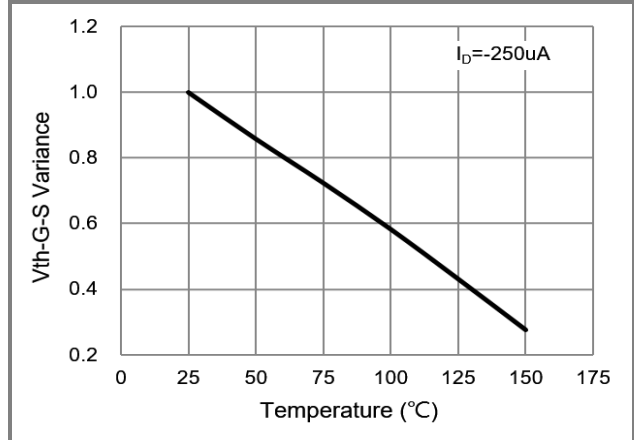


Fig.8 Threshold Voltage Variation with Temperature.

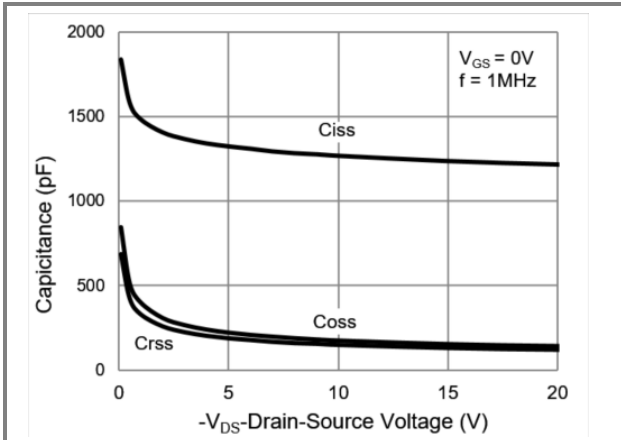


Fig.9 Capacitance vs. Drain-Source Voltage.

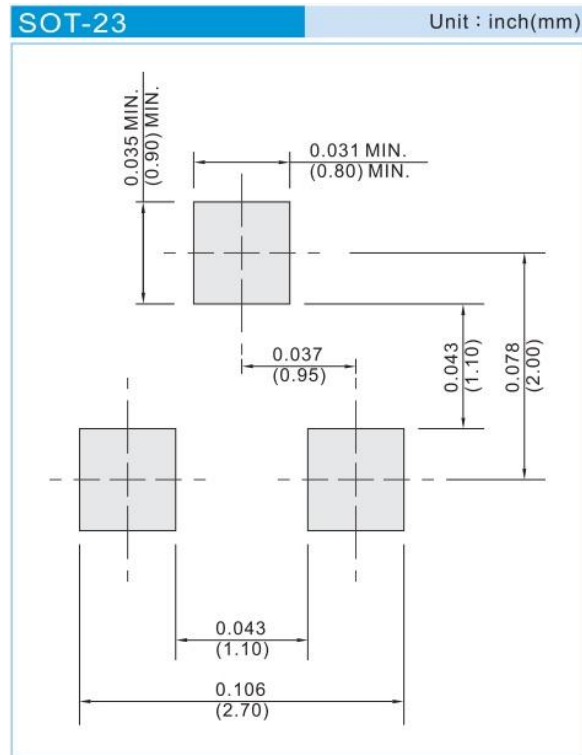


# PJA3457

## PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3457_R1_00001	SOT-23	3K pcs / 7" reel	A57	Halogen free

## MOUNTING PAD LAYOUT





## PJA3457

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