



#### **60V N-Channel Enhancement Mode MOSFET**

Voltage 60 V Current 300mA

#### **Features**

- RDS(ON), VGS@10V, ID@600mA<3Ω
- RDS(ON) , VGS@4.5V, ID@200mA<4Ω</li>
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

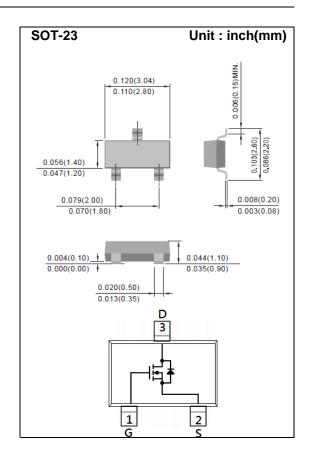
#### **Mechanical Data**

Case: SOT-23 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.0003 ounces, 0.0084 grams

Marking: A2B



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	60	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 30	V
Continuous Drain Current		I <sub>D</sub>	300	mA
Pulsed Drain Current		I <sub>DM</sub>	1200	mA
Power Dissipation	T <sub>A</sub> =25°C	$P_{D}$	500	mW
	Derate above 25°C		4	mW/°C
Operating Junction and Storage Temperature Range		$T_{J}, T_{STG}$	-55~150	°C
Typical Thermal resistance				
- Junction to Ambient (Note 3)		$R_{\theta JA}$	250	°C/W





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static (Note 1)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	60	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250uA$	1.0	1.8	2.5	V	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =600mA	-	1.3	3	Ω	
		V <sub>GS</sub> =4.5V,I <sub>D</sub> =200mA	-	1.7	4		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	uA	
Gate-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}=\underline{+}30V, V_{DS}=0V$	-	-	<u>+</u> 100	nA	
Dynamic (Note 4)							
Total Gate Charge	$Q_g$	V <sub>DS</sub> =15V, I <sub>D</sub> =600mA, V <sub>GS</sub> =4.5V	-	0.82	-	nC	
Gate-Source Charge	$Q_gs$		-	0.53	-		
Gate-Drain Charge	$Q_gd$		-	0.22	-		
Input Capacitance	Ciss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	34	-	pF	
Output Capacitance	Coss		-	11	-		
Reverse Transfer Capacitance	Crss		-	3.0	-		
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}$ =10V, $I_{D}$ =600mA, $V_{GS}$ =10V, $R_{G}$ =6 $\Omega$ (Note 1,2)	-	2.7	-		
Turn-On Rise Time	tr		-	21	-		
Turn-Off Delay Time	td <sub>(off)</sub>		-	3.8	-		
Turn-Off Fall Time	tf		-	18	-		
Drain-Source Diode							
Maximum Continuous Drain-Source				-	300	mA	
Diode Forward Current	I <sub>S</sub>		_				
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =300mA, V <sub>GS</sub> =0V	-	0.9	1.5	V	

#### NOTES:

- 1. Pulse width<a></a>300us, Duty cycle<a></a>2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Rejah is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
- 4. Guaranteed by design, not subject to production testing





#### **TYPICAL CHARACTERISTIC CURVES**

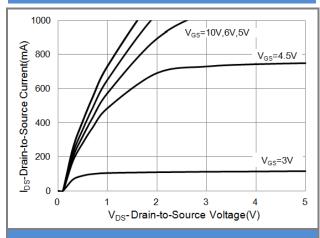
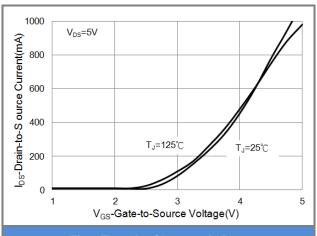


Fig.1 On-Region Characteristics



**Fig.2 Transfer Characteristics** 

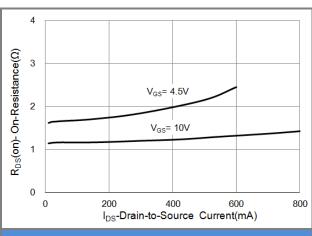


Fig.3 On-Resistance vs. Drain Current

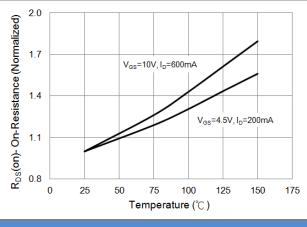


Fig.4 On-Resistance vs. Junction temperature

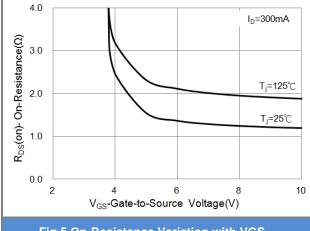


Fig.5 On-Resistance Variation with VGS.

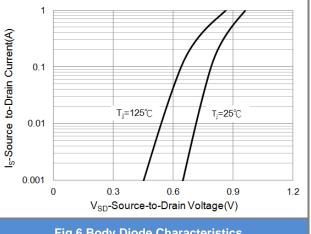
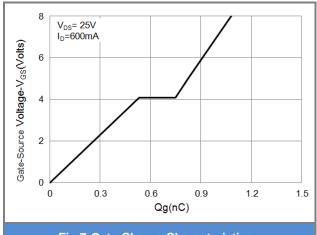


Fig.6 Body Diode Characteristics





#### **TYPICAL CHARACTERISTIC CURVES**



**Fig.7 Gate-Charge Characteristics** 

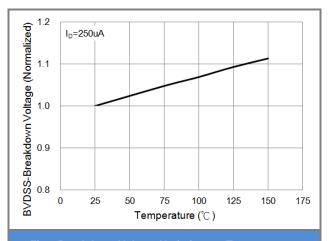


Fig.8 Breakdown Voltage Variation vs. Temperature

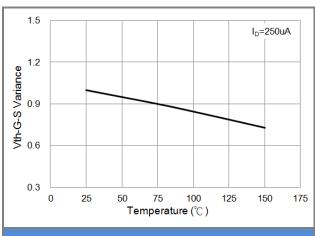


Fig.9 Threshold Voltage Variation with Temperature.

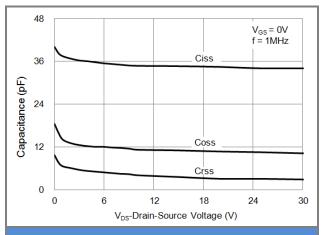


Fig.10 Capacitance vs. Drain-Source Voltage.

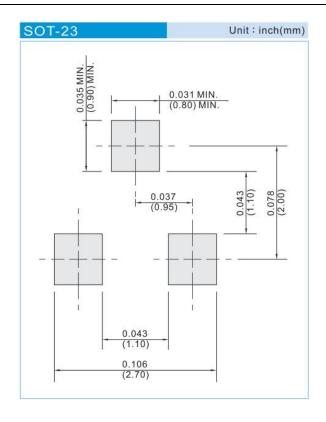




#### PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJA3472B_R1_00001	SOT-23	3K pcs / 7" reel	A2B	Halogen free
PJA3472B_R2_00001	SOT-23	12K pcs / 13" reel	A2B	Halogen free

#### **MOUNTING PAD LAYOUT**







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