20V N-Channel Enhancement Mode MOSFET – ESD Protected SOT-23 20 V Current 6.5A Voltage Features RDS(ON), VGS@4.5V, ID@6.5A<22mΩ RDS(ON), VGS@2.5V, ID@5.5A<26mΩ 0.056(1.40) RDS(ON), VGS@1.8V, ID@5.0A<34mΩ • 0.047(1.20) • Advanced Trench Process Technology 0.079(2.00) Specially Designed for Switch Load, PWM Application, etc. • 0.070(1.80) ESD Protected 2KV HBM Lead free in compliance with EU RoHS 2011/65/EU directive 0.004(0.10) 0.000(0.00) • Green molding compound as per IEC61249 Std. (Halogen Free) **Mechanical Data**

- Case: SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.0084 grams
- Marking: A6E

Maximum Ratings and Thermal Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V _{DS}	20	V
Gate-Source Voltage		V _{GS}	<u>+</u> 8	V
Continuous Drain Current		I _D	6.5	А
Pulsed Drain Current (Note 4)		I _{DM}	32	А
Power Dissipation	T _a =25°C	P _D	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	°C
Typical Thermal resistance - Junction to Ambient ^(Note 3)		R _{θJA}	100	°C/W



Unit: inch(mm)

0.006(0.15)MIN

0.103(2.60) 0.086(2.20)

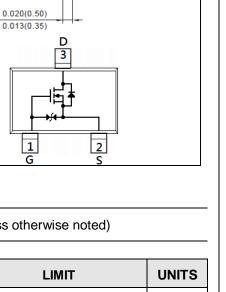
0.008(0.20)

0.003(0.08)

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0.120(3.04)

0.110(2.80)



0.044(1.10)

0.035(0.90)

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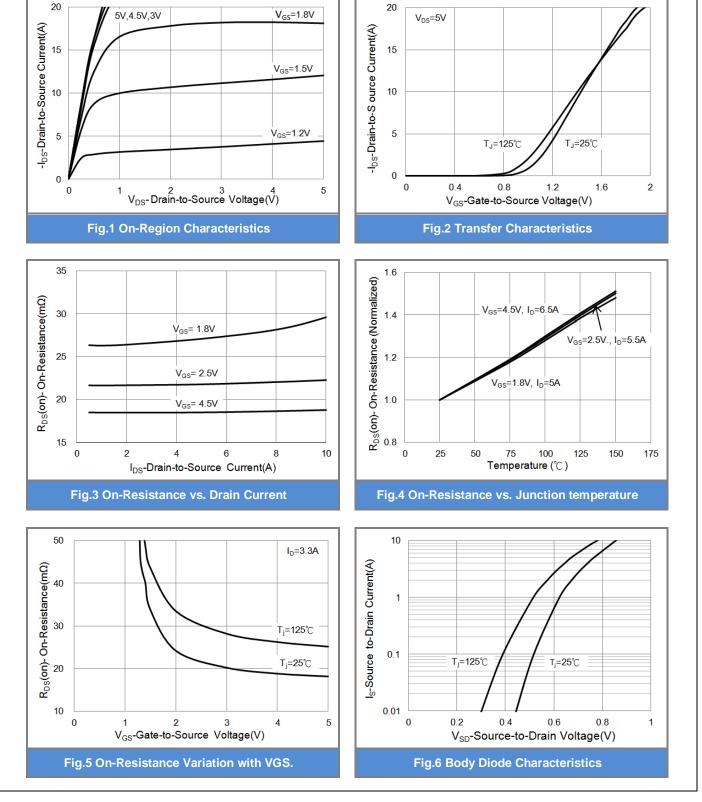
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Electrical Characteristics ($T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		1	1			
Drain-Source Breakdown Voltage	BV _{DSS}	V_{GS} =0V, I_{D} =250uA	20	-	-	V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	0.4	0.58	1.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =6.5A	-	18.4	22	mΩ
		V_{GS} =2.5V, I_{D} =5.5A	-	21.5	26	
		V _{GS} =1.8V, I _D =5.0A	-	26.4	34	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =20V, V_{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 8V, V _{DS} =0V	-	-	<u>+</u> 10	uA
Dynamic						
Total Gate Charge	Qg	V_{DS} =10V, I _D =6.5A, V_{GS} =4.5V ^(Note 1,2)	-	8.6	-	nC
Gate-Source Charge	Q_gs		-	1.06	-	
Gate-Drain Charge	Q_gd		-	1.04	-	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1.0MHZ	-	836	-	pF
Output Capacitance	Coss		-	96	-	
Reverse Transfer Capacitance	Crss		-	80	-	
Switching						
Turn-On Delay Time	td _(on)		-	24	-	ns
Turn-On Rise Time	tr	V_{DD} =10V, I_{D} =1A, V_{GS} =4.5V,	-	46	-	
Turn-Off Delay Time	td _(off)		-	0.22	-	us
Turn-Off Fall Time	tf	$R_G=3\Omega^{(Note 1,2)}$	-	0.30	-	
Drain-Source Diode						
Maximum Continuous Drain-Source			-	-	1.5	А
Diode Forward Current	I _S					
Diode Forward Voltage	V_{SD}	I _S =1.0A, V _{GS} =0V	-	0.74	1.0	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.



TYPICAL CHARACTERISTIC CURVES

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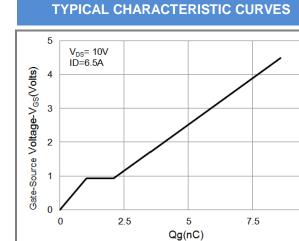


Fig.7 Gate-Charge Characteristics

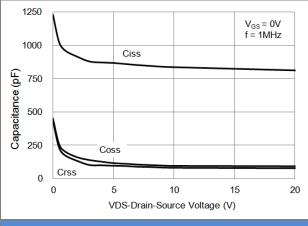


Fig.9 Capacitance vs. Drain-Source Voltage.



75

100

125

150

175

50

1.2

1.0

0.8

0.6

0.4

0.2

0

25

Vth-G-S Variance

10



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I_D=250uA



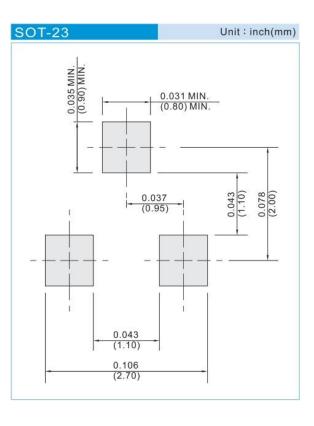


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PART NO PACKING CODE VERSION

PART NO PACKING CODE	Package Type	Packing type	Marking	Version
PJA3416AE_R1_00001	SOT-23	3K pcs / 7" reel	A6E	Halogen free
PJA3416AE_R2_00001	SOT-23	12K pcs / 13" reel	A6E	Halogen free

MOUNTING PAD LAYOUT





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