

P80FG7R5EN

Power MOSFETs 75V, 80A, N-channel

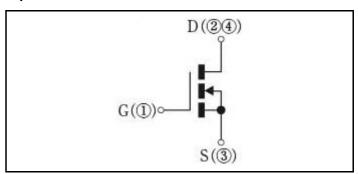
Feature

- N-channel
- SMD
- Low Ron
- 10V Gate Drive
- Low Capacitance
- · Pb free terminal
- RoHS:Yes

OUTLINE



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Channel tempertature	Tch		150	°C
Drain-source voltage	V_{DSS}		75	V
Gate-source voltage	V_{GSS}		±20	V
Continuous drain current(DC)	I _D		80	Α
Continuous drain current(Peak)	I _{DP}	Pulse width 10µs, duty=1/100	320	Α
Total power dissipation	P _T		128	W
Single avalanche current	I _{AS}	Starting Tch=25°C Tch≦150°C	44	Α
Single avalanche energy	E _{AS}	Starting Tch=25°C Tch≦150°C	175	mJ

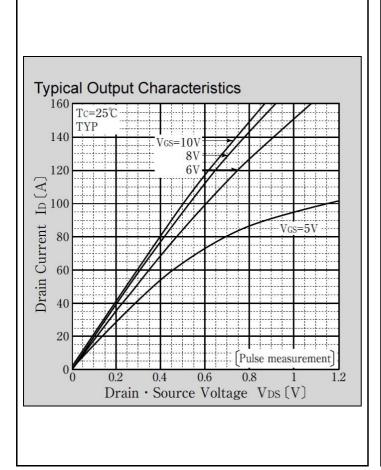
st :See the original Specifications

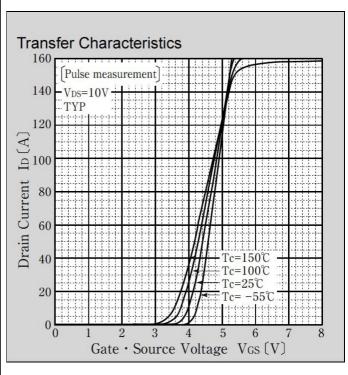
Electrical Characteristics (unless otherwise specified : Tc=25°C)

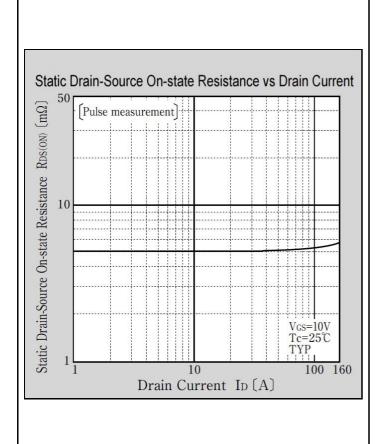
Item	Symbol	Conditions		Ratings		
item		Conditions		TYP	MAX	Unit
Drain-Source breakdown voltage	V _{(BR)DSS}	ID=1mA, VGS=0V	75			٧
Zero gate voltage drain current	I _{DSS}	VDS=75V, VGS=0V			1	μA
Gate-source leakage current	I _{GSS}	VGS=±20V, VDS=0V			±0.1	μΑ
Forward transconductance	9fs	ID=40A, VDS=10V	20	40		S
Static drain-source on-state resistance	R _{DS(ON)}	ID=40A, VGS=10V		0.0051	0.0064	Ω
Gate threshold voltage	Vth	ID=1mA, VDS=10V	2	3	4	V
Source-drain diode forward voltage	V_{SD}	IS=80A, VGS=0V			1.5	٧
Thermal resistance	Rth(j-c)	Junction to case			0.97	°C/W
Total gate charge	Qg	VDD=60V, VGS=10V, ID=80A		67		nC
Gate to source charge	Qgs	VDD=60V, VGS=10V, ID=80A		19		nC
Gate to drain charge	Qgd	VDD=60V, VGS=10V, ID=80A		24		nC
Input capacitance	Ciss	VDS=25V, VGS=0V, f=1MHz		4100		pF
Reverce transfer capacitnce	Crss	VDS=25V, VGS=0V, f=1MHz		230		pF
Output capacitance	Coss	VDS=25V, VGS=0V, f=1MHz		510		pF
Turn-on delay time	td(on)	ID=40A, RL=0.94Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		13		ns
Rise time	tr	ID=40A, RL=0.94Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		30		ns
Turn-off delay time	td(off)	ID=40A, RL=0.94Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		25		ns
Fall time	tf	ID=40A, RL=0.94Ω, VDD=37.5V, Rg=0Ω, VGS(+)=10V, VGS(-)=0V		5		ns
Diode reverse recovery time	trr	IF=80A, VGS=0V, di/dt=100A/μs		51		ns
Diode reverse recovery charge	Qrr	IF=80A, VGS=0V, di/dt=100A/μs		95		nC

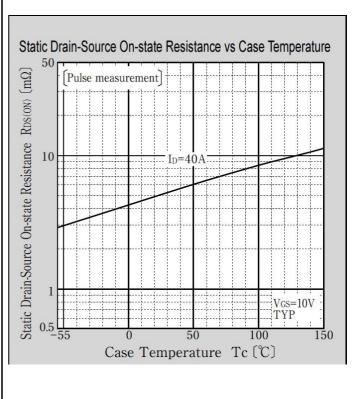
^{*} :See the original Specifications

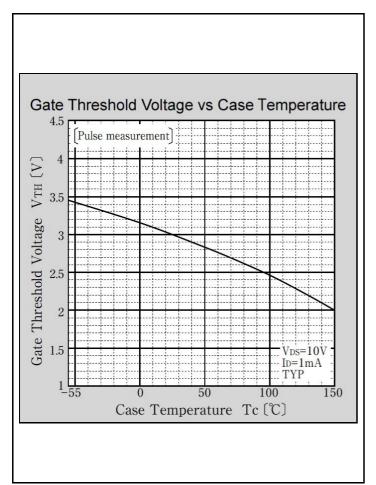
CHARACTERISTIC DIAGRAMS

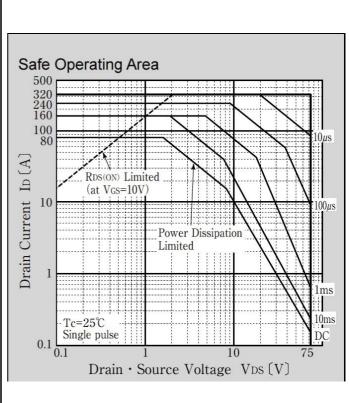


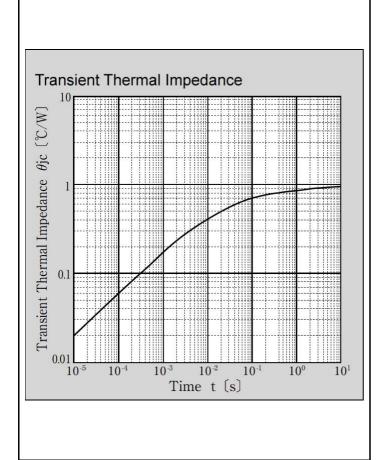


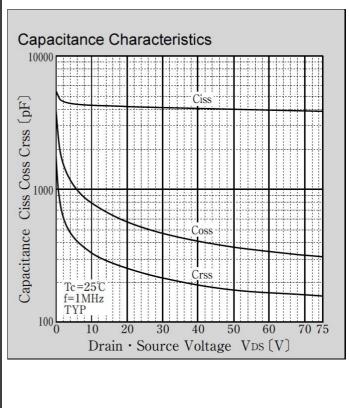


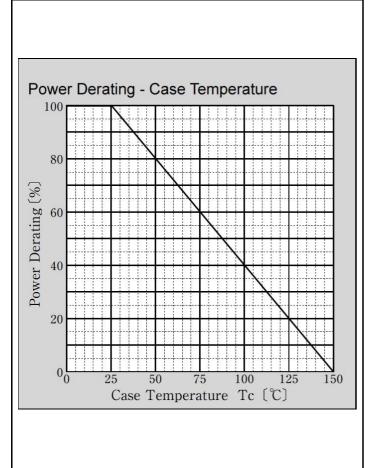


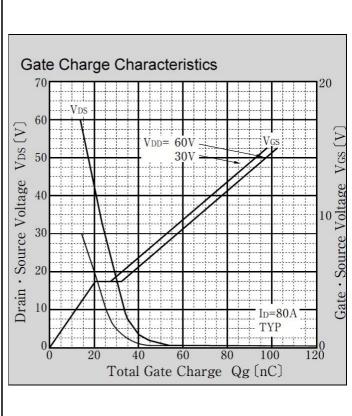


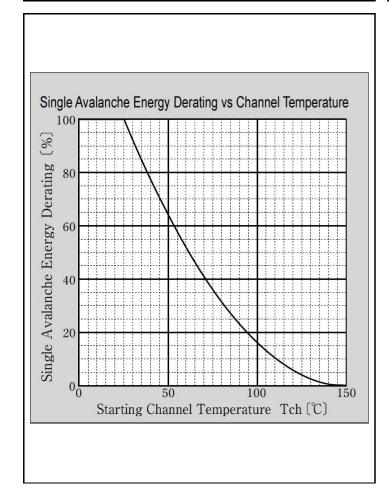










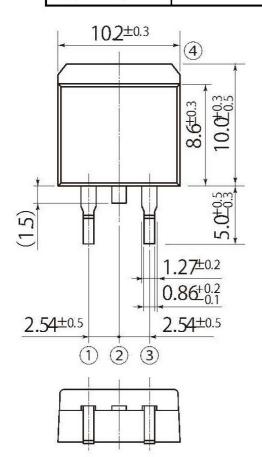


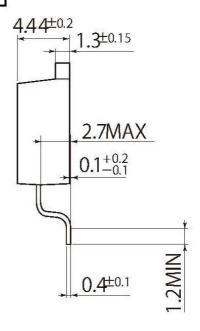
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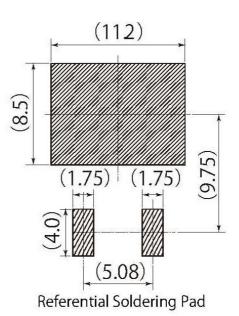
scale: 3/1

H4

JEDEC Code	TO-263AB		
JEITA Code	()		
House Name	FG		







 \bullet Optimize soldering pad to the board design and soldering condition.

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