# PJEC5V0V6TM Very Low Capacitance ESD Protection VRWM 5V Features

- Bidirectional ESD protection of one line
- IEC61000-4-2(ESD):  $\pm$ 15kV Air,  $\pm$ 8kV Contact Compliance with the capability up to  $\pm$ 30kV
- IEC61000-4-4(EFT): 40A(5/50nS)
- IEC61000-4-5(Lightning): 3.5A(8/20μS)
- Low leakage current, maximum of  $0.1\mu A$  at rated voltage
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.

#### (Halogen Free)

#### Mechanical Data

- Case: SOD-923, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.00002 ounces, 0.0004 grams
- Marking: P

#### Applications

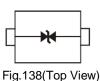
- Mobile Phones and accessories
- Desktops, Servers and Notebook
- Hand held portable
- Digital Cameras
- Computer Interfaces Protection
- Serial and Parallel Ports Protection
- Control Signal Lines Protection

### Maximum Ratings ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
ESD IEC61000-4-2(Air)	M	±30	kV	
ESD IEC61000-4-2(Contact)	$V_{ESD}$	±30		
Operating Junction Temperature	$T_{J}$	-55 to +125	°C	
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C	

SOD-923 Unit : inch(mm) 0.034(0.85) 0.026(0.65) 0.029(0.75) 0.026(0.65) 0.021(0.55) 0.013(0.35)  $0.007(0.18)^{\frac{1}{2}}$  0.042(1.05)0.037(0.95)



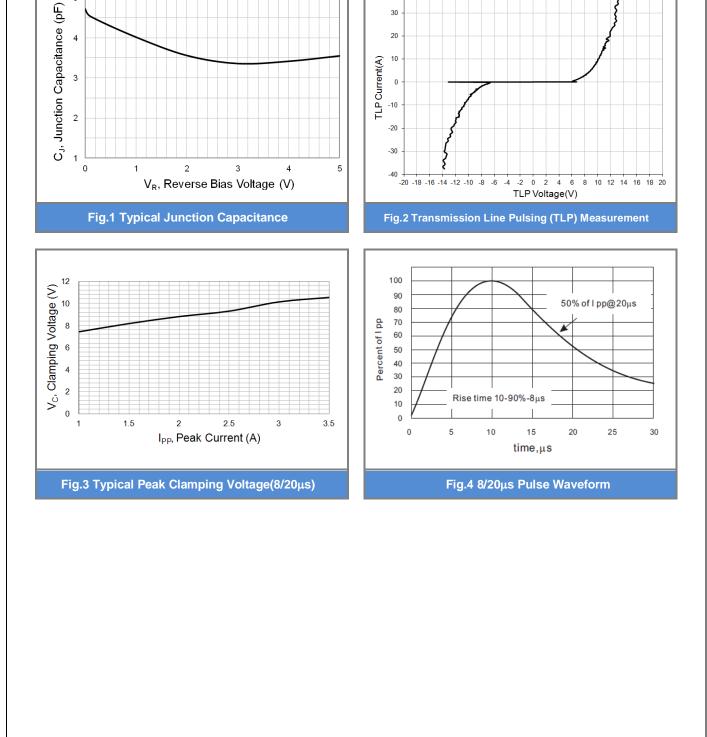




Electrical Characteristics ( $T_A=25^{\circ}C$ unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Reverse Stand-Off Voltage	V <sub>RWM</sub>	-	-	-	5	V	
Snap-Break Voltage	V <sub>SB</sub>	I <sub>SB</sub> =50mA	5	-	8	V	
Reverse leakage current	I <sub>R</sub>	V <sub>R</sub> =5.0V	-	-	0.1	μA	
Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =1A, t <sub>P</sub> =8/20μs	-	-	9	V	
		I <sub>PP</sub> =3.5A, t <sub>P</sub> =8/20μs	-	-	12.5		
Clamping Voltage TLP <sup>(Note 1)</sup>	V <sub>CL</sub>	I <sub>PP</sub> =4A, t <sub>P</sub> =100ns	-	8.6	-	V	
		I <sub>PP</sub> =8A, t <sub>P</sub> =100ns	-	9.7	-		
Dynamic Resistance	R <sub>DYN</sub>	t <sub>P</sub> =100ns	-	0.27	-	Ω	
Off State Junction Capacitance	CJ	0Vdc Bias f=1MHz	-	-	6	рF	

NOTES :

1. Testing using Transmission Line Pulse (TLP) conditions:  $Z_0$  = 50  $\Omega$  ,  $t_P$  = 100 ns.



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### **TYPICAL CHARACTERISTIC CURVES**

PJEC5V0V6TM

### PANJ SEMI CONDUCTOR

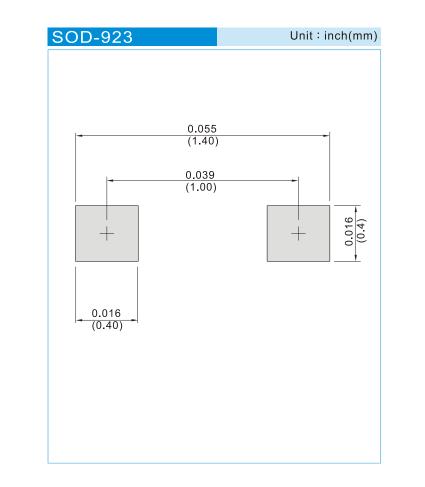
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#### MOUNTING PAD LAYOUT



#### ORDER INFORMATION

- Packing information
  - T/R 8K per 7" plastic Reel





#### Part No\_packing code\_Version

PJEC5V0V6TM\_R1\_00001

#### For example :

#### RB500V-40\_R2\_00001

Part No.

Serial number
Version code means HF

Packing size code means 13"

#### - Packing type means T/R

Packing Code XX			Version Code XXXXX			
Packing type	1 <sup>st</sup> Code	Packing size code	2 <sup>nd</sup> Code	HF or RoHS	1 <sup>st</sup> Code	2 <sup>nd</sup> ~5 <sup>th</sup> Code
Tape and Ammunition Box (T/B)	A	N/A	0	HF	0	serial number
Tape and Reel (T/R)	R	7"	1	RoHS	1	serial number
Bulk Packing (B/P)	В	13"	2			
Tube Packing (T/P)	т	26mm	X			
Tape and Reel (Right Oriented) (TRR)	S	52mm	Y			
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP (PBCU)	U			
FORMING	F	PANASERT T/B CATHODE DOWN (PBCD)	D			



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