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# ERC12864-2 Series

# **Graphic Module User Manual**

# **EastRising Technology Co., Limited**

Attention:

- A. Click" CONTENTS" list could be directed to the detail page. Click "Return to CONTENTS" could be directed to CONTENTS Page.
- B. Some specifications of IC are not listed in this manual. Please refer to the IC manual for more details.
- C. The drawing for related touch panel, schematic drawing, demo code is all available, please contact with our sales if you need.
- D. Please pay more attention to "INSPECTION CRITERIA" in this manual. We assume you already agree with these criterions when you place the order with us. No more recommendations.

REV	DESCRIPTION	RELEASE DATE
1.0	Preliminary Release	Jun-26-2012



Graphic Module User Manual ERC12864-2 Series

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### **1. ORDERING INFORMATION**

1.1 ERC12864-2 Series Table

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\*The number of series table is in accordance with number of the below series image 1.2.

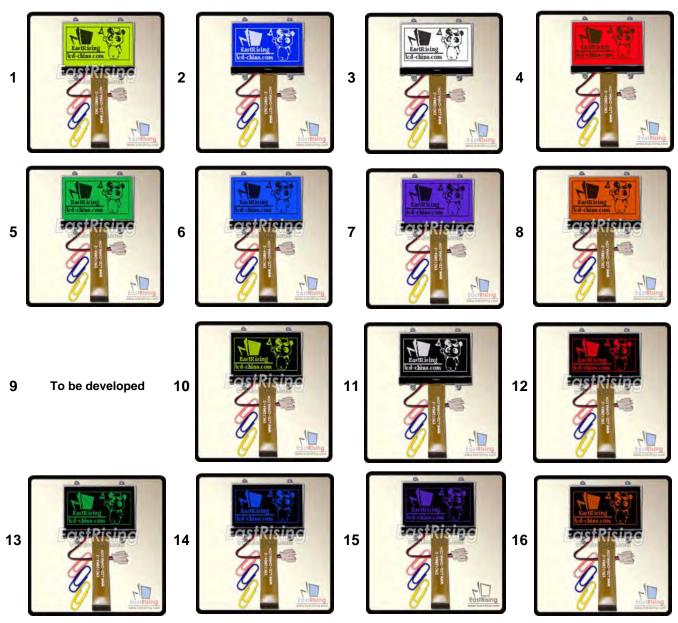
No.	Part Number	LCD Type	Backlight Color	Graphic & Font Color	Background Color
1	ERC12864FYGF-2	FSTN Positive	Yellow Green Color	Dark Blue	Yellow Green Color
2	ERC12864SBSF-2	STN Negative Blue	White Color	White Color	Blue Color
3	ERC12864FSF-2	FSTN Positive	White Color	Black Color	White Color
4	ERC12864FRF-2	FSTN Positive	Red Color	Black Color	Red Color
5	ERC12864FGF-2	FSTN Positive	Green Color	Black Color	Green Color
6	ERC12864FBF-2	FSTN Positive	Blue Color	Black Color	Blue Color
7	ERC12864FPF-2	FSTN Positive	Purple Color	Black Color	Purple Color
8	ERC12864FAMF-2	FSTN Positive	Amber Color	Black Color	Amber Color
9	ERC12864F7-2	FSTN Positive	RGB Color	Black Color	RGB Color
10	ERC12864DNYGF-2	FFSTN Negative	Yellow Green Color	Yellow Green Color	Black Color
11	ERC12864DNSF-2	FFSTN Negative	White Color	White Color	Black Color
12	ERC12864DNRF-2	FFSTN Negative	Red Color	Red Color	Black Color
13	ERC12864DNGF-2	FFSTN Negative	Green Color	Green Color	Black Color
14	ERC12864DNBF-2	FFSTN Negative	Blue Color	Blue Color	Black Color
15	ERC12864DNPF-2	FFSTN Negative	Purple Color	Purple Color	Black Color
16	ERC12864DNAMF-2	FFSTN Negative	Amber Color	Amber Color	Black Color
17	ERC12864DN7-2	FFSTN Negative	RGB Color	RGB Color	Black Color



### 1.2 ERC12864-2 Series Image

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\*The number of series image is in accordance with number of the above series table 1.1.



#### 17 To be developed



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### 2. ATTRIBUTES

2.1 Display Attributes

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ITEM	STANDARD VALUE	UNIT
Dot Matrix	128 x 64 Dots	
Display Connector	FPC	
FPC Connector	0.5mm Pitch Horizontal SMT Bottom Contact 30 Pins	
Operating Temperature	-20 ~ +70	°C
Storage Temperature	-30 ~ +80	°C
Touch Panel Optional	N/A	
Font Chip Optional	N/A	
*Sunlight Readable	No1,No3,No4,No5,No6,No7,No8,No9	

\*Number of sunlight readable is from 1.1 ERC12864-2 Series Table of the manual.

### 2.2 Mechanical Attributes

2.2 Mechanical Attributes	>>Return to CONTENTS	
ITEM	STANDARD VALUE	UNIT
Outline Dimension with FPC Folded	69.54(W) X 58.00(H) X 5.0(T) (MAX)	mm
Visual Area	64.5(W) X 37.0(H)	mm
Active Area	59.49(W) × 29.73(H)	mm
Dot Size	0.435 X 0.435	mm
Dot Pitch	0.465 X 0.465	mm
Net Weight	26.0 ± 15% grams (typical)	g

### 2.3 Electrical Attributes

#### >>Return to CONTENTS ITEM **STANDARD VALUE** UNIT IC Package COG ---Controller NT7538 --8080 8-bit Parallel, 6800 8-bit Parallel, 4-Wire SPI Interface --

### 2.4 Optical Attributes

ITEM	EM     STANDARD VALUE	
LCD Type	Type Refer to 1.1 ERC12864-2 Series Table	
Backlight Color	Refer to1.1 ERC12864-2 Series Table	
Viewing Direction	6:00	Clock
LCD Duty	1/65	Duty
LCD Bias	1/9	Bias

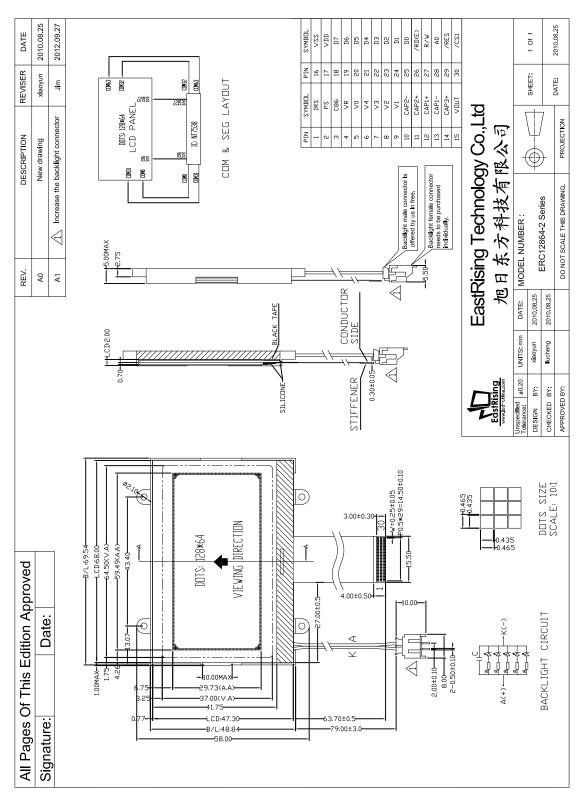


Graphic Module User Manual

ERC12864-2 Series

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## **3. OUTLINE DRAWING**





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## 4. ELECTRICAL SPEC

4.1 Pin Configuration

No.         Name         Descriptions           1         IRS         This terminal selects the resistors for the V0 voltage level adjustment IRS="H": Use the internal resistors IRS="L": Do not use the internal resistors           2         PS         P/S = "H": Parallel data input/serial data input switch terminal           3         C86         EThis is the parallel data input switch terminal           3         C86         = "H": Serial data input           4         VR         This is the MPU interfaces           4         VR         Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.           5-9         V3         application. Voltages should be according to the following relationship:           V2         V0≥V1≥V3≥V3≥V3≥V3≥V3≥V52         V1           10         CAP2-         Capacitor 2- pad for internal DC/DC voltage converter           11         CAP2+         Capacitor 1+ pad for internal DC/DC voltage converter           12         CAP1+         Capacitor 1+ pad for internal DC/DC voltage converter           14         CAP3+         Capacitor 3+ pad for internal DC/DC voltage converter           15         VOUT         DC/DC voltage converter           16         VSS         Ground. These pads must be connected to each other.           17         VDD         Power supply output	Pin	Pin	Descriptions
IRS="H": Use the internal resistors IRS="L": Do not use the internal resistors2PSP/S = "H": Parallel data input/serial data input switch terminal P/S = "L": Serial data input P/S = "L": Serial data input3C86C86 = "H": 6800 Series MPU interface C86 = "L": 8080 Series MPU interface C86 = "L": 8080 Series MPU interface4VRVoltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.4VRVoltage adjustment pad. Applies voltage between V0 and VSS using a resistive 	No.	Name	Descriptions
IRS="L": Do not use the internal resistors         IRS="L": Do not use the internal resistors         This is the parallel data input/ P/S = "H": Parallel data input         IRS="L": Serial data input         PS       P/S = "H": Parallel data input         IRS="L": Serial data input         P/S = "L": 8080 Series MPU interface         C86 = "L": 8080 Series MPU interface         VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         1       CD driver suppliers voltages. The voltage convertier on apperation amplifier for application. Voltages should be according to the following relationship:         V	1	IRS	This terminal selects the resistors for the V0 voltage level adjustment
2       PS       This is the parallel data input/serial data input switch terminal         2       PS       P/S = "H": Parallel data input         3       C86       This is the MPU interfaces switch terminal         3       C86       "H": 8000 Series MPU interface         4       VR       Vage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         4       VR       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V0 ≥ V1 ≥ V2 ≥ V3 ≥ V4 ≥ VSS2         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 1+ pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1+ pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 1+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected(P/S="L"), then D7 serves as the serial data input			IRS="H": Use the internal resistors
2       PS       P/S = "H": Parallel data input         3       C86       C86 = "L": Serial data input         3       C86       C86 = "H": 6800 Series MPU interface C86 = "L": 8080 Series MPU interface C86 = "L": 8080 Series MPU interface         4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         5       V0       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for 3 application. Voltages should be according to the following relationship:         V0       V2       V0≥V1≥V3≥V4≥VS2         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 1+ pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1- pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 3+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected(P/S="L"), then D7 serves as the serial data input termina			IRS="L": Do not use the internal resistors
P/S = "L": Serial data input         3       C86       Fhis is the MPU interfaces switch terminal C86 = "H": 6800 Series MPU interface C86 = "L": 6080 Series MPU interface C86 = "L": 6080 Series MPU interface         4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         5       V0       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for v3         5-9       V3       application. Voltages should be according to the following relationship: V2         V0       LCD driver suppliers voltage. The voltage converter         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 1+ pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1+ pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 3+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected(P/S="L"), then D7 serves as the serial data input termina			This is the parallel data input/serial data input switch terminal
3       C86       This is the MPU interfaces switch terminal         3       C86       "H": 6800 Series MPU interface         4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         5-9       V3       application. Voltages should be according to the following relationship:         V2       V0       V0≥V1≥V2≥V3≥V4≥VSS2         V1       V1         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 2- pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1- pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 3+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected, fix D0-D5 pads to VDD or VSS level.         When the chip select is inactive, D0 to D7 are set to high impendence.       <	2	PS	P/S = "H": Parallel data input
3       C86       C86 = "H": 6800 Series MPU interface C86 = "L": 8080 Series MPU interface         4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         5-9       V0       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V2         5-9       V3       application. Voltages should be according to the following relationship: V2         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 2- pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1- pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 3+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/Dc voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected (P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the chip select is inactive, D0 to D7 are set to high impendence.         26<			P/S = "L": Serial data input
C86 = "L": 8080 Series MPU interface4VRVoltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.5-9V0LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V2 V15-9V3application. Voltages should be according to the following relationship: V2 V1 >V2 >V3 >V4 >V2 >V3 >V4 >VSS2 V110CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 1- pad for internal DC/DC voltage converter12CAP1+Capacitor 1- pad for internal DC/DC voltage converter13CAP1-Capacitor 3+ pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7-D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the chip select is inactive, D0 to D7 are set to high impedance.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data			This is the MPU interfaces switch terminal
4       VR       Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive divider.         5-9       V0       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V0 >V1 >V2 >V3 >V4 >VSS2         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 2- pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1+ pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 3+ pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7~D0       When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the chip select is inactive, D0 to D7 are set to high impendence.         26       /RD(E)       This is used as an enable clock input of the 6800 series MPU         27       R/W       When R/W = "H": Read When R/W = "L": Write         28       A0	3	C86	C86 = "H": 6800 Series MPU interface
4       VR       divider.         4       V0       LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship:         5-9       V3       application. Voltages should be according to the following relationship:         V2       V0 ≥V1 ≥V2 ≥V3 ≥V4 ≥VSS2         V1			C86 = "L": 8080 Series MPU interface
Idivider.V0LCD driver suppliers voltages. The voltage determined by the LCD cell is impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V2 V0>V1>V2>V3>V4>VSS2 V110CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 1- pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	1	VR	Voltage adjustment pad. Applies voltage between V0 and VSS using a resistive
V4impedance-converted by a resistive driver or an operation amplifier for application. Voltages should be according to the following relationship: V2 V0≥V1≥V2≥V3≥V4≥VS210CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	-	VIX	divider.
5-9       V3       application. Voltages should be according to the following relationship:         V2       V0 ≥ V1 ≥ V2 ≥ V3 ≥ V4 ≥ VSS2         10       CAP2-       Capacitor 2- pad for internal DC/DC voltage converter         11       CAP2+       Capacitor 2+ pad for internal DC/DC voltage converter         12       CAP1+       Capacitor 1+ pad for internal DC/DC voltage converter         13       CAP1-       Capacitor 1- pad for internal DC/DC voltage converter         14       CAP3+       Capacitor 3+ pad for internal DC/DC voltage converter         15       VOUT       DC/DC voltage converter output         16       VSS       Ground. These pads must be connected to each other.         17       VDD       Power supply output for pad option         18-25       D7-D0       When the serial interface is selected (P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the chip select is inactive, D0 to D7 are set to high impendence.         26       /RD(E)       This is used as an enable clock input of the 6800 series MPU         27       R/W       When R/W = "H": Read When R/W = "H": Write         28       A0       A0="H": Indicate that D0 to D7 are display data		V0	LCD driver suppliers voltages. The voltage determined by the LCD cell is
V2 V1 $V0 \ge V1 \ge V2 \ge V3 \ge V4 \ge VSS2$ 10CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data		V4	impedance-converted by a resistive driver or an operation amplifier for
V110CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	5-9	V3	application. Voltages should be according to the following relationship:
10CAP2-Capacitor 2- pad for internal DC/DC voltage converter11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data		V2	$V0 \ge V1 \ge V2 \ge V3 \ge V4 \ge VSS2$
11CAP2+Capacitor 2+ pad for internal DC/DC voltage converter12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data		V1	
12CAP1+Capacitor 1+ pad for internal DC/DC voltage converter13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	10	CAP2-	Capacitor 2- pad for internal DC/DC voltage converter
13CAP1-Capacitor 1- pad for internal DC/DC voltage converter14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	11	CAP2+	Capacitor 2+ pad for internal DC/DC voltage converter
14CAP3+Capacitor 3+ pad for internal DC/DC voltage converter15VOUTDC/DC voltage converter output16VSSGround. These pads must be connected to each other.17VDDPower supply output for pad option18-25D7~D0When the serial interface is selected(P/S="L"), then D7 serves as the serial data input terminal (SI) and D6 serves as the serial clock input terminal (SCL). When the serial interface is selected, fix D0~D5 pads to VDD or VSS level. When the chip select is inactive, D0 to D7 are set to high impendence.26/RD(E)This is used as an enable clock input of the 6800 series MPU27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data	12	CAP1+	Capacitor 1+ pad for internal DC/DC voltage converter
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27R/WWhen R/W = "H": Read When R/W = "L": Write28A0A0="H": Indicate that D0 to D7 are display data			When the chip select is inactive, D0 to D7 are set to high impendence.
When R/W = "L": Write       28     A0       A0="H": Indicate that D0 to D7 are display data	26	/RD(E)	This is used as an enable clock input of the 6800 series MPU
28     A0     A0="H": Indicate that D0 to D7 are display data	27	R/W	When R/W = "H": Read
			When R/W = "L": Write
A0="L": Indicate that D0 to D7 are control data	28	A0	A0="H": Indicate that D0 to D7 are display data
			A0="L": Indicate that D0 to D7 are control data



_						
	29	/RES	When /RES is set to "L", the settings are initialized. The reset operation is			
			performed by the /RES signal level			
	30	/CS1	This is the chip select signal.			



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4.2 Absolute Maximum Ratings >>Return to CONT					NTENTS
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Power Supply for Logic	VDD-VSS	-0.3	-	+4.0	V
Power Supply for LCD	VOUT	-0.3	-	+15	V
Input Voltage	VIN	-0.3	-	Vdd+0.3	V
Supply Current for Backlight	ILED	-	-	125	mA

### 4.3 Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply for LCM	VDD-VSS	-	3.0	3.3	3.6	V
Input Voltage	VIL	L Level	VSS	-	0.2VDD	V
	VIH	H Level	0.8VDD	-	VDD	V
LCD Driving Voltage	V0-VSS	-	11.3	11.5	11.7	V
Supply Current for LCM	IDD	VDD=3.3V	-	-	450.0	uA
Supply Current for Backlight	ILED	-	50	75	100	mA
Power Supply for Backlight	VLED	-	-	3.3	-	V
(White,Blue,Green Color)						
Power Supply for Backlight	VLED	-	1.8	2.0	2.2	V
(Red,Purple,Amber Color)						
Power Supply for Backlight	VLED	-	1.8	2.0	2.2	V
(Yellow Green Color)						



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### **5. INSPECTION CRITERIA**

5.1 Acceptable Quality Level

Each lot should satisfy the quality level defined as follows

PARTITION	AQL	DEFINITION
A. Major	0.4%	Functional defective as product
B. Minor	1.5%	Satisfy all functions as product but not satisfy cosmetic stanard

5.2 Definition of Lot

One lot means the delivery quantity to customer at one time.

- 5.3 Condition of Cosmetic Inspection
- INSPECTION AND TEST

-FUNCTION TEST -APPEARANCE INSPECTION

- -PACKING SPECIFICTION
- ♦ INSPECTION CONDITION
  - Put under the lamp (20wiÁ2) at a distance 100mm from
  - Tilt upright 45 degree by the front (back) to inspect LCD appearance.
- ◆ AQL INSPECTION LEVEL
  - SAMPLING METHOD: MIL-STD-105D
  - SAMPLING PLAN: SINGLE
  - MAJOR DEFECT: 0.65% (MAJOR)
  - MINOR DEFECT: 2.5% (MINOR)
  - GENERAL LEVEL: II/NORMAL



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5.4 Module Cosmetic Criteria

NO.	Item	Judgment Criterion	Partition
1	Difference in Spec.	None allowed	Major
2	Pattern Peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on substrate	Invisible copper foil( $@0.5$ mm or more)on substrate pattern	Minor
5	Accretion of metallic	No soldering dust	Minor
	Foreign matter	No accretion of metallic foreign matters(Not exceed ⊄ 0.2mm)	
6	Stain	No stain to spoil cosmetic badly	Minor
7	Plate discoloring	No plate fading,rusting and discoloring	Minor
8	Solder amount	a. Soldering side of PCB	Minor
	1.Lead parts	Solder to form a'Filet' all around the lead. Solder should not hide the lead form perfectly.(too much) b.Components side (In case of 'Through Hole PCB') Solder to reach the Components side of PCB	Minor
	2.Flat packages	Either 'toe'(A) or 'heal' (B) of the lead to be covered by 'Filet' Lead form to be assume over Solder.	Minor
	3.Chips	(3/2) H≧h≧(1/2)H	Minor



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9	Backlight defects	<ol> <li>Light fails or flickers.(Major)</li> <li>Color and luminance do not correspond to specifications. (Major)</li> <li>Exceeds standards for display's blemishes, foreign matter, dark lines or scratches.(Minor)</li> </ol>	See list ←
10	PCB defects	Oxidation or contamination on connectors.* 2. Wrong parts, missing parts, or parts not in specification.* 3.Jumpers set incorrectly.(Minor) 4.Solder(if any)on bezel,LED pad,zebra pad,or screw hole pad is not smooth.(Minor) *Minor if display functions correctly.Major if the display fails.	See list ←
11	Soldering defects	<ol> <li>Unmelted solder paste.</li> <li>Cold solder joints, missing solder connections, or oxidation.*</li> <li>Solder bridges causing short circuits.*</li> <li>Residue or solder balls.</li> <li>Solder flux is black or brown.</li> <li>*Minor if display functions correctly.Major if the display fails.</li> </ol>	Minor



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5	5.5 Screen Cosmetic Criteria (Non-Operating) >>Return to			CONTENTS
No.	Defect	Judgment Criterion		Partition
1	Spots	In accordance with Screen Cosmetic Criteria (Operating) No.1.		Minor
2	Lines	In accordance with Screen Cosme	tic Criteria (Operation) No.2.	Minor
3	Bubbles in			
	Polarizer	Size: d mm	Acceptable Qty in active area	
		d≦0.3	Disregard	
		0.3 <d≦1.0< td=""><td>3</td><td></td></d≦1.0<>	3	
		1.0 <d≦1.5< td=""><td>1</td><td></td></d≦1.5<>	1	
		1.5 <d< td=""><td>0</td><td></td></d<>	0	
4	Scratch In accordance with spots and lines operating cosmetic criteria, When the		s operating cosmetic criteria, When the	Minor
		light reflects on the panel surface,		
5	Allowable density	Above defects should be separated more than 30mm each other.		Minor
6	Coloration	Not to be noticeable coloration in t	Minor	
		Back-lit type should be judged with		
7	Contamination	Not to be noticeable.		Minor



5.6 Screen Cosmetic Criteria (Operating)

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No.	Defect	Judgmei	nt Criterion	Partition
1	Spots	A) Clear		Minor
		Size:d mm	Acceptable Qty in active area	
		d≦0.1	Disregard	
		0.1 <d≦0.2< td=""><td>6</td><td></td></d≦0.2<>	6	
		0.2 <d≦0.3< td=""><td>2</td><td></td></d≦0.3<>	2	
		0.3 <d< td=""><td>0</td><td></td></d<>	0	
		Note: Including pin holes and defecti	ve dots which must be within one pixel	
		Size.		
		B) Unclear		
		Size:d mm	Acceptable Qty in active area	
		d≦0.2	Disregard	
		0.2 <d≦0.5< td=""><td>6</td><td></td></d≦0.5<>	6	
		0.5 <d≦0.7< td=""><td>2</td><td></td></d≦0.7<>	2	
		0.7 <d< td=""><td>0</td><td></td></d<>	0	
2	Lines	A) Clear		Minor
		L 5.0 (0)		
		2.0 (6)	See No.1	
			w	
		0.02 0.05	0.1	
		Note: () – Acceptable Qty in active a	roa	
		L - Length (mm)		
		W -Width(mm)		
		∞-Disregard		
		B) Unclear		
		b) Officieal		
		L 10.0	(0)	
		∞ (6)		
		2.0	See No.1	
			W	
		0.05 0	.3 0.5	

'Clear' = The shade and size are not changed by Vo.'Unclear' = The shade and size are changed by Vo.



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No.	Defect	Judgment Criterion	Partition			
3	Rubbing line	Not to be noticeable.				
4	Allowable density	Above defects should be separated more than 10mm each other.	Minor			
5	Rainbow	Not to be noticeable.	Minor			
6	Dot size	To be 95%~105%of the dot size (Typ.) in drawing. Partial defects of each dot (ex.pin-hole) should be treated as'spot'.	Minor			
		(see Screen Cosmetic Criteria (Operating) No.1)				
7	Brightness (only back-lit Module)	Brightness Uniformity must be BMAX/BMIN≦2 - BMAX :Max.value by measure in 5 points - BMIN : Min.value by measure in 5 points Divide active area into 4 vertically and horizontally. Measure 5 points shown in the following figure.	Minor			
8	Contrast Uniformity	Contrast Uniformity must be BmAX/BMIN≦2 Measure 5 points shown in the following figure. Dashed lines divide active area into 4 vertically and horizontally. Measuring points are located at the inter-sections of dashed line.	Minor			

Note:

(1) Size : d=(long length + short length)/2

(2) The limit samples for each item have priority.

(3) Complexed defects are defined item by item, but if the number of defects are defined in above table, the total number should not exceed 10.



(4) In case of 'concentration', even the spots or the lines of 'disregarded' size should not be allowed. Following three situations

Should be treated as 'concentration'.

- -20 or over defects in circle of  $\not \subset$  20mm

### 6. PRECAUTIONS FOR USING

6.1 Handling Precautions

- This device is susceptible to Electro-Static Discharge (ESD) damage. Observe Anti-Static precautions.
- EastRising display panel is made of glass. Do not subject it to a mechanical shock by dropping it or impact.
- If EastRising display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- Do not apply excessive force to the EastRising display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the EastRising display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- If EastRising display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following Isopropyl or alcohol.
- Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the Water.
- Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- Install the EastRising LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the cable or the backlight cable.
- Do not attempt to disassemble or process EastRising LCD module.
- NC terminal should be open. Do not connect anything.
- If the logic circuit power is off, do not apply the input signals.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - -Be sure to ground the body when handling EastRising LCD modules.
  - -Tools required for assembling, such as soldering irons, must be properly grounded.
  - -To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
  - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### some variance between models.

- Prevent the application of reverse polarity to VDD and VSS, however briefly.
- Use a clean power source free from transients. Power-up conditions are occasionally jolting and may exceed the maximum ratings of EastRising modules.

• Identify and, at all times, observe absolute maximum ratings for both logic and LC drivers. Note that there is

◆ The VDD power of EastRising module should also supply the power to all devices that may access the display. Don't allow the data bus to be driven when the logic supply to the module is turned off.

### 6.3 Operating Precautions

- DO NOT plug or unplug EastRising module when the system is powered up.
- Minimize the cable length between EastRising module and host MPU.
- ◆ For models with backlights, do not disable the backlight by interrupting the HV line. Unload inverters produce voltage extremes that may arc within a cable or at the display.
- Operate EastRising module within the limits of the modules temperature specifications.

#### 6.4 Mechanical/Environmental Precautions

- ◆ Improper soldering is the major cause of module difficulty. Use of flux cleaner is not recommended as they may seep under the electrometric connection and cause display failure.
- Mount EastRising module so that it is free from torque and mechanical stress.
- Surface of the LCD panel should not be touched or scratched. The display front surface is an easily scratched, plastic polarizer. Avoid contact and clean only when necessary with soft, absorbent cotton dampened with petroleum benzene.
- ◆ Always employ anti-static procedure while handling EastRising module.
- Prevent moisture build-up upon the module and observe the environmental constraints for storage tem
- Do not store in direct sunlight
- ◆ If leakage of the liquid crystal material should occur, avoid contact with this material, particularly ingestion. If the body or clothing becomes contaminated by the liquid crystal material, wash thoroughly with water and soap

### 6.5 Storage Precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep EastRising modules in bags (avoid high temperature / high humidity and low temperatures below 0C Whenever possible, EastRising LCD modules should be stored in the same conditions in which they were shipped from our company.

### 6.6 Others

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Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module

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6.2 Power Supply Precautions



is subject to a low temperature.

If EastRising LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

-Exposed area of the printed circuit board.

-Terminal electrode sections.

## 7. USING LCD MODULES

- 7.1 Liquid Crystal Display Modules >>Return to CONTENTS EastRising LCD is composed of glass and polarizer. Pay attention to the following items when handling.
- ◆ Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- When EastRising display surface becomes dusty, wipe gently with absorbent cotton or other soft material like chamois soaked in petroleum benzin. Do not scrub hard to avoid damaging the display surface.
- Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- Avoid contacting oil and fats.
- Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- Do not put or attach anything on EastRising display area to avoid leaving marks on.
- Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

### 7.2 Installing LCD Modules

- Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
- When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be  $\pm 0.1$  mm.



### www.lcd-china.com 7.3 Precaution for Handling LCD Modules

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Since EastRising LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist EastRising LCM.

### 7.4 Electro-Static Discharge Control

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Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- Make certain that you are grounded when handing LCM.
- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

### 7.5 Precaution for Soldering to EastRising LCM

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◆ Observe the following when soldering lead wire, connector cable and etc. to the LCM.
 -Soldering iron temperature : 280°C ± 10°C
 2 black in the content of the c

-Soldering time: 3-4 sec.

-Solder: eutectic solder.

If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

- When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.



7.6 Precaution for Operation

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- Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- Driving the EastRising LCD in the voltage above the limit shortens its life.
- Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- If EastRising display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- ◆ Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- When turning the power on, input each signal after the positive/negative voltage becomes stable.

### 7.7 Limited Warranty

Unless agreed between EastRising and customer, EastRising will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with EastRising LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to EastRising within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of EastRising limited to repair and/or replacement on the terms set forth above. EastRising will not be responsible for any subsequent or consequential events.

### 7.8 Return Policy

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No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- -Broken LCD glass.
- -PCB eyelet damaged or modified.
- -PCB conductors damaged.
- -Circuit modified in any way, including addition of components.
- -PCB tampered with by grinding, engraving or painting varnish.
- -Soldering to or modifying the bezel in any manner.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet's, conductors and terminals

That's the end of the Manual