

**UTC** UNISONIC TECHNOLOGIES CO., LTD

**ULD3380** 

Preliminary

LINEAR INTEGRATED CIRCUIT

# HIGH EFFICIENCY PWM BUCK LED DRIVER CONTROLLER

#### DESCRIPTION

The UTC ULD3380 is a PWM mode step-down converter. By well controlling the external MOSFET and regulating a constant output current. The output duty cycle of the UTC ULD3380 can be up to 100% for wider input voltage application.

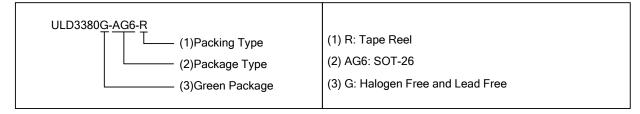
The UTC ULD3380 is available in a SOT-26 package.

#### **FEATURES**

- \* Universal input voltage range with off-line topology
- \* Programmable constant LED current
- \* Output LED string short protection
- \* Output LED string open protection
- \* Dimmable LED current by ACTL
- \* OCP
- \* Built-in OTP

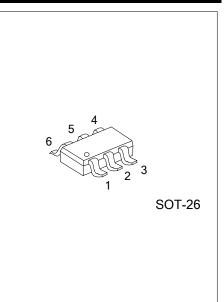
#### **ORDERING INFORMATION**

| Ordering Number | Package | Packing   |
|-----------------|---------|-----------|
| ULD3380G-AG6-R  | SOT-26  | Tape Reel |



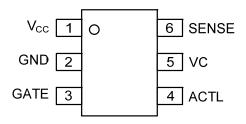
### MARKING





## ULD3380

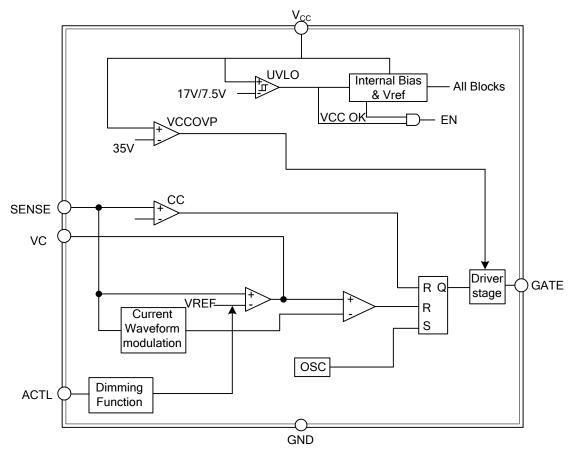
### PIN CONFIGURATIONS



### ■ PIN DESCRIPTION

| PIN NO. | PIN NAME        | DESCRIPTION                             |  |  |
|---------|-----------------|---|--|--|
| 1       | V <sub>CC</sub> | Power supply                            |  |  |
| 2       | GND             | Ground of the chip.                     |  |  |
| 3       | GATE            | Gate driver for external MOSFET switch. |  |  |
| 4       | ACTL            | Analog dimming control.                 |  |  |
| 5       | VC              | Compensation pin.                       |  |  |
| 6       | SENSE           | LED current sense input pin.            |  |  |

### BLOCK DIAGRAM





## Preliminary LINEAR INTEGRATED CIRCUIT

#### **ABSOLUTE MAXIMUM RATING**

| PARAMETER                                | SYMBOL             | RATINGS    | UNIT |
|--|--------------------|------------|------|
| Supply Input Voltage                     | V <sub>cc</sub>    | 40         | V    |
| GATE Voltage                             | V <sub>GATE</sub>  | 14         | V    |
| ACTL Voltage (Note 2)                    | V <sub>ACTL</sub>  | 8          | V    |
| VC Voltage                               | V <sub>VC</sub>    | 6          | V    |
| SENSE Voltage                            | V <sub>SENSE</sub> | -0.3 ~ 6   | V    |
| Power Dissipation (T <sub>A</sub> =25°C) | PD                 | 0.392      | W    |
| Junction Temperature                     | TJ                 | +150       | °C   |
| Storage Temperature                      | T <sub>STG</sub>   | -65 ~ +150 | °C   |

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

### **RECOMMENDED OPERATING CONDITIONS (Note)**

| PARAMETER                  | SYMBOL          | RATINGS    | UNIT |
|----------------------------|-----------------|------------|------|
| Supply Input Voltage       | V <sub>CC</sub> | 17 ~ 32    | V    |
| Junction Temperature Range | TJ              | -40 ~ +125 | °C   |

Note: The device is not guaranteed to function outside its operating conditions.

#### THERMAL DATA

| PARAMETER           | SYMBOL          | RATING | UNIT |
|---------------------|-----------------|--------|------|
| Junction to Ambient | θ <sub>JA</sub> | 255    | °C/W |

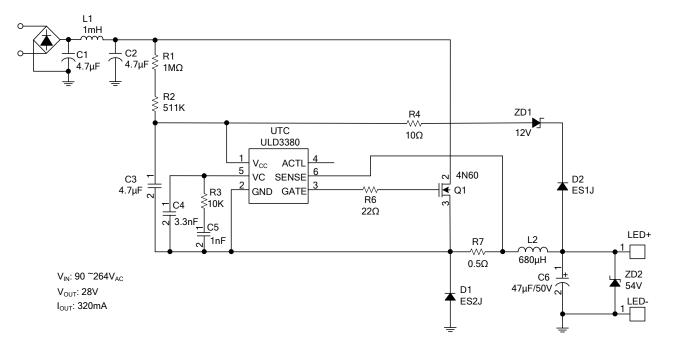
#### ELECTRICAL CHARACTERISTICS (V<sub>CC</sub>=24V<sub>DC</sub>, T<sub>A</sub>=25°C, unless otherwise specified)

| PARAMETER  | SYMBOL               | TEST CONDITIONS                                       | MIN  | TYP  | MAX  | UNIT |
|--|----------------------|---|------|------|------|------|
| Input Start-Up Voltage   | V <sub>ST</sub>      |   | 15   | 17   | 19   | V    |
| Minimum Operation Voltage After Star t-Up                      | V <sub>IN(MIN)</sub> |   | 6.0  | 7.5  | 9.0  | V    |
| Input Quiescent Current  | l <sub>QC</sub>      | After Start-Up, V <sub>CC</sub> =24V                  |      | 1.65 | 5.0  | mA   |
| Maximum Startup Current in V <sub>CC</sub><br>Hiccup Operation | I <sub>ST(MAX)</sub> | Maximum $I_{\text{CC}}$ at low end of $V_{\text{CC}}$ |      | 250  | 300  | μA   |
| Input Shutdown Current   | I <sub>SHDN</sub>    | Before Start-Up, V <sub>CC</sub> =15V                 |      | 0.1  | 5.0  | μA   |
| Over Voltage Protection  | V <sub>OVP</sub>     | VCC Pin   | 32.5 | 35.5 | 36.5 | V    |
| Current Sense Voltage  | V <sub>SENSE</sub>   |   |      | 178  |      | mV   |
| Switching Frequency  | f <sub>sw</sub>      |   | 38   | 47   | 55   | kHz  |
| Oscillator Maximum Duty Cycle                                  | D <sub>MAX</sub>     | V <sub>C</sub> =3V                                    |      |      | 100  | %    |
| Minimum Turn-On Time   | t <sub>ON(MIN)</sub> |   | 300  |      |      | ns   |
| GATE Pin Maximum Voltage                                       | V <sub>GATE</sub>    | No Load at GATE Pin                                   | 11.1 | 12   | 13.1 | V    |
|  | V <sub>GATE_H</sub>  | I <sub>GATE</sub> =-20mA                              | 11   | 12   | 13   | V    |
| GATE Voltage High  |                      | I <sub>GATE</sub> =-100μA                             | 11.1 | 12   | 13.1 | V    |
|  | $V_{GATE_L}$         | I <sub>GATE</sub> =20mA                               | 0.55 | 0.75 | 0.95 | V    |
| GATE Voltage Low   |                      | I <sub>GATE</sub> =100μA                              | 0.3  | 0.5  | 0.7  | V    |
| GATE Drive Rise and Fall Time                                  |                      | 1nF Load at GATE                                      |      | 75   |      | ns   |
| GATE Drive Source and Sink Peak Current                        |                      | 1nF Load at GATE                                      |      | 0.2  |      | Α    |
| ACTL LED Dimming   |                      |   |      |      |      |      |
| Analog Dimming ACTL Pin Input Current                          | I <sub>ACTL</sub>    |   |      |      | 25   | μA   |
| Analog Dimming Range   |                      |   | 0    |      | 1.3  | V    |
| Analog Dimming High Level                                      |                      |   |      | 1.2  | 1.3  | V    |
| Threshold Voltage Low Level                                    |                      |   | 0    | 0.1  |      | V    |
| VC Threshold for PWM Switch Off                                | V <sub>VC</sub>      |   | 1.1  | 1.25 | 1.4  | V    |
| Thermal Protection   |                      |   |      |      |      |      |
| Thermal Shutdown Temperature                                   | T <sub>SD</sub>      |   |      | 150  |      | °C   |



<sup>2.</sup> If the ACTL pin is connected with a serial  $1M\Omega$  resistor, the maximum voltage can go up to 36V.

### TYPICAL APPLICATION CIRCUIT



Preliminary

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