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VS-MBRS190-M3, VS-MBRS1100-M3

Vishay Semiconductors

High Performance Schottky Rectifier, 1.0 A



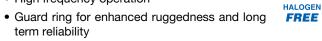
o——∣	o
Cathode	Anode

SMB

PRODUCT SUMMARY			
Package	SMB		
I _{F(AV)}	1 A		
V _R	90 V, 100 V		
V _F at I _F	0.78 V		
I _{RM}	1 mA at 125 °C		
T _J max.	175 °C		
Diode variation	Single die		
E _{AS}	1.0 mJ		

FEATURES

- · Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-MBRS190-M3, VS-MBRS1100-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS			
I _{F(AV)}	Rectangular waveform	1.0	А		
V _{RRM}		90, 100	V		
I _{FSM}	t _p = 5 μs sine	870	А		
V _F	1.0 A _{pk} , T _J = 125 °C	0.63	V		
TJ	Range	-55 to +175	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-MBRS190-M3	VS-MBRS1100-M3	UNITS
Maximum DC reverse voltage	V _R	90	100	V
Maximum working peak reverse voltage	V _{RWM}	90	100	v

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 147 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	870	A
	IFSM	10 ms sine or 6 ms rect. pulse		50	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 0.5 \text{ A}, L = 8 \text{ mH}$ 1.0		mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s0.5Frequency limited by T _J maximum V _A = 1.5 x V _R typical0.5		А	

RoHS

COMPLIANT

FREE



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V (1)	1 A	T _J = 25 °C	0.78	V
See fig. 1	V _{FM} ⁽¹⁾	VFM (1) TA	T _J = 125 °C	0.62	Ň
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.5	mA
See fig. 2		$T_{J} = 125 \text{ °C}$		1.0	
Typical junction capacitance	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		42	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +175	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	36	°C 444
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
A nove vize at a weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	19/	10

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB

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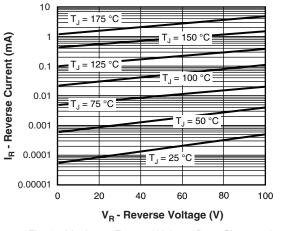
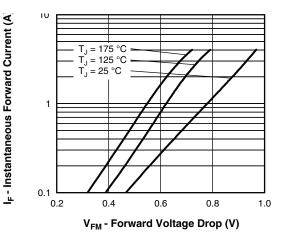
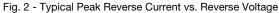


Fig. 1 - Maximum Forward Voltage Drop Characteristics





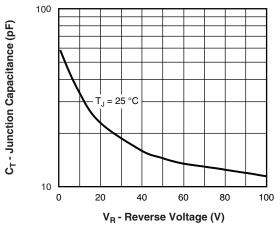


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

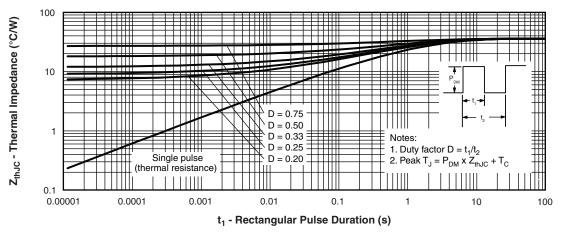
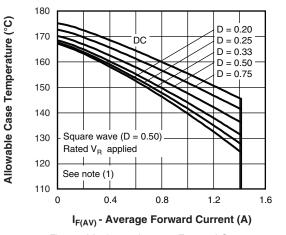


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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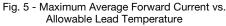
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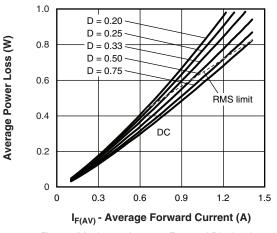
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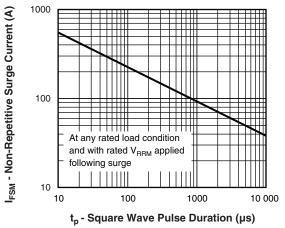


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

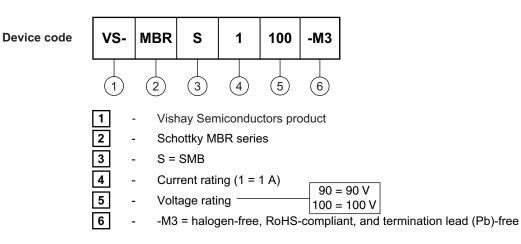
(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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ORDERING INFORMATION TABLE



ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-MBRS190-M3/5BT	5BT	3200	13" diameter plastic tape and reel		
VS-MBRS1100-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS			
Dimensions www.vishay.com/doc?95401			
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?95516		

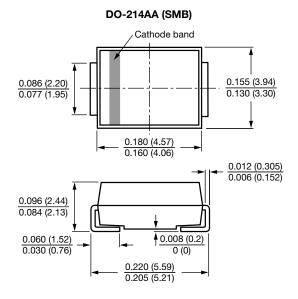


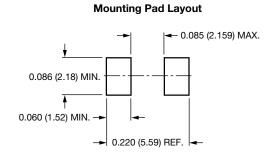
Outline Dimensions

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SMB

DIMENSIONS in inches (millimeters)







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