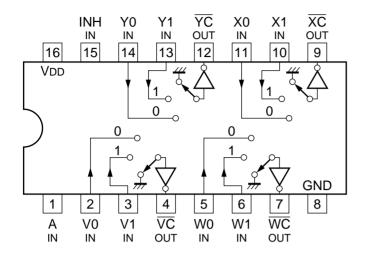
C-MOS 2-LINE-TO-1-LINE INVERTED DATA SELECTOR/MULTIPLEXER -TOP VIEW-



_2	VQ	
3	v_1 \sqrt{c}	4
5	wo 💥	
6	$\overline{W1}$ \overline{WC}	7
11	xo 💥	
10	x_1 $\overline{x_C}$	9
14	YQ 7/7	
13	Y1	12
	<i>}</i>	
	A INH	
	1 15	•

0; LOW LEVEL 1; HIGH LEVEL

X; DON'T CARE

CON	T. IN	ON
INH	Α	CHANNEL
0	0	0
0	1	1
1	Χ	GND

NOTE:

TYPE	Vdd
74AC/HC	+2 to +6V
74ACT/HCT	+5V