



PJU50N10 / PJD50N10 / PJP50N10 / PJF50N10

100V N-Channel Enhancement Mode MOSFET

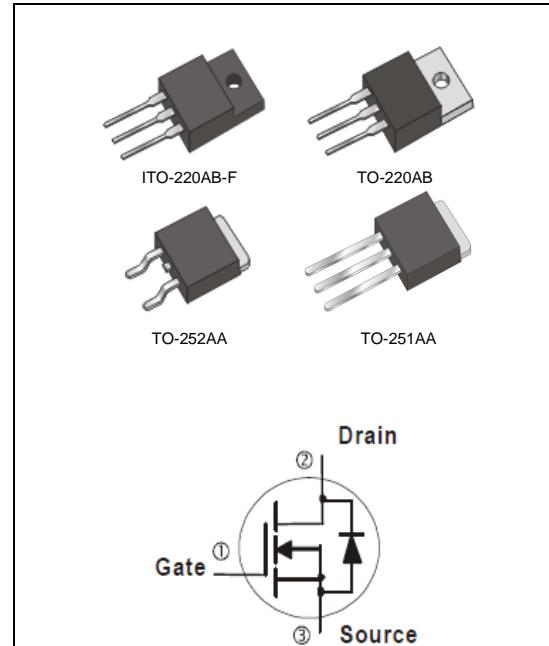
Voltage **100 V** **Current** **50 A**

Features

- $R_{DS(ON)}$, $V_{GS} @ 10V, I_D @ 20A < 25m\Omega$
- $R_{DS(ON)}$, $V_{GS} @ 6V, I_D @ 20A < 28m\Omega$
- High switching speed
- Improved dv/dt capability
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std.
(Halogen Free)

Mechanical Data

- Case : TO-251AA, TO-252AA, TO-220AB, ITO-220AB-F Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- TO-251AA Approx. Weight : 0.0104 ounces, 0.297grams
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- TO-220AB Approx. Weight : 0.067 ounces, 1.9 grams
- ITO-220AB-F Approx. Weight : 0.068 ounces, 2 grams



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TO-251AA	TO-220AB	ITO-220AB-F	TO-252AA	UNITS
Drain-Source Voltage	V_{DS}		100			V
Gate-Source Voltage	V_{GS}		± 20			V
Continuous Drain Current $T_C=25^\circ C$	I_D		50			A
			32			
Pulsed Drain Current ^(Note 1)	I_{DM}		200			
Power Dissipation $T_C=25^\circ C$	P_D	104	125	45	104	W
		24	50	18	42	
Continuous Drain Current $T_A=25^\circ C$	I_D		6.3			A
			5.1			
Power Dissipation $T_A=25^\circ C$	P_D	1.1	2.0	1.0	1.1	W
Power Dissipation $T_A=70^\circ C$		0.7	1.3	0.7	0.7	
Single Pulse Avalanche Energy ^(Note 6)	E_{AS}		90			mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}		-55~150			°C
Typical Thermal resistance - Junction to Case - Junction to Ambient	$R_{\theta JC}$	1.2	1	2.8	1.2	°C/W
	$R_{\theta JA}$	110	62.5	120	110	

● Limited only By Maximum Junction Temperature



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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	V_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2	2.6	4	V
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=20A$	-	22	25	$m\Omega$
		$V_{GS}=6V, I_D=20A$	-	23	28	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$	-	-	1.0	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Dynamic <small>(Note 7)</small>						
Total Gate Charge	Q_g	$V_{DS}=50V, I_D=20A,$ $V_{GS}=10V$ <small>(Note 2,3)</small>	-	53	-	nC
Gate-Source Charge	Q_{gs}		-	13	-	
Gate-Drain Charge	Q_{gd}		-	14	-	
Input Capacitance	C_{iss}	$V_{DS}=50V, V_{GS}=0V,$ $f=1.0MHz$	-	3152	-	pF
Output Capacitance	C_{oss}		-	110	-	
Reverse Transfer Capacitance	C_{rss}		-	66	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=50V, I_D=20A,$ $V_{GS}=10V, R_G=3\Omega$ <small>(Note 2,3)</small>	-	18	-	ns
Turn-On Rise Time	t_r		-	45	-	
Turn-Off Delay Time	$t_{d(off)}$		-	53	-	
Turn-Off Fall Time	t_f		-	15	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	50	A
Diode Forward Voltage	V_{SD}	$I_S=1A, V_{GS}=0V$	-	0.7	1.2	V

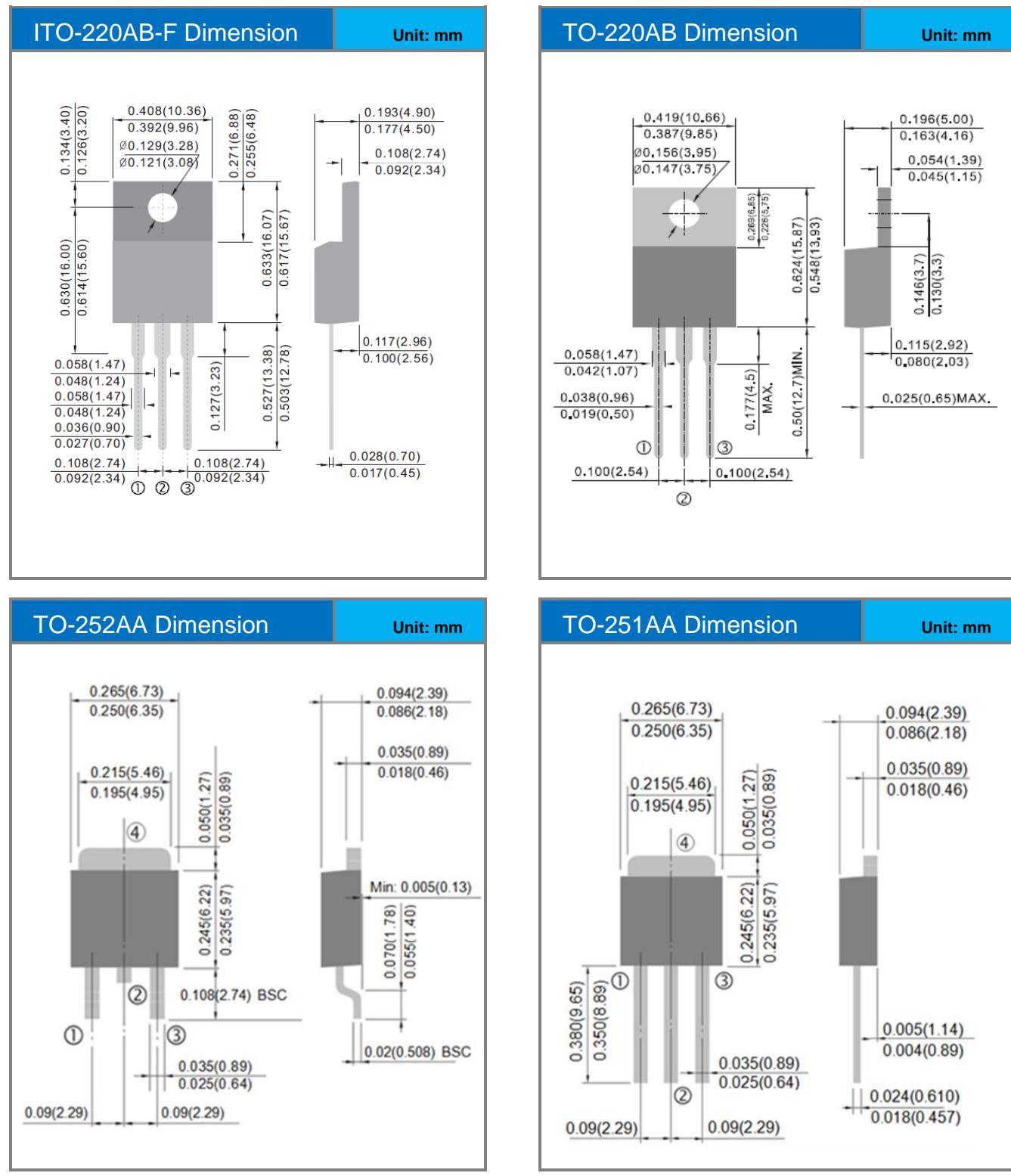
NOTES :

1. Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature $T_J(MAX)=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^\circ C$.
4. The maximum current rating is package limited.
5. R_{QJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
6. The test condition is $L=0.1mH, I_{AS}=37A, V_{DD}=25V, V_{GS}=10V, R_G=25ohm$, Starting $T_J=25^\circ C$
7. Guaranteed by design, not subject to production testing.



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Packaging Information



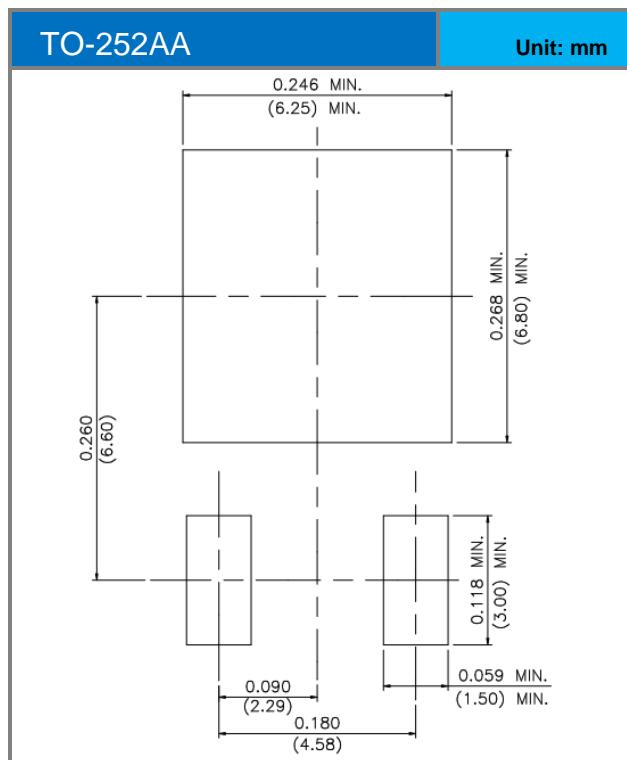


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PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJD50N10_L2_00001	TO-252AA	3,000pcs / 13" reel	D50N10	Halogen free
PJU50N10_T0_00001	TO-251AA	80pcs / Tube	U50N10	Halogen free
PJP50N10_T0_00001	TO-220AB	50pcs / Tube	P50N10	Halogen free
PJF50N10_T0_00001	ITO-220AB-F	50pcs / Tube	F50N10	Halogen free

MOUNTING PAD LAYOUT



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