

# LM340/LM78XX Series 3-Terminal Positive Regulators

#### **General Description**

The LM140/LM340A/LM340/LM78XXC monolithic 3-terminal positive voltage regulators employ internal current-limiting, thermal shutdown and safe-area compensation, making them essentially indestructible. If adequate heat sinking is provided, they can deliver over 1.0A output current. They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents

Considerable effort was expended to make the entire series of regulators easy to use and minimize the number of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

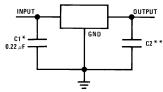
The 5V, 12V, and 15V regulator options are available in the steel TO-3 power package. The LM340A/LM340/LM78XXC series is available in the TO-220 plastic power package, and the LM340-5.0 is available in the SOT-223 package, as well as the LM340-5.0 and LM340-12 in the surface-mount TO-263 package.

#### **Features**

- Complete specifications at 1A load
- Output voltage tolerances of ±2% at T<sub>j</sub> = 25°C and ±4% over the temperature range (LM340A)
- $\blacksquare$  Line regulation of 0.01% of  $V_{OUT}\!/\!V$  of  $\Delta V_{IN}$  at 1A load (LM340A)
- Load regulation of 0.3% of V<sub>OUT</sub>/A (LM340A)
- Internal thermal overload protection
- Internal short-circuit current limit
- Output transistor safe area protection
- P<sup>+</sup> Product Enhancement tested

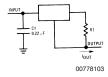
#### **Typical Applications**

#### **Fixed Output Regulator**



00778101

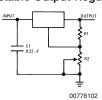
#### **Current Regulator**



$$I_{OUT} = \frac{V2-3}{R1} + I_{Q}$$

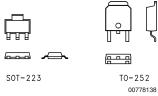
 $\Delta I_Q = 1.3$  mA over line and load changes.

#### **Adjustable Output Regulator**



$$\begin{split} V_{OUT} &= 5V + (5V/R1 + I_Q) \; R2 \; 5V/R1 > 3 \; I_Q, \\ &\text{load regulation } (L_r) \approx [(R1 + R2)/R1] \; (L_r \; \text{of LM340-5}). \end{split}$$

## Comparison between SOT-223 and D-Pak (TO-252) Packages



Scale 1:1

<sup>\*</sup>Required if the regulator is located far from the power supply filter.

<sup>\*\*\*</sup>Although no output capacitor is needed for stability, it does help transient response. (If needed, use 0.1  $\mu$ F, ceramic disc).

4-Lead

Die

SOT-223

Unpackaged

#### **Ordering Information** Package Temperature **Part Number Transport Media** NSC **Packaging Marking** Range Drawing 3-Lead TO-3 0°C to +125°C LM340K-5.0 LM340K-5.0 7805P+ 50 Per Bag K02A LM340K-12 LM340K 12 7812P+ 50 Per Bag LM340K-15 LM340K 15 7815P+ 50 Per Bag 0°C to +125°C 3-lead TO-220 45 Units/Rail LM340AT-5.0 LM340AT 5.0 P+ T03B LM340T-5.0 LM340T5 7805 P+ 45 Units/Rail 45 Units/Rail LM340T-12 LM340T12 7812 P+ LM340T-15 LM340T15 7815 P+ 45 Units/Rail 3-Lead TO-263 0°C to +125°C 45 Units/Rail TS3B LM340S-5.0 LM340S-5.0 P+ LM340SX-5.0 500 Units Tape and Reel 45 Units/Rail LM340S-12 LM340S-12 P+ 500 Units Tape and Reel LM340SX-12 45 Units/Rail LM340AS-5.0

LM340ASX-5.0

LM340MP-5.0

LM340MPX-5.0

LM140KG-5 MD8

LM140KG-12 MD8

LM140KG-15 MD8

0°C to +125°C

-55°C to 125°C

LM340AS-5.0 P+

N00A

500 Units Tape and Reel

1k Units Tape and Reel

2k Units Tape and Reel

221 Per Waffle Pack

221 Per Waffle Pack

221 Per Waffle Pack

MA04A

DL069089

DL059093

DL059093

#### **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

(Note 5)

DC Input Voltage

All Devices except

LM7824/LM7824C 35V LM7824/LM7824C 40V

Internal Power Dissipation (Note 2) Internally Limited Maximum Junction Temperature 150°C

Storage Temperature Range -65°C to +150°C

Lead Temperature (Soldering, 10 sec.)

TO-3 Package (K) 300°C

TO-220 Package (T), TO-263

Package (S) 230°C ESD Susceptibility (Note 3) 2 kV

#### **Operating Conditions** (Note 1)

Temperature Range (T<sub>A</sub>) (Note 2)

#### **LM340A Electrical Characteristics**

 $I_{OUT} = 1A, -55^{\circ}C \leq T_{J} \leq +150^{\circ}C \text{ (LM140A), or } 0^{\circ}C \leq T_{J} \leq +125^{\circ}C \text{ (LM340A) unless otherwise specified (Note 4)}$ 

	Output Voltage 5V 12V 15V												
Symbol	Input Volta	age (unless o	therwise noted)		10V			19V			Units		
	Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25^{\circ}C$		4.9	5	5.1	11.75	12	12.25	14.7	15	15.3	V
		P <sub>D</sub> ≤ 15W, 5	$5 \text{ mA} \le I_{O} \le 1 \text{A}$	4.8		5.2	11.5		12.5	14.4		15.6	V
		$V_{MIN} \leq V_{IN} \leq$	(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.8	≤ V <sub>IN</sub>	≤ 27)	(17.9	V			
$\Delta V_{O}$	Line Regulation	$I_{\rm O} = 500  \text{m/s}$	1			10			18			22	mV
		$\Delta V_{IN}$		(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.8	≤ V <sub>IN</sub>	≤ 27)	(17.9	≤ V <sub>IN</sub>	≤ 30)	V
		$T_J = 25^{\circ}C$			3	10		4	18		4	22	mV
		$\Delta V_{IN}$		(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.5	≤ V <sub>IN</sub>	≤ 27)	(17.5	≤ V <sub>IN</sub>	≤ 30)	V
		$T_J = 25^{\circ}C$				4			9			10	mV
		Over Tempe	erature			12			30			30	mV
		$\Delta V_{IN}$		(8 ≤	V <sub>IN</sub> ≤	≤ 12)	(16 ≤	V <sub>IN</sub> :	≤ 22)	(20 ≤	V <sub>IN</sub> ≤	26)	V
$\Delta V_{O}$	Load Regulation	$T_J = 25^{\circ}C$	$5 \text{ mA} \leq I_{O} \leq 1.5 \text{A}$		10	25		12	32		12	35	mV
			$250~\text{mA} \leq I_{O} \leq 750$			15			19			21	mV
			mA										
		Over Tempe	erature,			25			60			75	mV
		5 mA ≤ I <sub>O</sub> ≤	1A										
$I_Q$	Quiescent	$T_J = 25^{\circ}C$				6			6			6	mA
	Current												
		Over Tempe				6.5	6.5				0.5	6.5	mA
$\Delta I_{Q}$	Quiescent	5 mA ≤ I <sub>O</sub> ≤	1A		0.5		0.5					mA	
	Current	T 05°0 I	4.4										
	Change	$T_{J} = 25^{\circ}C, I_{J}$	_	/7.5		0.8	0.8			(47.0	mA		
		$V_{MIN} \le V_{IN} \le$		(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.8	≤ V <sub>IN</sub>	≤ 27)	(17.9	≤ V <sub>IN</sub>		V
		$I_{O} = 500 \text{ m/s}$		(0)		0.8	45		0.8	(47.0		0.8	mA
	0	$V_{MIN} \le V_{IN} \le$		(8 ≤	V <sub>IN</sub> ≤	≤ 25)	$(15 \le V_{IN} \le 30)$			(17.9	≤ 30)	V	
$V_N$	Output Noise Voltage	$I_A = 25 \text{ C},$	10 Hz ≤ f ≤ 100 kHz		40			75			90		μV
	Ripple Rejection	T = 25°C f	= 120 Hz, I <sub>O</sub> = 1A	68	80		61	72		60	70		dB
$rac{\Delta V_{ extsf{IN}}}{\Delta V_{ extsf{OUT}}}$	Nipple Nejection	_	$z, I_O = 500 \text{ mA},$	68	80		61	12		60	70		dB
4001		1											ub
		Over Temperature, $V_{MIN} \leq V_{IN} \leq V_{MAX}$		(8 <	V <sub>IN</sub> ≤	< 18)	(15 ≤	: V	< 25)	(18	5 ≤ V <sub>I</sub>	<	V
		MIN - VIN -	- ▼MAX	(0 3	VIN -	,	(15 =	- VIN -	_ 20)		$3 \ge v_1$ 28.5)	N -	•
R <sub>o</sub>	Dropout Voltage	$T_{J} = 25^{\circ}C, I_{J}$	o = 1A		2.0			2.0			2.0		V
O	Output	f = 1 kHz	<u> </u>		8			18			19		mΩ
	Resistance				-								
	1	1		-									

 $\begin{tabular}{ll} LM340A Electrical Characteristics & (Continued) \\ I_{OUT} = 1A, -55^{\circ}C \le T_{J} \le +150^{\circ}C & (LM140A), or 0^{\circ}C \le T_{J} \le +125^{\circ}C & (LM340A) & unless otherwise specified (Note 4) \\ \end{tabular}$ 

	Output Voltage Input Voltage (unless otherwise noted)			5V		12V						
Symbol				10V			19V			23V		
	Parameter	Conditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
	Short-Circuit	$T_J = 25^{\circ}C$		2.1			1.5			1.2		Α
	Current											
	Peak Output	$T_J = 25^{\circ}C$		2.4			2.4			2.4		Α
	Current											
	Average TC of	Min, $T_J = 0^{\circ}C$ , $I_O = 5$ mA		-0.6			-1.5			-1.8		mV/°C
	V <sub>O</sub>											
V <sub>IN</sub>	Input Voltage	$T_J = 25^{\circ}C$										
	Required to		7.5			14.5			17.5			V
	Maintain											
	Line Regulation											

### LM140 Electrical Characteristics (Note 4)

 $-55^{\circ}C \leq T_{J} \leq +150^{\circ}C$  unless otherwise specified

	Output Voltage				5V	'		12V					
Symbol	Input Volta	ge (unless oth	nerwise noted)		10\	/		19V		23V U			nits
	Parameter	C	Conditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	$T_J = 25^{\circ}C$ , 5 mA $\leq I_O \leq 1$ A			5	5.2	11.5	12	12.5	14.4	15	15.6	V
		P <sub>D</sub> ≤ 15W, 5	$mA \le I_O \le 1A$	4.75		5.25	11.4		12.6	14.25		15.75	V
		$V_{MIN} \leq V_{IN} \leq Y_{IN}$	$V_{MAX}$	$(8 \le V_{IN} \le 20)$			$(15.5 \le V_{IN} \le 27)$			(18.5 ≤ V <sub>IN</sub> ≤			V
											30)		
$\Delta V_{O}$	Line Regulation	$I_{O} = 500 \text{ mA}$	$T_J = 25^{\circ}C$		3	50		4	120		4	150	mV
			$\Delta V_{IN}$	(7 ≤	≤ V <sub>IN</sub>	≤ 25)	(14.5	$\leq V_{IN}$	≤ 30)	(17.	.5 ≤ V	' <sub>IN</sub> ≤	V
											30)		
		$-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$				50			120			150	mV
			$\Delta V_{IN}$	(8 ≤	≤ V <sub>IN</sub>	≤ 20)	(15 ≤	≤ V <sub>IN</sub> ≤	≤ 27)	(18.	.5 ≤ V	' <sub>IN</sub> ≤	V
											30)		
		I <sub>O</sub> ≤ 1A	$T_J = 25^{\circ}C$		50			120			150	mV	
		$\Delta V_{IN}$		(7.5	$\leq V_{II}$	<sub>√</sub> ≤ 20)	(14.6	$\leq V_{IN}$	≤ 27)	(17.	.7 ≤ V	' <sub>IN</sub> ≤	V
											30)		
			$-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$			25			60			75	mV
			$\Delta V_{IN}$	(8 ≤		≤ 12)	(16 ≤	≤ V <sub>IN</sub> ≤	<u>(22)</u>	(20 ≤	≤ V <sub>IN</sub> :	≤ 26)	V
$\Delta V_{O}$	Load Regulation	$T_J = 25^{\circ}C$	5 mA ≤ I <sub>O</sub> ≤ 1.5A		10	50		12	120		12	150	mV
			$250 \text{ mA} \le I_P \le 750$			25			60			75	mV
			mA										L
		$-55^{\circ}C \leq T_{J} \leq$				50			120			150	mV
		5 mA ≤ I <sub>O</sub> ≤ 1											
$I_Q$	Quiescent Current	I <sub>O</sub> ≤ 1A	$T_J = 25^{\circ}C$			6			6			6	mA
			$-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$			7			7			7	mA
$\Delta I_Q$	Quiescent Current	5 mA ≤ I <sub>O</sub> ≤ 1			0.5			0.5			0.5		mA
	Change	$T_J = 25^{\circ}C, I_O$				0.8			0.8			8.0	mA
		$V_{MIN} \leq V_{IN} \leq Y_{IN}$	$V_{MAX}$	(8 ≤	≤ V <sub>IN</sub>	≤ 20)	(15 ≤	≤ V <sub>IN</sub> ≤	≤ 27)	(18.	.5 ≤ V	' <sub>IN</sub> ≤	V
											30)		ļ .
			$_{O} = 500 \text{ mA}, -55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$			8.0			8.0		_	0.8	mA
		$V_{MIN} \leq V_{IN} \leq Y_{IN}$	$V_{MAX}$	(8 ≤	≤ V <sub>IN</sub>	≤ 25)	(15 s	≤ V <sub>IN</sub> ≤	≦ 30)	(18.	.5 ≤ V	' <sub>IN</sub> ≤	V
						30)		ļ.,					
$V_N$	Output Noise Voltage	$I_A = 25^{\circ}C, 10$	) Hz ≤ f ≤ 100 kHz		40			75			90		μV

#### LM140 Electrical Characteristics (Note 4) (Continued)

 $-55^{\circ}C \leq T_{J} \leq +150^{\circ}C$  unless otherwise specified

		Output Voltage			5V			12V			15V		
Symbol	Input Volta	ge (unless oth	erwise noted)		10V			19V			23V	U	nits
	Parameter	Conditions			Тур	Max	Min	Тур	Max	Min	Тур	Max	
ΔV <sub>IN</sub>	Ripple Rejection		$I_{O} \le 1A$ , $T_{J} = 25^{\circ}C$	68	80		61	72		60	70		dB
ΔV <sub>OUT</sub>			or										
		f = 120 Hz	I <sub>O</sub> ≤ 500 mA,	68			61			60			dB
			–55°C ≤ T <sub>J</sub> ≤+150°C										
		$V_{MIN} \le V_{IN} \le V_{IN}$	/ <sub>MAX</sub>	(8 ≤	≤ V <sub>IN</sub> ≤	≤ 18)	$(15 \le V_{IN} \le 25)$			(18.5 ≤ V <sub>IN</sub> ≤			V
											28.5)		
Ro	Dropout Voltage	$T_J = 25^{\circ}C, I_O = 1A$			2.0			2.0			2.0		V
	Output Resistance	f = 1 kHz			8			18		19			mΩ
	Short-Circuit	$T_J = 25^{\circ}C$			2.1			1.5			1.2		Α
	Current												
	Peak Output	$T_J = 25^{\circ}C$			2.4		2.4			2.4			Α
	Current												
	Average TC of	$0^{\circ}C \leq T_{J} \leq +1$	$50^{\circ}$ C, $I_{O} = 5 \text{ mA}$		-0.6			-1.5			-1.8	m	V/°C
	V <sub>OUT</sub>												
V <sub>IN</sub>	Input Voltage	$T_J = 25^{\circ}C, I_O$	≤ 1A										
	Required to			7.5			14.6			17.7			V
	Maintain												
	Line Regulation												

### LM340/LM78XXC Electrical Characteristics (Note 4)

 $0^{\circ}C \le T_{J} \le +125^{\circ}C$  unless otherwise specified

	Output Voltage				5V			12V					
Symbol	Input Voltag	e (unless othe	rwise noted)	10V			19V			23V			Units
	Parameter	Co	onditions	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	
Vo	Output Voltage	$T_{J} = 25^{\circ}C, 5 \text{ mA} \le I_{O} \le 1A$			5	5.2	11.5	12	12.5	14.4	15	15.6	V
		P <sub>D</sub> ≤ 15W, 5	$mA \le I_O \le 1A$	4.75		5.25	11.4		12.6	14.25		15.75	V
		$V_{MIN} \le V_{IN} \le V_{MAX}$		(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.	5 ≤ V 27)	ı <sub>N</sub> ≤	(17.5	V		
$\Delta V_{O}$	Line Regulation	I <sub>O</sub> = 500 mA	$T_J = 25^{\circ}C$		3	50		4	120		4	150	mV
			$\Delta V_{IN}$	(7 ≤	V <sub>IN</sub> ≤	25)	(14.	.5 ≤ V	ı <sub>N</sub> ≤	(17.5	$\leq V_{IN}$	ı ≤ 30)	V
								30)					
			$0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$		50				120			150	mV
		$\Delta V_{IN}$		(8 ≤	V <sub>IN</sub> ≤	20)	(15 ≤	≤ V <sub>IN</sub> ≤	≤ 27)	(18.5	$\leq V_{IN}$	<sub>1</sub> ≤ 30)	V
	$I_O \le 1A$ $T_J = 25^{\circ}C$		$T_J = 25^{\circ}C$			50	120					150	mV
			$\Delta V_{IN}$	(7.5	≤ V <sub>IN</sub>	≤ 20)	(14.6 ≤ V <sub>IN</sub> ≤			$(17.7 \le V_{IN} \le 30)$			V
							27)						
			$0^{\circ}\text{C} \leq \text{T}_{\text{J}} \leq +125^{\circ}\text{C}$			25	60			75			mV
			$\Delta V_{IN}$	(8 ≤	V <sub>IN</sub> ≤	12)	$(16 \le V_{IN} \le 22)$			$(20 \le V_{IN} \le 26)$			V
$\Delta V_{O}$	Load Regulation	$T_J = 25^{\circ}C$	5 mA ≤ I <sub>O</sub> ≤ 1.5A		10	50		12	120		12	150	mV
			250 mA ≤ I <sub>O</sub> ≤ 750 n	nΑ		25			60			75	mV
	$5 \text{ mA} \le I_O \le 1 \text{A}, \ 0^{\circ}\text{C} \le T_J \le +125^{\circ}\text{C}$		A, $0^{\circ}C \leq T_{J} \leq$			50			120			150	mV
I <sub>Q</sub>	Quiescent Current	I <sub>O</sub> ≤ 1A	$T_J = 25^{\circ}C$			8			8			8	mA
			$0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$			8.5			8.5			8.5	mA
$\Delta I_Q$	Quiescent Current	5 mA ≤ I <sub>O</sub> ≤ 1	A		0.5			0.5			0.5		mA
	Change	$T_J = 25^{\circ}C, I_O$	≤ 1A			1.0			1.0			1.0	mA

#### LM340/LM78XXC Electrical Characteristics (Note 4) (Continued)

 $0^{\circ}C \leq T_{J} \leq +125^{\circ}C$  unless otherwise specified

	Output Voltage				5V		12V			15V			
Symbol	Input Voltage	e (unless othe	erwise noted)		10V			19V			23V		Units
	Parameter	Conditions			Тур	Max	Min	Тур	Max	Min	Тур	Max	
		V <sub>MIN</sub> ≤ V <sub>IN</sub> ≤	$V_{MIN} \le V_{IN} \le V_{MAX}$			≤ 20)	(14.8 ≤ V <sub>IN</sub> ≤ 27)			$(17.9 \le V_{IN} \le 30)$			V
		I <sub>O</sub> ≤ 500 mA,	$I_{O} \le 500 \text{ mA}, \ 0^{\circ}\text{C} \le T_{J} \le +125^{\circ}\text{C}$			1.0	1.0			1.0			mA
		$V_{MIN} \le V_{IN} \le V_{MAX}$			V <sub>IN</sub> ≤	25)	(14.	.5 ≤ V 30)	<sub>IN</sub> ≤	(17.5	≤ V <sub>IN</sub>	≤ 30)	V
V <sub>N</sub>	Output Noise Voltage	$T_A = 25^{\circ}C, 10^{\circ}$	) Hz ≤ f ≤ 100 kHz		40			75			90		μV
$\frac{\Delta V_{\text{IN}}}{\Delta V_{\text{OUT}}}$	Ripple Rejection		$I_{O} \le 1A, T_{J} = 25^{\circ}C$	62	80		55	72		54	70		dB
		f = 120 Hz	or $I_O \le 500 \text{ mA}$ , $0^{\circ}\text{C} \le T_J \le +125^{\circ}\text{C}$	62			55			54			dB
		$V_{MIN} \le V_{IN} \le$	V <sub>MAX</sub>	(8 ≤	V <sub>IN</sub> ≤	18)	(15 ≤	≤ V <sub>IN</sub> ≤	≤ 25)	(18	.5 ≤ V 28.5)		V
R <sub>o</sub>	Dropout Voltage	$T_J = 25^{\circ}C, I_O$	= 1A		2.0			2.0			2.0		V
	Output Resistance	f = 1 kHz			8			18			19		mΩ
	Short-Circuit Current	$T_J = 25^{\circ}C$			2.1			1.5			1.2		Α
	Peak Output	$T_J = 25^{\circ}C$			2.4			2.4			2.4		Α
	Current												
	Average TC of V <sub>OUT</sub>				-0.6			-1.5			-1.8		mV/°C
V <sub>IN</sub>	Input Voltage	$T_J = 25^{\circ}C, I_O$	≤ 1A										
	Required to Maintain			7.5			14.6			17.7			V
	Line Regulation												

**Note 1:** Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specifications might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note 2: The maximum allowable power dissipation at any ambient temperature is a function of the maximum junction temperature for operation  $(T_{JMAX} = 125^{\circ}C)$  or 150°C), the junction-to-ambient thermal resistance  $(\theta_{JA})$ , and the ambient temperature  $(T_A)$ .  $P_{DMAX} = (T_{JMAX} - T_A)/\theta_{JA}$ . If this dissipation is exceeded, the die temperature will rise above  $T_{JMAX}$  and the electrical specifications do not apply. If the die temperature rises above 150°C, the device will go into thermal shutdown. For the TO-3 package (K, KC), the junction-to-ambient thermal resistance  $(\theta_{JA})$  is 39°C/W. When using a heatsink,  $\theta_{JA}$  is the sum of the 4°C/W junction-to-case thermal resistance  $(\theta_{JC})$  of the TO-3 package and the case-to-ambient thermal resistance of the heatsink. For the TO-220 package (T),  $\theta_{JA}$  is 54°C/W and  $\theta_{JC}$  is 4°C/W. If SOT-223 is used, the junction-to-ambient thermal resistance is 174°C/W and can be reduced by a heatsink (see Applications Hints on heatsinking).

If the TO-263 package is used, the thermal resistance can be reduced by increasing the PC board copper area thermally connected to the package: Using 0.5 square inches of copper area,  $\theta_{JA}$  is 50°C/W; with 1 square inch of copper area,  $\theta_{JA}$  is 32°C/W.

Note 3: ESD rating is based on the human body model, 100 pF discharged through 1.5 k $\Omega$ .

Note 4: All characteristics are measured with a 0.22  $\mu$ F capacitor from input to ground and a 0.1  $\mu$ F capacitor from output to ground. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ( $t_w \le 10$  ms, duty cycle  $\le 5\%$ ). Output voltage changes due to changes in internal temperature must be taken into account separately.

**Note 5:** A military RETS specification is available on request. At the time of printing, the military RETS specifications for the LM140AK-5.0/883, LM140AK-12/883, and LM140AK-15/883 complied with the min and max limits for the respective versions of the LM140A. At the time of printing, the military RETS specifications for the LM140K-5.0/883, LM140K-12/883, and LM140K-15/883 complied with the min and max limits for the respective versions of the LM140. The LM140H/883, LM140K/883 may also be procured as a Standard Military Drawing.

#### LM7808C Electrical Characteristics

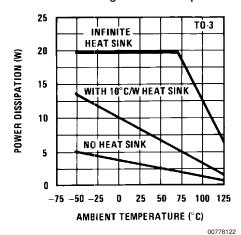
 $0^{\circ}C \leq T_{J} \leq +150^{\circ}C,~V_{I} = 14V,~I_{O} = 500~mA,~C_{I} = 0.33~\mu\text{F},~C_{O} = 0.1~\mu\text{F},~unless~otherwise~specified$ 

Symbol	Paramet	er	Condi		LM78080	0	Units	
						Тур	Max	
V <sub>O</sub>	Output Voltage		$T_J = 25^{\circ}C$			8.0	8.3	V
$\Delta V_{O}$	Line Regulation		$T_{J} = 25^{\circ}C$ $10.5V \le V_{I} \le 25V$			6.0	160	mV
				$11.0V \le V_I \le 17V$		2.0	80	
$\Delta V_{O}$	Load Regulation		$T_J = 25^{\circ}C$	5.0 mA ≤ I <sub>O</sub> ≤ 1.5A		12	160	mV
				250 mA $\leq$ I <sub>O</sub> $\leq$ 750 mA		4.0	80	
Vo	Output Voltage		$11.5V \le V_1 \le 23V$ , $5.0 \text{ mA} \le I_0 \le 1.0A$ , $P \le 15W$				8.4	V
IQ	Quiescent Current		$T_J = 25^{\circ}C$		4.3	8.0	mA	
$\Delta I_Q$	Quiescent	With Line	$11.5V \le V_1 \le 25V$				1.0	mA
	Current Change	With Load	5.0 mA ≤ I <sub>O</sub> ≤ 1.0A				0.5	
V <sub>N</sub>	Noise		$T_A = 25^{\circ}C, 10 \text{ Hz} \le f$	≤ 100 kHz		52		μV
$\Delta V_I / \Delta V_O$	Ripple Rejection		f = 120 Hz, I <sub>O</sub> = 350	$mA, T_J = 25^{\circ}C$	56	72		dB
V <sub>DO</sub>	Dropout Voltage		$I_{\rm O} = 1.0 {\rm A}, T_{\rm J} = 25 {\rm ^{\circ}C}$			2.0		V
R <sub>O</sub>	Output Resistance		f = 1.0 kHz	f = 1.0 kHz				mΩ
I <sub>os</sub>	Output Short Circuit	Current	$T_J = 25^{\circ}C, V_I = 35V$			0.45		Α
I <sub>PK</sub>	Peak Output Curren	it	$T_J = 25^{\circ}C$			2.2		Α
$\Delta V_O/\Delta T$	Average Temperatu	re	I <sub>O</sub> = 5.0 mA			0.8		mV/°C
	Coefficient of Outpu	t Voltage						

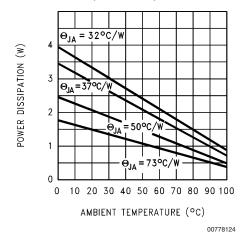
Note 6: All characteristics are measured with a 0.22  $\mu$ F capacitor from input to ground and a 0.1  $\mu$ F capacitor from output to ground. All characteristics except noise voltage and ripple rejection ratio are measured using pulse techniques ( $t_w \le 10$  ms, duty cycle  $\le 5\%$ ). Output voltage changes due to changes in internal temperature must be taken into account separately.

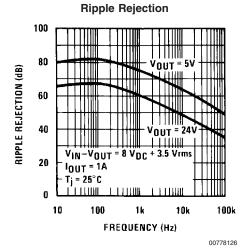
### **Typical Performance Characteristics**

#### **Maximum Average Power Dissipation**

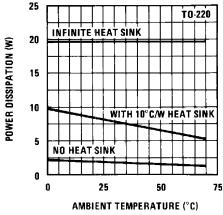


## Maximum Power Dissipation (TO-263) (See Note 2)



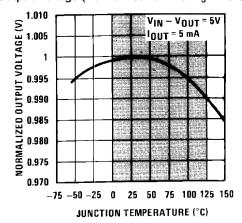


#### **Maximum Average Power Dissipation**



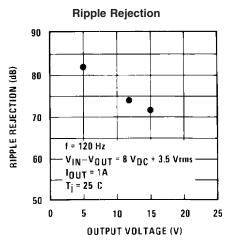
00778123

#### Output Voltage (Normalized to 1V at T<sub>J</sub> = 25°C)



00778125

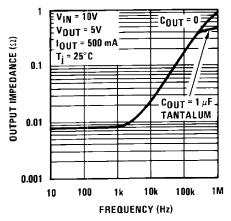
Note: Shaded area refers to LM340A/LM340, LM7805C, LM7812C and LM7815C.



00778127

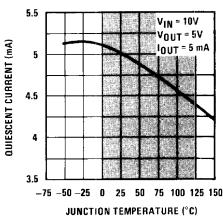
### Typical Performance Characteristics (Continued)

#### **Output Impedance**



00778128

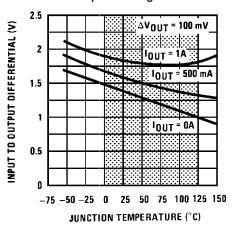
#### **Quiescent Current**



007704

 $\mbox{\bf Note:}$  Shaded area refers to LM340A/LM340, LM7805C, LM7812C and LM7815C.

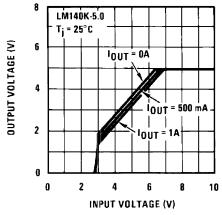
#### **Dropout Voltage**



00778132

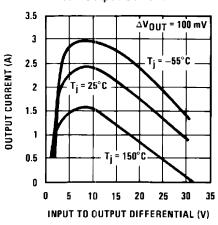
Note: Shaded area refers to LM340A/LM340, LM7805C, LM7812C and LM7815C.

#### **Dropout Characteristics**



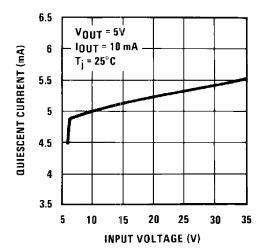
00778129

#### **Peak Output Current**

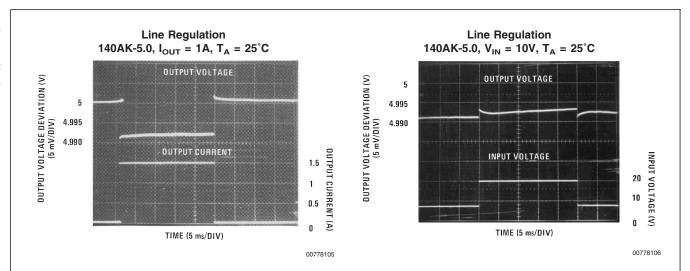


00778131

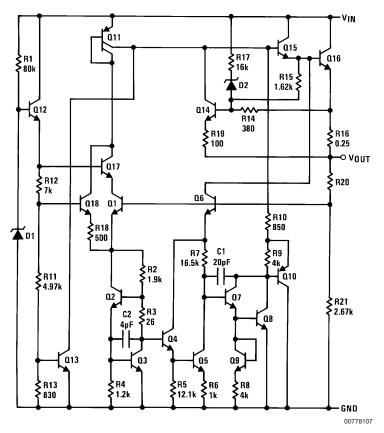
#### Quiescent Current



00778133



### **Equivalent Schematic**



#### **Application Hints**

The LM340/LM78XX series is designed with thermal protection, output short-circuit protection and output transistor safe area protection. However, as with *any* IC regulator, it becomes necessary to take precautions to assure that the regulator is not inadvertently damaged. The following describes possible misapplications and methods to prevent damage to the regulator.

#### SHORTING THE REGULATOR INPUT

When using large capacitors at the output of these regulators, a protection diode connected input to output (*Figure 1*) may be required if the input is shorted to ground. Without the protection diode, an input short will cause the input to rapidly approach ground potential, while the output remains near the initial  $V_{\text{OUT}}$  because of the stored charge in the large output capacitor. The capacitor will then discharge through a large internal input to output diode and parasitic transistors. If the energy released by the capacitor is large enough, this diode, low current metal and the regulator will be destroyed. The fast diode in *Figure 1* will shunt most of the capacitors discharge current around the regulator. Generally no protection diode is required for values of output capacitance  $\leq 10~\mu\text{F}$ .

### RAISING THE OUTPUT VOLTAGE ABOVE THE INPUT VOLTAGE

Since the output of the device does not sink current, forcing the output high can cause damage to internal low current paths in a manner similar to that just described in the "Shorting the Regulator Input" section.

#### **REGULATOR FLOATING GROUND (Figure 2)**

When the ground pin alone becomes disconnected, the output approaches the unregulated input, causing possible damage to other circuits connected to  $V_{\rm OUT}.$  If ground is reconnected with power "ON", damage may also occur to the regulator. This fault is most likely to occur when plugging in regulators or modules with on card regulators into powered up sockets. Power should be turned off first, thermal limit ceases operating, or ground should be connected first if power must be left on.

#### TRANSIENT VOLTAGES

If transients exceed the maximum rated input voltage of the device, or reach more than 0.8V below ground and have sufficient energy, they will damage the regulator. The solution is to use a large input capacitor, a series input breakdown diode, a choke, a transient suppressor or a combination of these.

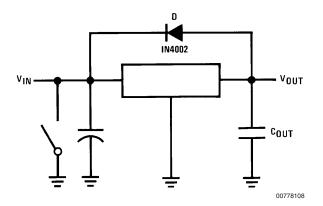


FIGURE 1. Input Short

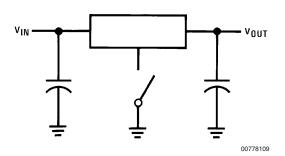


FIGURE 2. Regulator Floating Ground

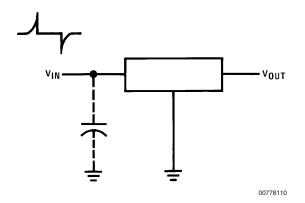


FIGURE 3. Transients

When a value for  $\theta_{(H-A)}$  is found using the equation shown, a heatsink must be selected that has a value that is less than or equal to this number.

 $\theta_{(H-A)}$  is specified numerically by the heatsink manufacturer in this catalog, or shown in a curve that plots temperature rise vs power dissipation for the heatsink.

#### Application Hints (Continued)

#### HEATSINKING TO-263 AND SOT-223 PACKAGE PARTS

Both the TO-263 ("S") and SOT-223 ("MP") packages use a copper plane on the PCB and the PCB itself as a heatsink. To optimize the heat sinking ability of the plane and PCB, solder the tab of the plane.

shows for the TO-263 the measured values of  $\theta_{(J-A)}$  for different copper area sizes using a typical PCB with 1 ounce copper and no solder mask over the copper area used for heatsinking.

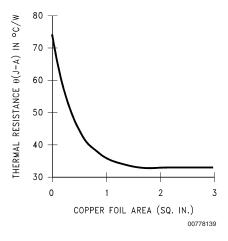


FIGURE 4.  $\theta_{(J-A)}$  vs Copper (1 ounce) Area for the TO-263 Package

As shown in the figure, increasing the copper area beyond 1 square inch produces very little improvement. It should also be observed that the minimum value of  $\theta_{(J-A)}$  for the TO-263 package mounted to a PCB is  $32\,^{\circ}\text{C/W}.$ 

As a design aid, *Figure 5* shows the maximum allowable power dissipation compared to ambient temperature for the TO-263 device (assuming  $\theta_{(J-A)}$  is 35°C/W and the maximum junction temperature is 125°C).

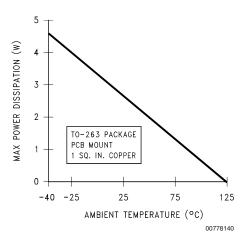


FIGURE 5. Maximum Power Dissipation vs  $T_{\rm AMB}$  for the TO-263 Package

Figures 6, 7 show the information for the SOT-223 package. Figure 6 assumes a  $\theta_{(J-A)}$  of 74°C/W for 1 ounce copper and 51°C/W for 2 ounce copper and a maximum junction temperature of 125°C.

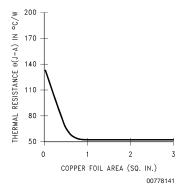


FIGURE 6.  $\theta_{(J-A)}$  vs Copper (2 ounce) Area for the SOT-223 Package

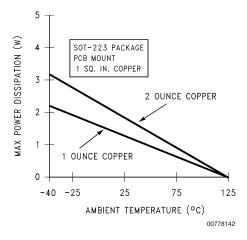
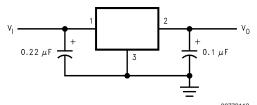


FIGURE 7. Maximum Power Dissipation vs T<sub>AMB</sub> for the SOT-223 Package

Please see AN-1028 for power enhancement techniques to be used with the SOT-223 package.

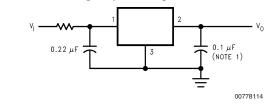
### **Typical Applications**

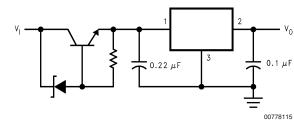
#### **Fixed Output Regulator**



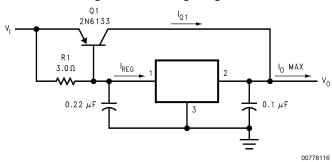
Note: Bypass capacitors are recommended for optimum stability and transient response, and should be located as close as possible to the regulator.

#### **High Input Voltage Circuits**





#### **High Current Voltage Regulator**

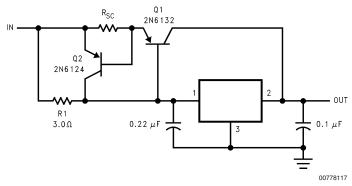


$$\beta(Q1) \ge \frac{I_{O Max}}{I_{REG Max}}$$

$$R1 = \frac{0.9}{I_{REG}} = \frac{\beta(Q1) \ V_{BE(Q1)}}{I_{REG \ Max} (\beta \ + \ 1) - I_{O \ Max}}$$

### Typical Applications (Continued)

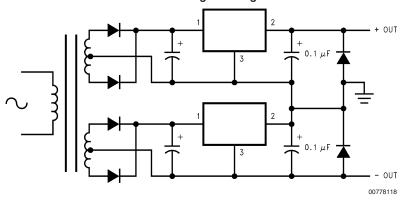
#### High Output Current, Short Circuit Protected



$$R_{SC} = \frac{0.8}{I_{SC}}$$

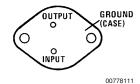
$$R1 = \frac{\beta V_{BE(Q1)}}{I_{REG Max}(\beta + 1) - I_{O Max}}$$

#### **Positive and Negative Regulator**



#### **Connection Diagrams and Ordering Information**

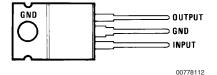
#### TO-3 Metal Can Package (K)



**Bottom View** 

Steel Package Order Numbers: LM140K-5.0 LM140K-12 LM140K-15 LM340K-12 LM340K-15 LM340K-5.0

See Package Number K02A LM140K-5.0/883 LM140K-12/883 LM140K-15/883 See Package Number K02C TO-220 Power Package (T)

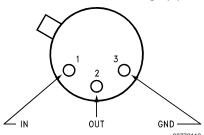


**Top View** 

Plastic Package Order Numbers: LM340AT-5.0 LM340T-5.0 LM340T-12 LM340T-15 LM7805CT LM7812CT

LM7815CT LM7808CT See Package Number T03B

TO-39 Metal Can Package (H)



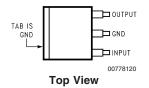
**Top View** 

Metal Can Order Numbers†:

LM140H-5.0/883 LM140H-8.0/883 LM140H-15/883 LM140H-24/883

See Package Number H03A

#### TO-263 Surface-Mount Package (S)



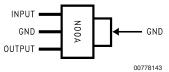


Side View

Surface-Mount Package Order Numbers: LM340S-5.0 LM340S-12

See Package Number TS3B

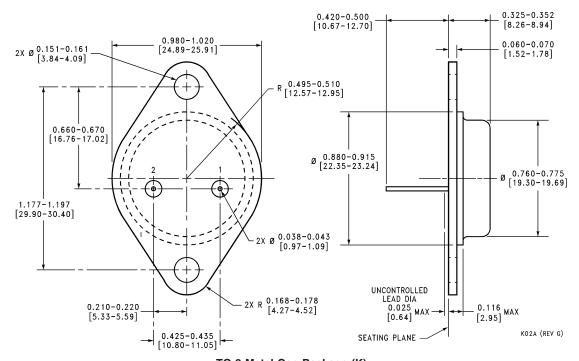
3-Lead SOT-223 (Front View) Order Number LM340MP-5.0 Package Marked NO0A See Package Number MA04A



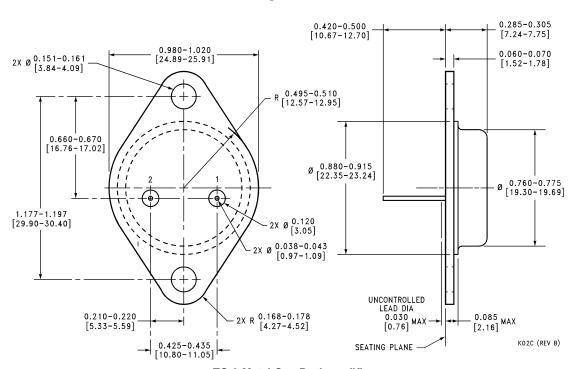
†The specifications for the LM140H/883 devices are not contained in this datasheet. If specifications for these devices are required, contact the National Semiconductor Sales Office/Distributors.

### Physical Dimensions inches (millimeters)

unless otherwise noted

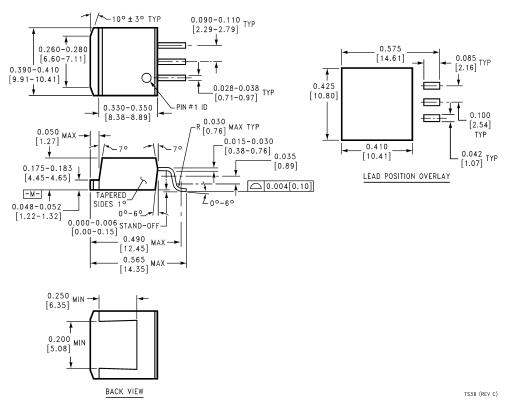


TO-3 Metal Can Package (K)
Order Number LM140K-5.0, LM340K-5.0, LM140K-12, LM340K-12,
LM140K-15, LM340K-15, LM7806CK, LM7808CK, LM7818CK or LM7824CK
NS Package Number K02A



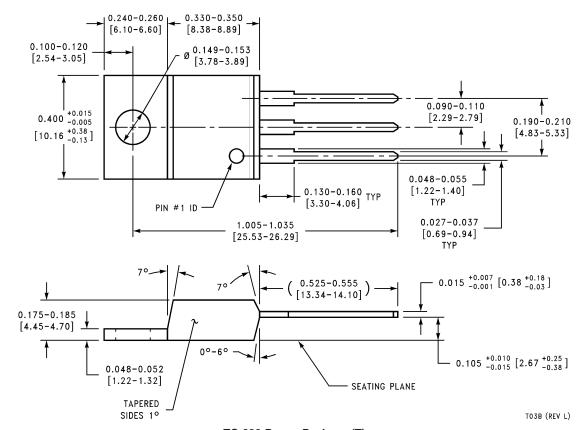
TO-3 Metal Can Package (K)
Mil-Aero Products
Order Number LM140K-5.0/883, LM140K-12/883, or LM140K-15/883
NS Package Number K02C

#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



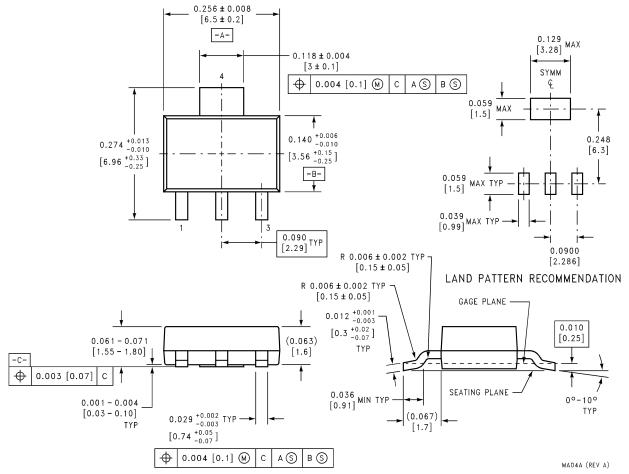
TO-263 Surface-Mount Package (S) Order Number LM340S-5.0 or LM340S-12 NS Package Number TS3B

#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



TO-220 Power Package (T)
Order Number LM340AT/LM340T-5.0, LM340AT/LM340T-12, LM340AT/LM340T-15,
LM7805CT, LM7812CT, LM7815CT, LM7806CT, LM7808CT, LM7818CT or LM7824CT
NS Package Number T03B

#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



3-Lead SOT-223 Package Order Part Number LM340MP-5.0 NS Package Number MA04A

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- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



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