# **MORNSUN®**

# **B XT-1WR2 Series**

1W, FIXED INPUT, ISOLATED & UNREGULATED SINGLE OUTPUT

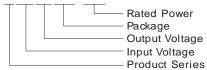




Patent Protected RoHS

## PART NUMBER SYSTEM

B0505XT-1WR2



## **FEATURES**

- Miniature SMD package
- 1500VDC isolation
- Operating temperature range: -40°C~+105°C
- Internal SMD construction
- No external component required
- Industry standard pinout

## **APPLICATIONS**

The B\_XT-1WR2 Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage rang ≤ ±10%;
- 2) 1500VDC input and output isolation;
- 3) Regulated and low ripple noise is not required.

Such as: digital circuits, low frequency analog circuits, and relay drive circuit.

<b>SELECTION </b>	SUIDE								
Model	Input Voltage(VDC)	Output Voltage		Current nA)		(Typ.)	Reflected Ripple	Max.Capacitive	Efficiency (%, Typ.)
	Nominal (Range)	(VDC)	Max.	Min.	@Max. Load	@No Load	Current (mA.Typ.)	Load(µF)	@Max. Load
B0303XT-1WR2	3.3	3.3	303	30	415	25			73
B0305XT-1WR2	(2.97-3.63)	5	200	20	388	25			78
B0503XT-1WR2		3.3	303	30	263				76
B0505XT-1WR2		5	200	20	250				80
B0509XT-1WR2	5	9	111	12	250	20			80
B0512XT-1WR2	(4.5-5.5)	12	84	9	250				80
B0515XT-1WR2		15	67	7	250				80
B0524XT-1WR2		24	42	4	250				80
B1203XT-1WR2		3.3	303	30	111				75
B1205XT-1WR2		5	200	20	104	15	15	220	80
B1209XT-1WR2	12 (10.8-13.2)	9	111	12	104				80
B1212XT-1WR2	(10.0 10.2)	12	84	9	103				81
B1215XT-1WR2		15	67	7	103				81
B1515XT-1WR2	15 (13.5-16.5)	15	67	7	82	10			81
B2405XT-1WR2	24 (21.6-26.4)	5	200	20	52	7			80
B2409XT-1WR2		9	110	11	52				80
B2415XT-1WR2		15	67	7	51				81
B2424XT-1WR2		24	42	4	51				81

INPUT SPECIFICATIONS						
Item	Test Conditions	Min.	Тур.	Max.	Unit	
Input Surge Voltage (1 sec. max.)	3.3VDC Input	-0.7		5		
	5VDC Input	-0.7		9	VDC	
	12VDC Input	-0.7		18		
,	15VDC Input	-0.7		21		
	24VDC Input	-0.7		30		
Input Filter		Capacitance Filter				

Item	Test Conditions	Test Conditions			Max.	Unit
Output Voltage Accuracy				See tolerance envelope curve		
Line Regulation	For Vin change of±1%	3.3V output			±1.5	
Line Regulation	1 of vill change of 1 76	Others			±1.2	
		3.3V output		18		
		5V output		12		%
	400/ 4 4000/ 1	9V output		8		
Load Regulation	10% to 100% load	12V output		7		
		15V output		6		
		24V output		5		
Temperature Drift	100% load	100% load			±0.03	%/°C
Disale 0 Neiss*	COMMUNIC Description	Output Voltage ≤12V		30		>/
Ripple & Noise*	20MHz Bandwidth	Output Voltage:15V, 24V		60		mVp-p
Short Circuit Protection				Continuous, au	tomatic recove	ry

COMMON SPECI	FICATIONS				
Item	Test Conditions	Min.	Тур.	Max.	Unit
Isolation Voltage	Input-Output, tested for 1 minute and leakage current less than 1 mA	1500		-	VDC
Isolation Resistance	Input-Output, test at 500VDC	1000			МΩ
Isolation Capacitance	Inpu-Output,100KHz/0.1V		20		pF
Switching Frequency	Full load, nominal input		100	300	KHz
MTBF	MIL-HDBK-217F@25℃	3500			K hours
Case Material		7	Epoxy Res	in (UL94-V0)	

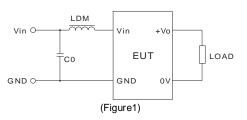
ENVIRONMENTAL SPECIFICATIONS						
Item	Test Conditions	Min.	Тур.	Max.	Unit	
Storage Humidity	Non condensing			95	%	
Operating Temperature	Power derating (above100℃, see Figure 2)	-40		105		
Storage Temperature		-55		125	°C	
Temp. rise at full load	Ta=25°C		25			
Lead Temperature	1.5mm from case for 10 seconds			300		
Cooling Free air convection						

EMC SPECIFICATIONS						
ЕМІ	CE	CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure1)				
	RE	CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure1)				
EMS	ESD	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B				

## **EMI RECOMMENDED CIRCUIT**

Weight

EMI Typical Recommended Circuit(CLASS B):



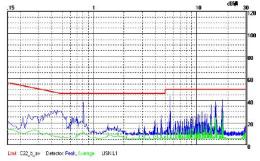
Recommended external circuit parameters:

	Vin(V)	3.3/5/12/15/24
EMI	C0	4.7µF /50V
LIVII	LDM	6.8µH

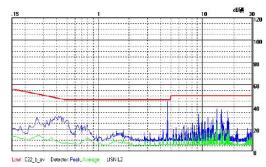
1.5

g

## **EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1)**

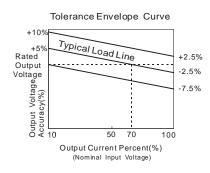


B0505XT-1WR2 CE(Class B, Positive line)

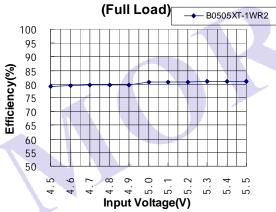


B0505XT-1WR2 CE(Class B, Negative line)

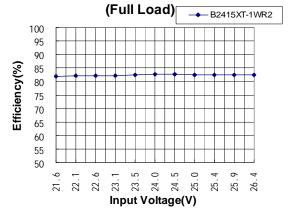
## PRODUCT TYPICAL CURVE

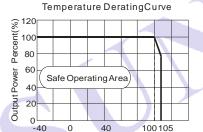


Efficiency VS Input Voltage curve



**Efficiency VS Input Voltage curve** 



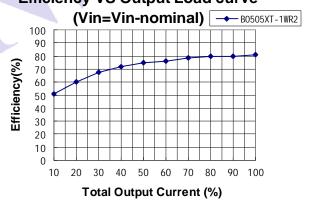


(Figure 2) **Efficiency VS Output Load curve** 

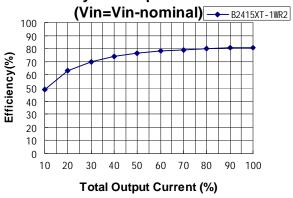
40

Environment Temp.(C)

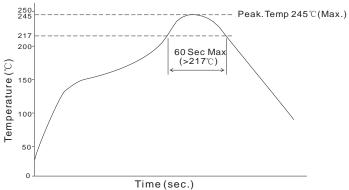
100 105



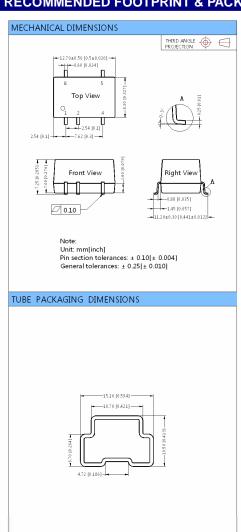
**Efficiency VS Output Load curve** 

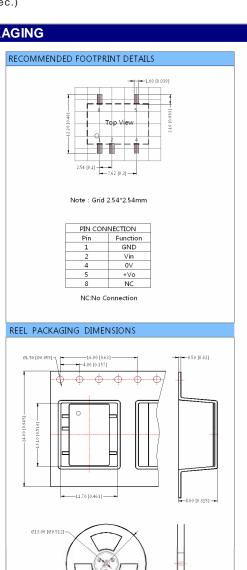


Recommended reflow soldering profile refer to IPC/JEDEC J-STD-020D standard, our products recommend reflow soldering profile as follows:



## DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING





Note:

Quantity:4000pcs

Note:
Unit: mm[inch]
General tolerances:±0.50[±0.020]
Per reel of packing quantity:500pcs
Inner carton:L"W"H=365\*350\*105
Quantity:2000pcs
Outer carton:L"W"H=390\*360\*245

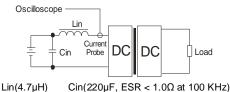
Unit: mm[inch]
General tolerances:±0.50mm[±0.020inch]
L=530[20.866inch] Quantity:40pcs
L=220[8.661] Quantity:15pcs
Inner carton(s):L'W'H=555\*170\*80
Outer carton(s):L'W'H=580\*200\*210
Outer carton(L):L'W'H=580\*200\*210
Outer carton(L): L'W'H=600\*215\*220.2 inner cartons(L)
Outer carton(L): L'W'H=600\*215\*325.3 inner cartons(L)

Unit: mm[inch]

## **TEST CONFIGURATIONS**

#### Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



## **DESIGN CONSIDERATIONS**

#### 1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load could not be less than 10% of the full load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load.

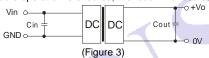
#### 2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to add a circuit breaker to the circuit.

#### 3) Recommended circuit

If you want to further decrease the input/output ripple, an capacitor filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 3).

It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).



#### **EXTERNAL CAPACITOR TABLE (Table 1)**

	0711710114		(100.0.1)
Vin	Cin	Vo	Cout
(VDC)	(µF)	(VDC)	(µF)
3.3	4.7	3.3	10
5	4.7	5	10
12	2.2	9	4.7
15	2.2	12	2.2
24	1	15	1
-		24	0.47

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

- 4) The input and the output of the product are recommended to be connected to ceramic capacitor or electrolytic capacitor. Using tantalum capacitor may cause risk of failure
- 5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

#### Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
- 2. Max. Capacitive Load is tested at nominal input voltage and full load.
- 3. Unless otherwise noted, All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load.
- 4. In this datasheet, all test methods are based on our corporate standards.
- 5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
- 6. Please contact our technical support for any specific requirement.
- 7. Specifications of this product are subject to changes without prior notice.

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